

# TEST DATA OF WMA75F-48

Regulated DC Power Supply  
November 9, 2020

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Design Manager

Prepared by : Takeshi Nakano  
Design Engineer

**COSEL CO.,LTD.**



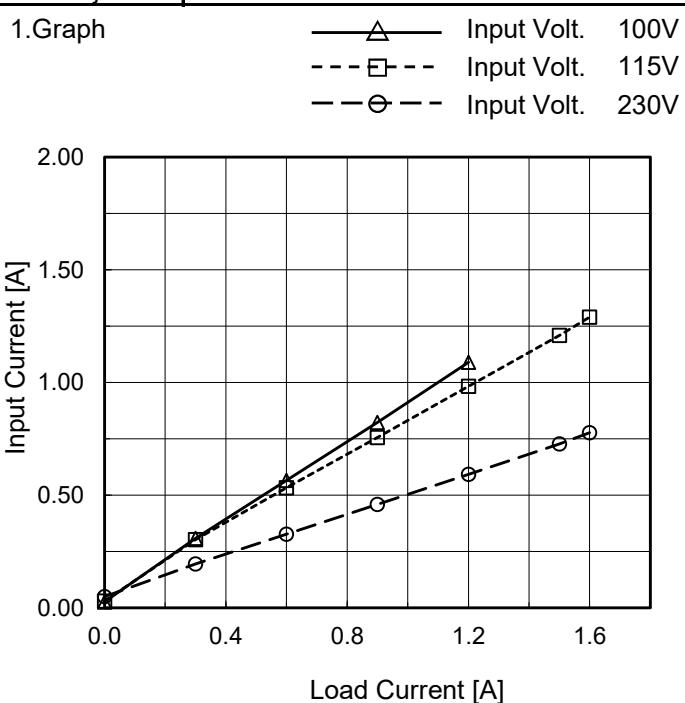
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Model	WMA75F-48
Item	Input Current (by Load Current)
Object	_____

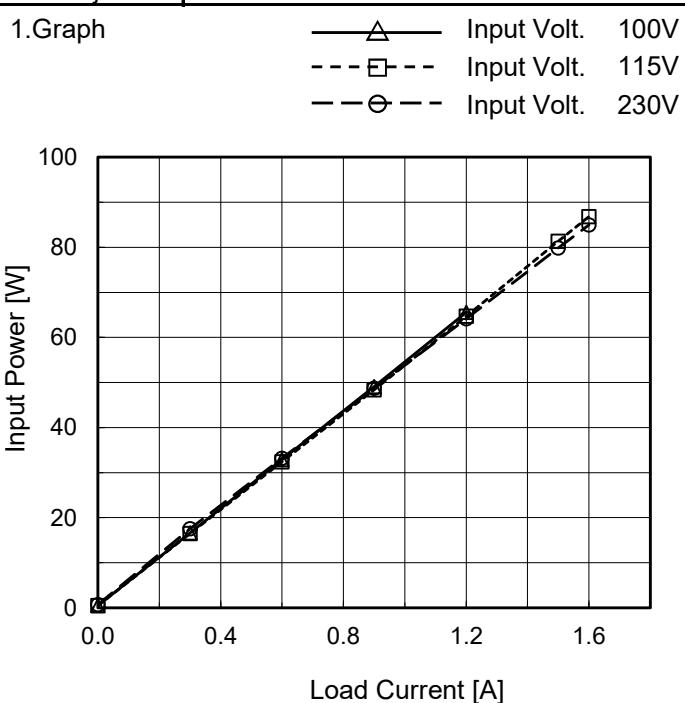

 Temperature 25°C  
 Testing Circuitry Figure A

## 2.Values

Load Current [A]	Input Current [A]		
	Input Volt. 100[V]	Input Volt. 115[V]	Input Volt. 230[V]
0.0	0.026	0.029	0.049
0.3	0.308	0.303	0.195
0.6	0.566	0.532	0.327
0.9	0.822	0.756	0.459
1.2	1.089	0.983	0.592
1.5	-	1.210	0.728
1.6	-	1.290	0.776
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-

**COSEL**

Model	WMA75F-48
Item	Input Power (by Load Current)
Object	_____


 Temperature 25°C  
 Testing Circuitry Figure A

## 2.Values

Load Current [A]	Input Power [W]		
	Input Volt. 100[V]	Input Volt. 115[V]	Input Volt. 230[V]
0.0	0.46	0.48	0.73
0.3	16.62	16.46	17.52
0.6	32.87	32.42	33.14
0.9	49.08	48.37	48.70
1.2	65.59	64.73	64.14
1.5	-	81.31	79.84
1.6	-	86.80	84.98
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-

**COSEL**

Model	WMA75F-48																																	
Item	Efficiency (by Input Voltage)	Temperature 25°C Testing Circuitry Figure A																																
Object	—	—																																
1.Graph																																		
<p>Efficiency [%]</p> <p>Input Voltage [V]</p> <p>Legend:</p> <ul style="list-style-type: none"> <li>Load 50% (Dashed line with squares)</li> <li>Load 100% (Solid line with triangles)</li> </ul> <table border="1"> <thead> <tr> <th>Input Voltage [V]</th> <th>Efficiency Load 50% [%]</th> <th>Efficiency Load 100% [%]</th> </tr> </thead> <tbody> <tr><td>85</td><td>87.5</td><td>-</td></tr> <tr><td>100</td><td>88.6</td><td>-</td></tr> <tr><td>115</td><td>89.2</td><td>88.7</td></tr> <tr><td>200</td><td>89.0</td><td>90.4</td></tr> <tr><td>230</td><td>88.4</td><td>90.3</td></tr> <tr><td>240</td><td>88.1</td><td>90.2</td></tr> <tr><td>264</td><td>87.3</td><td>89.8</td></tr> <tr><td>--</td><td>-</td><td>-</td></tr> <tr><td>--</td><td>-</td><td>-</td></tr> </tbody> </table>			Input Voltage [V]	Efficiency Load 50% [%]	Efficiency Load 100% [%]	85	87.5	-	100	88.6	-	115	89.2	88.7	200	89.0	90.4	230	88.4	90.3	240	88.1	90.2	264	87.3	89.8	--	-	-	--	-	-		
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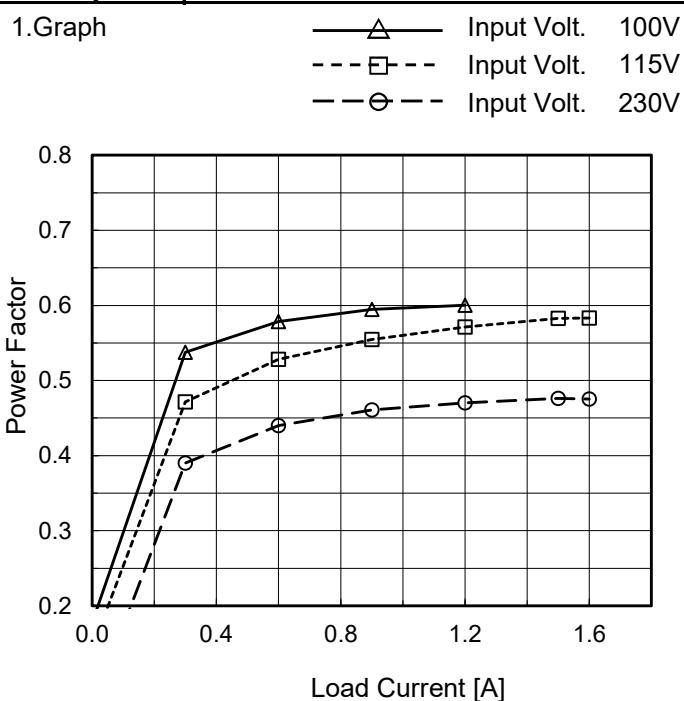
Model	WMA75F-48																																																					
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**COSEL**

Model	WMA75F-48																															
Item	Power Factor (by Input Voltage)	Temperature 25°C Testing Circuitry Figure A																														
Object	_____	_____																														
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Input Voltage [V]	Load 50% Power Factor	Load 100% Power Factor																														
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--	-	-																														
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2.Values																																

# COSEL

Model	WMA75F-48
Item	Power Factor (by Load Current)
Object	_____



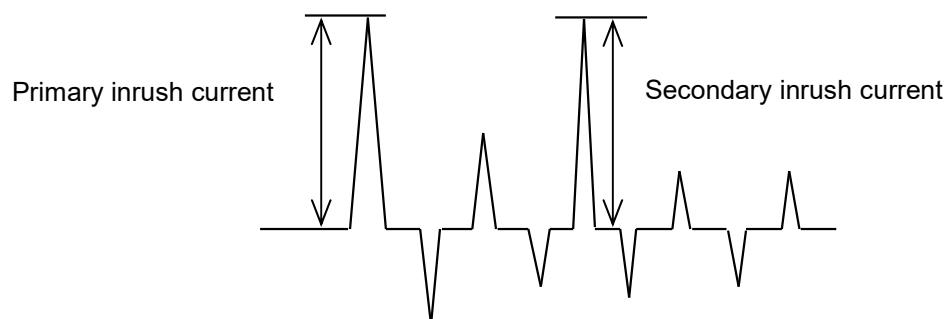
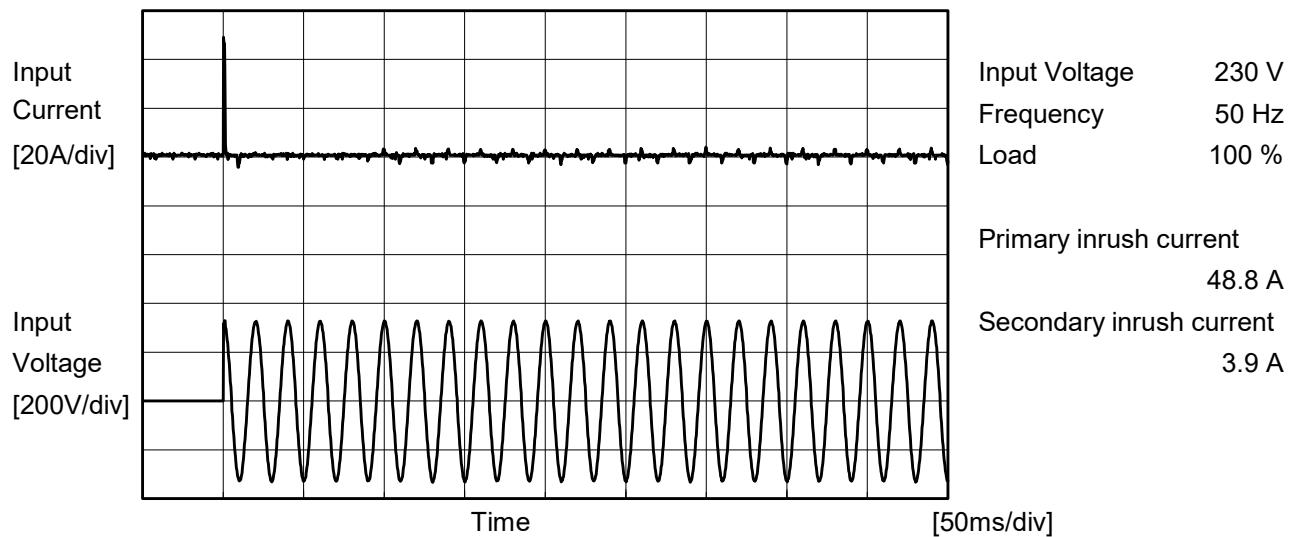
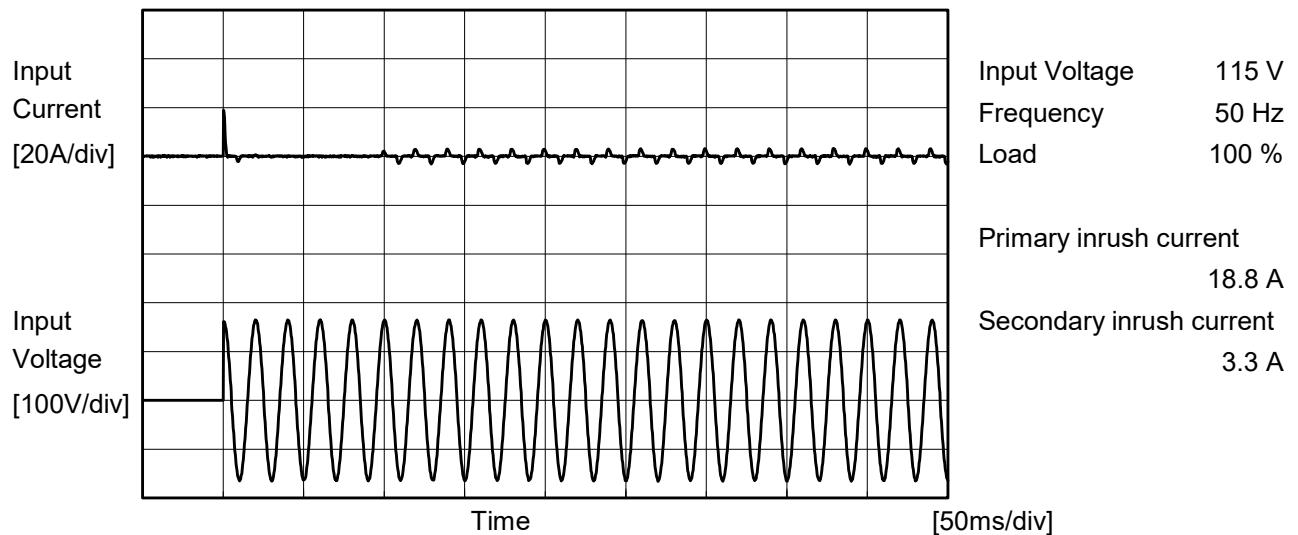
Temperature 25°C  
Testing Circuitry Figure A

## 2.Values

Load Current [A]	Power Factor		
	Input Volt. 100[V]	Input Volt. 115[V]	Input Volt. 230[V]
0.0	0.177	0.144	0.065
0.3	0.537	0.472	0.390
0.6	0.579	0.528	0.440
0.9	0.595	0.555	0.461
1.2	0.600	0.571	0.470
1.5	-	0.583	0.476
1.6	-	0.583	0.475
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-

**COSEL**

Model	WMA75F-48	Temperature	25°C
Item	Inrush Current	Testing Circuitry	Figure A
Object	_____		





Model	WMA75F-48	Temperature	25°C
Item	Leakage Current	Testing Circuitry	Figure B
Object	<hr/>		

## 1. Results

[mA]

Standards	Measuring Method	Input Volt.			Note
		100 [V]	115 [V]	230 [V]	
IEC60601-1	Both phases	0.14	0.16	0.35	Operation
	One of phases	0.22	0.25	0.57	Stand by

The value for "One of phases" is the reference value only.

## 2. Condition

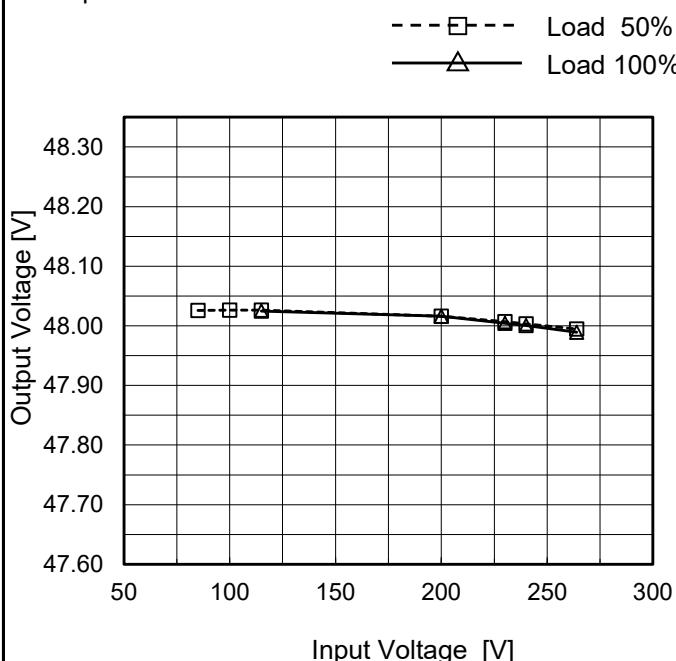
Leakage current value is concluded after measuring both phases of AC input and by choosing the larger one.

**COSEL**

Model	WMA75F-48
Item	Line Regulation
Object	+48V1.6A

 Temperature 25°C  
 Testing Circuitry Figure A

## 1.Graph



## 2.Values

Input Voltage [V]	Output Voltage [V]	
	Load 50%	Load 100%
85	48.026	-
100	48.026	-
115	48.027	48.025
200	48.016	48.016
230	48.007	48.004
240	48.004	48.000
264	47.995	47.989
--	-	-
--	-	-

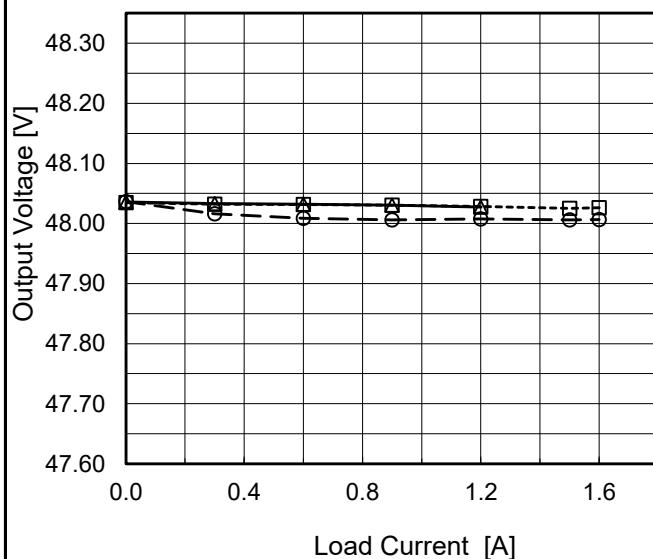
# COSEL

Model	WMA75F-48
Item	Load Regulation
Object	+48V1.6A

Temperature 25°C  
Testing Circuitry Figure A

## 1.Graph

—△— Input Volt. 100V  
- - □ - - Input Volt. 115V  
- - Θ - - Input Volt. 230V



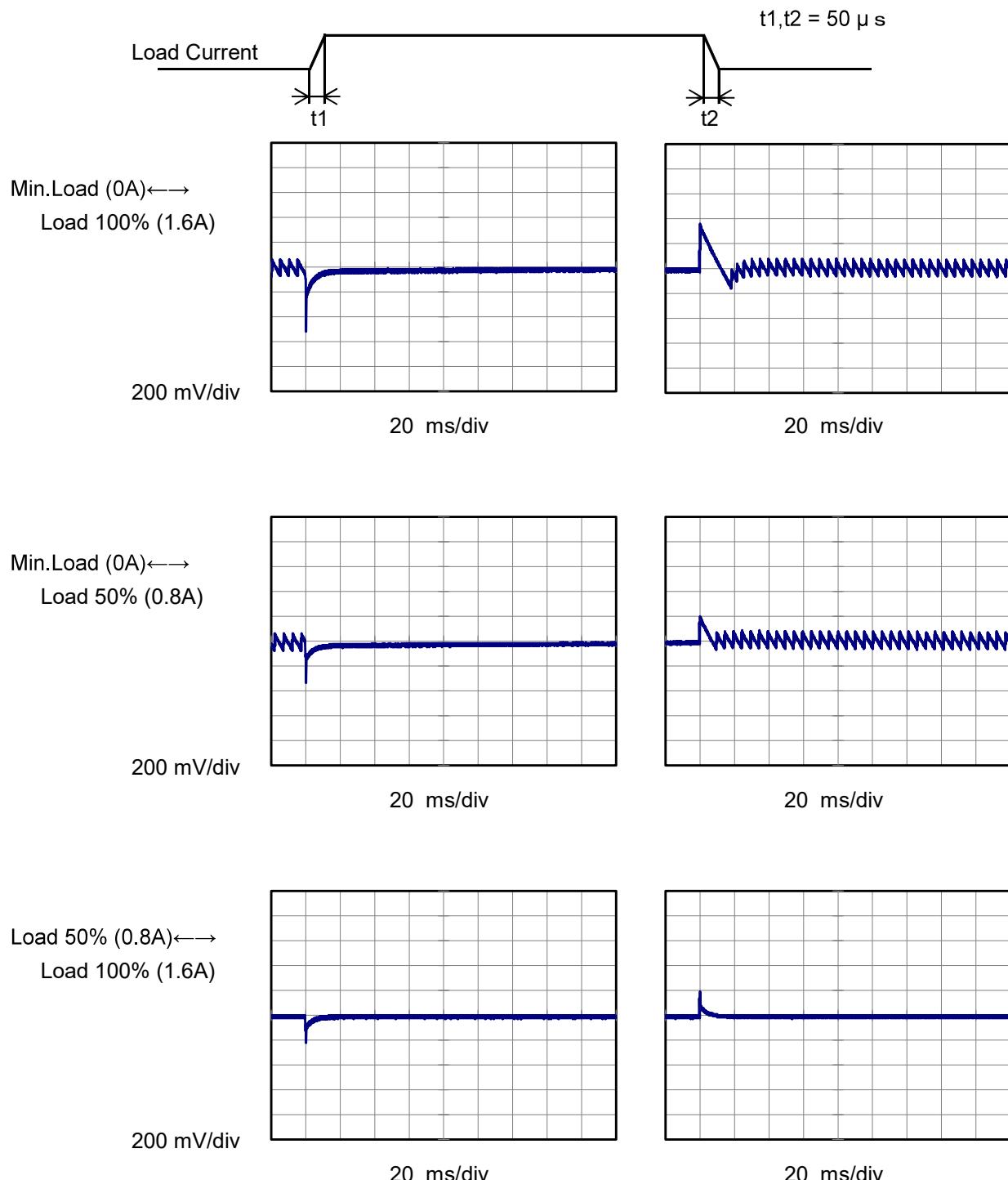
## 2.Values

Load Current [A]	Output Voltage [V]		
	Input Volt. 100[V]	Input Volt. 115[V]	Input Volt. 230[V]
0.0	48.036	48.035	48.036
0.3	48.033	48.032	48.016
0.6	48.032	48.032	48.009
0.9	48.031	48.030	48.006
1.2	48.027	48.028	48.008
1.5	-	48.025	48.006
1.6	-	48.026	48.007
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-

# COSEL

Model	WMA75F-48	Temperature	25°C
Item	Dynamic Load Response	Testing Circuitry	Figure A
Object	+48V1.6A		

Input Volt. 230 V  
 Cycle 1000 ms

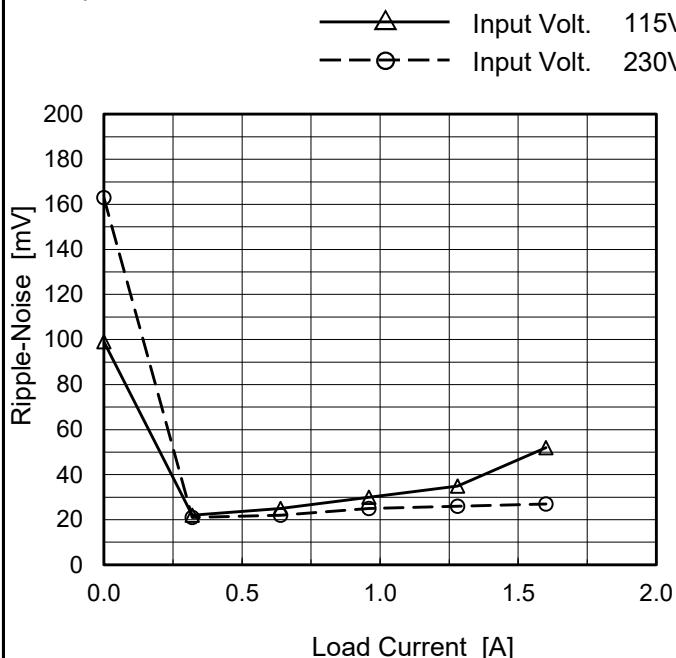


# COSEL

Model	WMA75F-48
Item	Ripple-Noise (by Load Current)
Object	+48V1.6A

Temperature 25°C  
Testing Circuitry Figure C

## 1.Graph



## 2.Values

Load Current [A]	Ripple-Noise [mV]	
	Input Volt. 115 [V]	Input Volt. 230 [V]
0.00	99	163
0.32	22	21
0.64	25	22
0.96	30	25
1.28	35	26
1.60	52	27
--	-	-
--	-	-
--	-	-
--	-	-
--	-	-

Measured by 20 MHz Oscilloscope.  
Ripple-Noise is shown as p-p in the figure below.

T1: Due to AC Input Line  
T2: Due to Switching

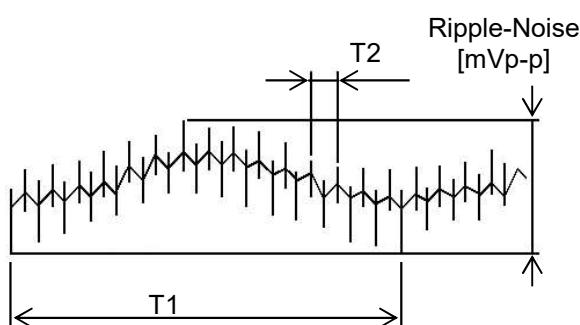


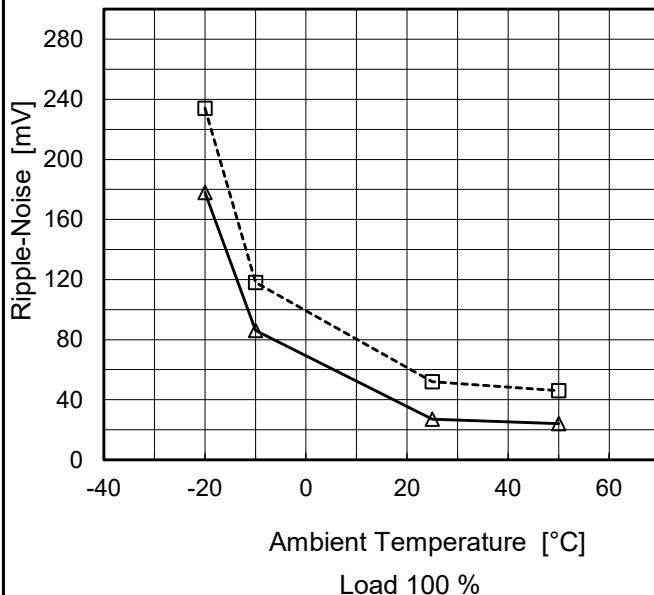
Fig. Complex Ripple Wave Form

**COSEL**

Model	WMA75F-48
Item	Ripple-Noise (by Ambient Temp.)
Object	+48V1.6A

## 1.Graph

- - - □ - - - Input Volt. 115V  
 —△— Input Volt. 230V



Measured by 20 MHz Oscilloscope.

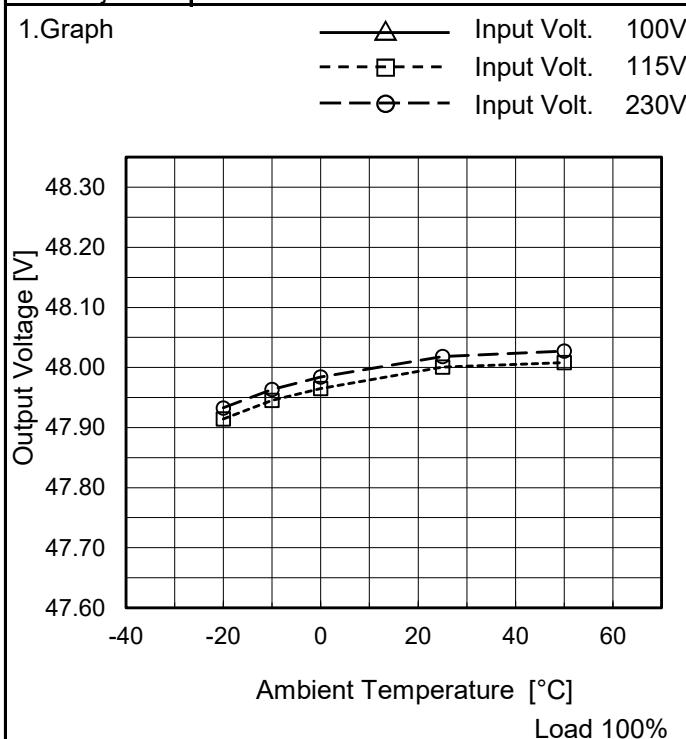
Testing Circuitry Figure C

## 2.Values

Ambient Temperature [°C]	Ripple-Noise [mV]	
	Input Volt. 115[V]	Input Volt. 230[V]
-20	234	178
-10	118	86
25	52	27
50	46	24
--	-	-
--	-	-
--	-	-
--	-	-
--	-	-
--	-	-
--	-	-
--	-	-

**COSEL**

Model	WMA75F-48
Item	Ambient Temperature Drift
Object	+48V1.6A



Testing Circuitry Figure A

## 2.Values

Ambient Temperature [°C]	Output Voltage [V]		
	Input Volt. 100[V]	Input Volt. 115[V]	Input Volt. 230[V]
-20	-	47.914	47.933
-10	-	47.945	47.963
0	-	47.965	47.984
25	-	48.001	48.018
50	-	48.008	48.027
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-



Model	WMA75F-48	Testing Circuitry Figure A
Item	Output Voltage Accuracy	
Object	+48V1.6A	

### 1. Output Voltage Accuracy

This is defined as the value of the output voltage, regulation load, ambient temperature and input voltage varied at random in the range as specified below.

Temperature : -20 - 50°C

Input Voltage : 100 - 230V

Load Current : 0 - 1.6A

\* Output Voltage Accuracy =  $\pm(\text{Maximum of Output Voltage} - \text{Minimum of Output Voltage}) / 2$

$$\text{* Output Voltage Accuracy (Ratio)} = \frac{\text{Output Voltage Accuracy}}{\text{Rated Output Voltage}} \times 100$$

### 2. Values

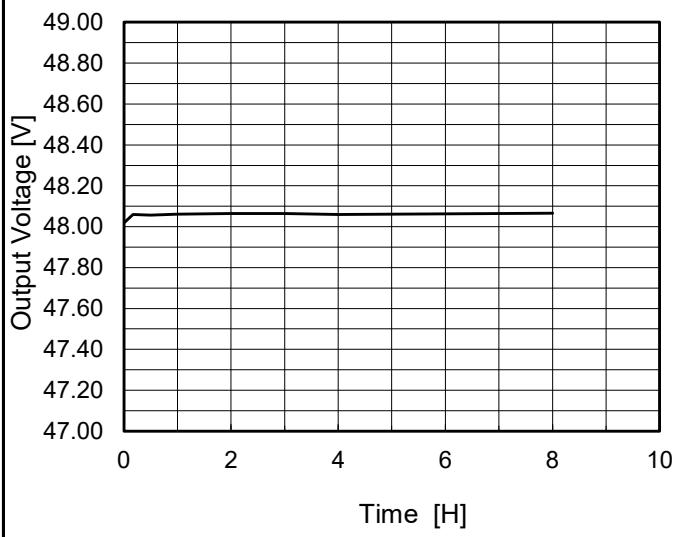
Item	Temperature [°C]	Input Voltage[V]	Output		Output Voltage Accuracy	
			Current[A]	Voltage[V]	Value [mV]	Ratio [%]
Maximum Voltage	50	100	0	48.038	±53	±0.1
Minimum Voltage	-20	100	1.6	47.933		

**COSEL**

Model	WMA75F-48
Item	Time Lapse Drift
Object	+48V1.6A

 Temperature 25°C  
 Testing Circuitry Figure A

## 1.Graph


 Input Volt. 115V  
 Load 100%

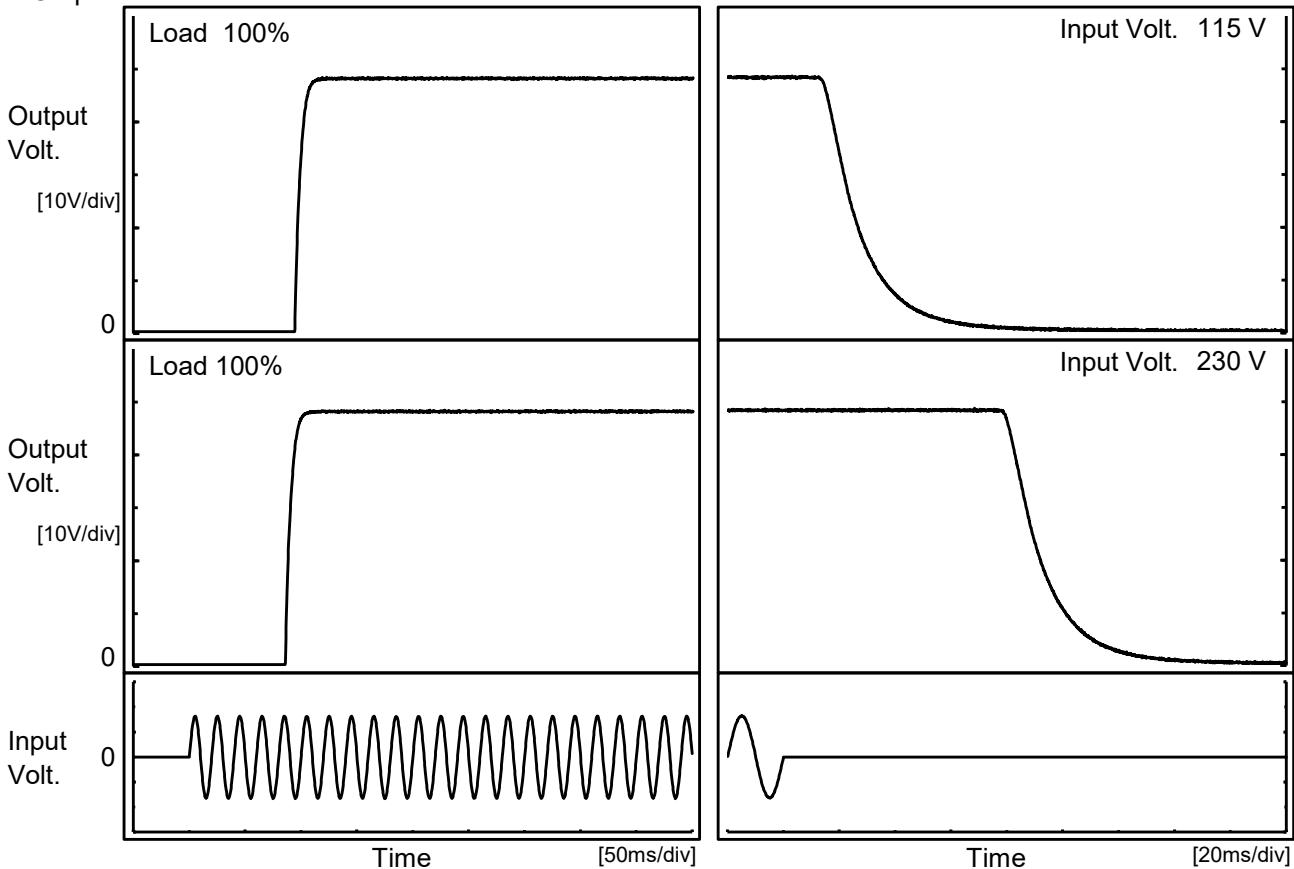
## 2.Values

Time since start [H]	Output Voltage [V]
0.0	48.019
0.2	48.059
0.5	48.057
1.0	48.061
2.0	48.065
3.0	48.064
4.0	48.060
8.0	48.066

# COSEL

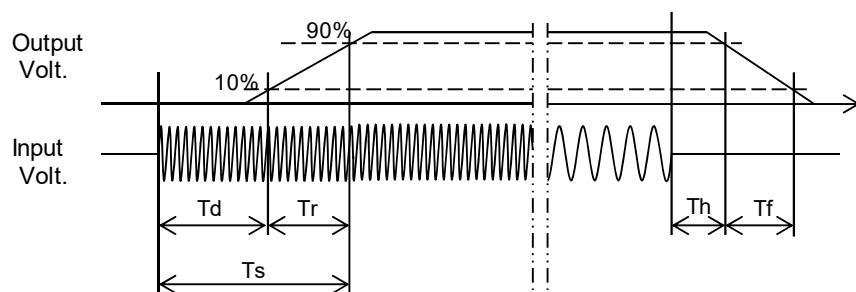
Model	WMA75F-48	Temperature	25°C
Item	Rise and Fall Time	Testing Circuitry	Figure A
Object	+48V1.6A		

## 1. Graph



## 2. Values

Input Volt	Time	Td	Tr	Ts	Th	Tf	[ms]
115		94.8	9.5	104.3	16.2	28.7	
230		86.3	9.8	96.1	81.4	28.6	

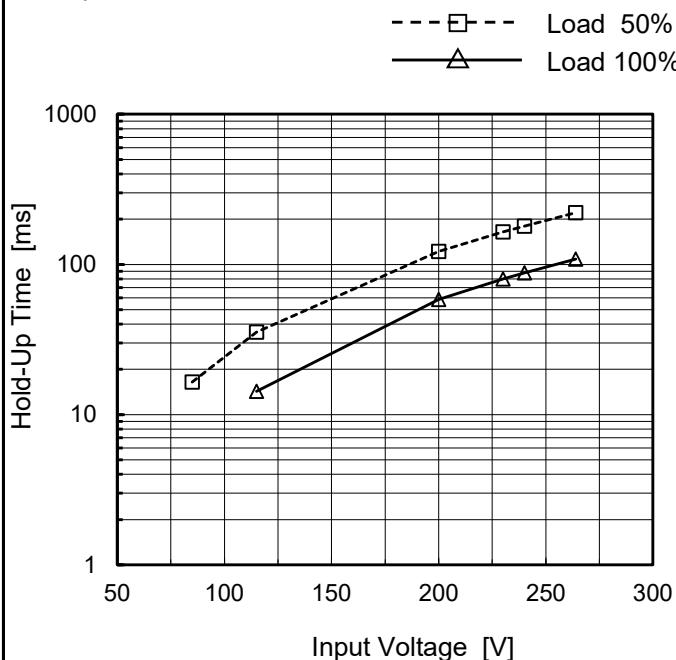


# COSEL

Model	WMA75F-48
Item	Hold-Up Time
Object	+48V1.6A

Temperature 25°C  
Testing Circuitry Figure A

## 1.Graph



## 2.Values

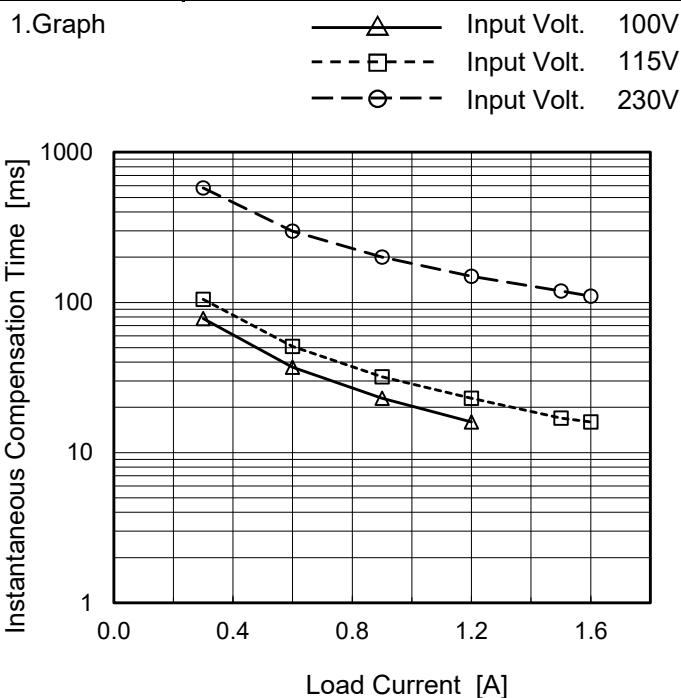
Input Voltage [V]	Hold-Up Time [ms]	
	Load 50%	Load 100%
85	17	-
100	25	-
115	35	14
200	122	58
230	164	80
240	180	88
264	221	109
--	-	-
--	-	-

This duration covers from Shut-off of input voltage to the moment when output voltage descends to the rated range of voltage accuracy.

# COSEL

Model	WMA75F-48
Item	Instantaneous Interruption Compensation
Object	+48V1.6A

Temperature 25°C  
Testing Circuitry Figure A



## 2.Values

Load Current [A]	Time [ms]		
	Input Volt. 100[V]	Input Volt. 115[V]	Input Volt. 230[V]
0.0	-	-	-
0.3	78	105	579
0.6	37	51	298
0.9	23	32	200
1.2	16	23	149
1.5	-	17	119
1.6	-	16	110
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-

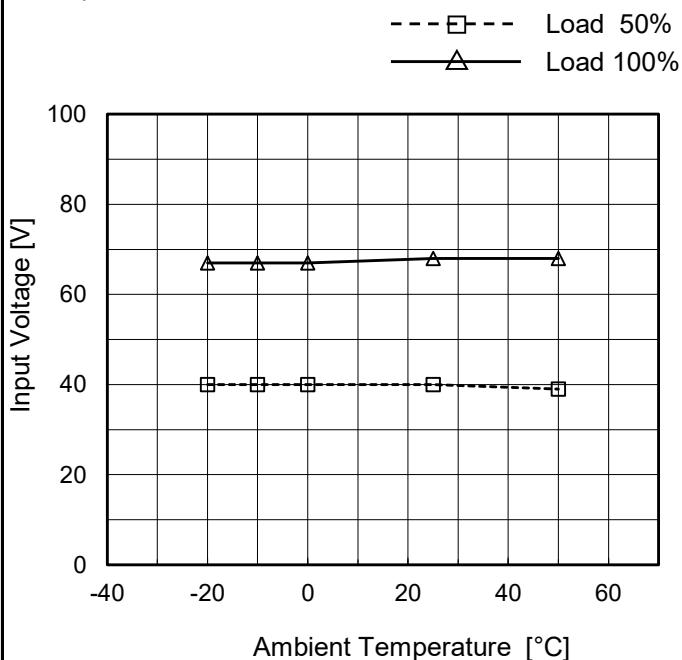
**COSEL**

Model WMA75F-48

Item Minimum Input Voltage  
for Regulated Output Voltage

Object +48V1.6A

## 1. Graph



Testing Circuitry Figure A

## 2. Values

Ambient Temperature [°C]	Input Voltage [V]	
	Load 50%	Load 100%
-20	40	67
-10	40	67
0	40	67
25	40	68
50	39	68
--	-	-
--	-	-
--	-	-
--	-	-
--	-	-
--	-	-



**COSEL**

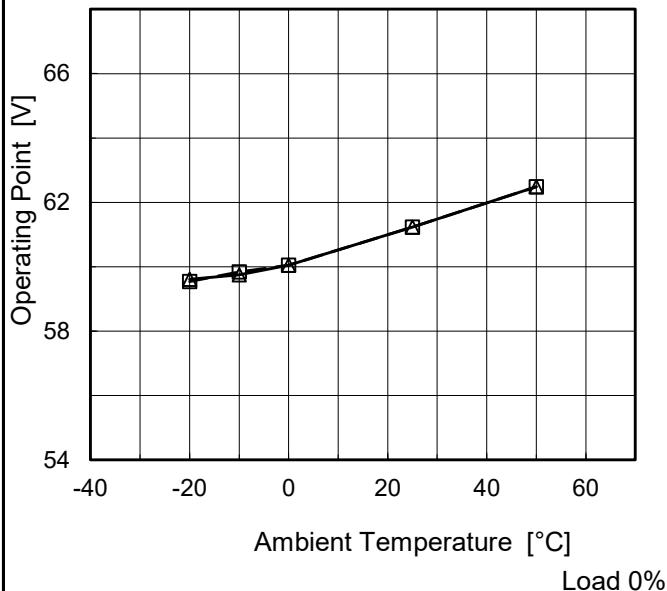
Model WMA75F-48

Item Overvoltage Protection

Object +48V1.6A

## 1. Graph

—△— Input Volt. 115V  
 - -□- - Input Volt. 230V



Testing Circuitry Figure A

## 2. Values

Ambient Temperature [°C]	Operating Point [V]	
	Input Volt. 115[V]	Input Volt. 230[V]
-20	59.61	59.54
-10	59.76	59.84
0	60.05	60.05
25	61.23	61.23
50	62.48	62.48
--	-	-
--	-	-
--	-	-
--	-	-
--	-	-
--	-	-

**COSEL**

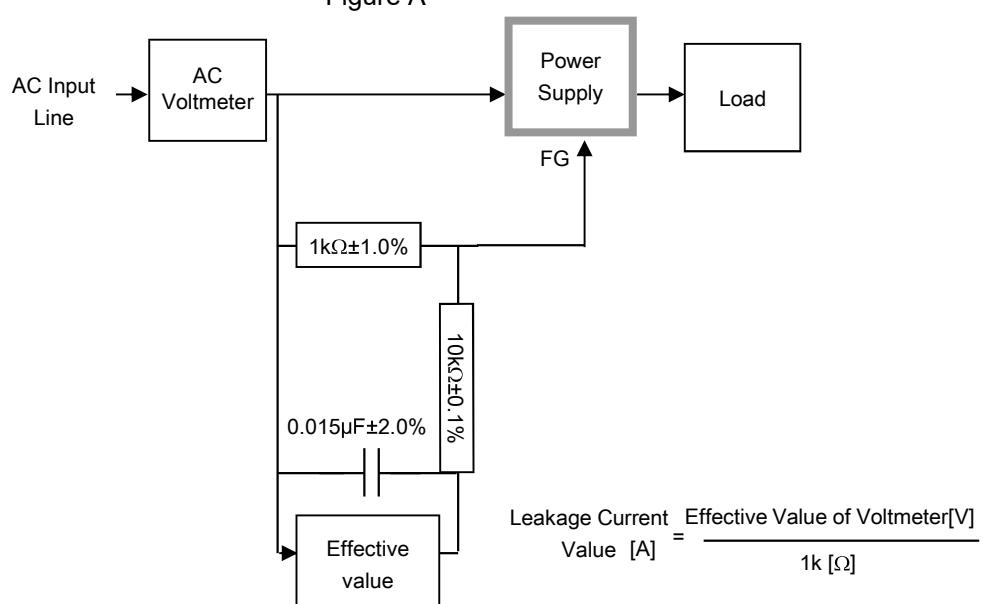
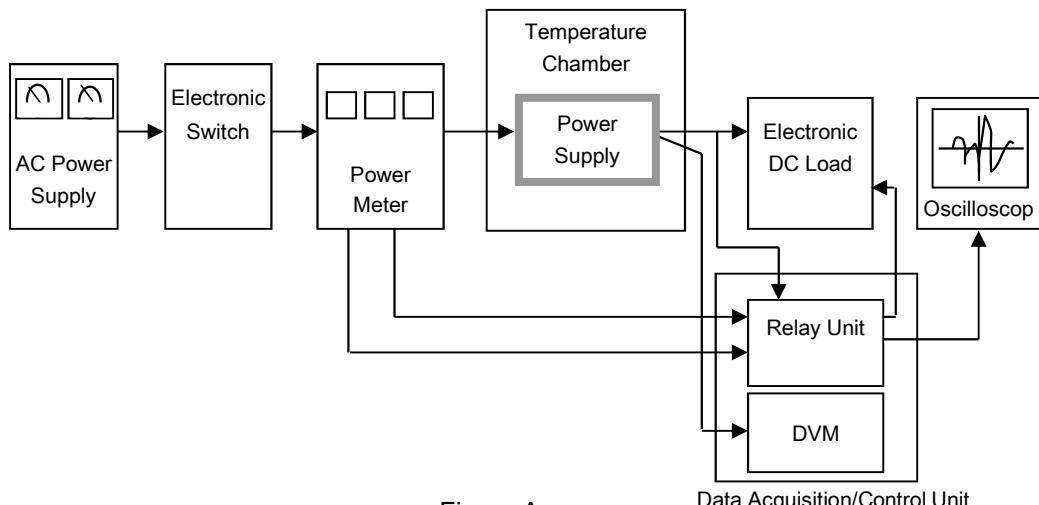
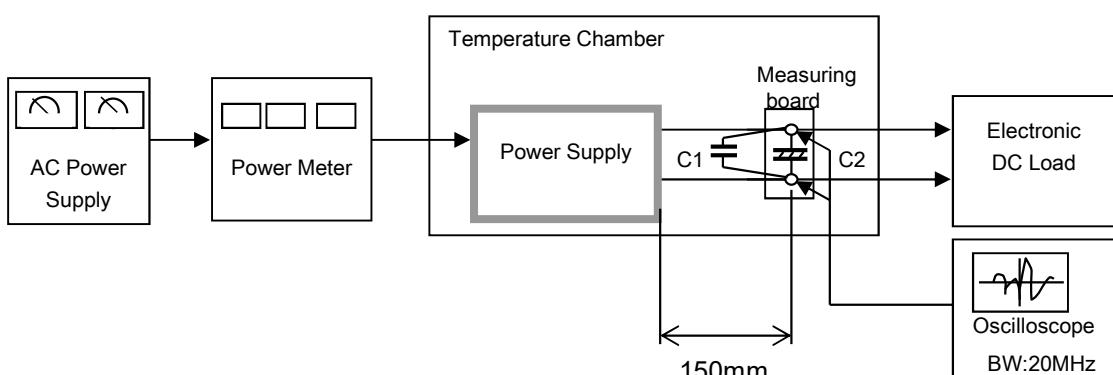


Figure B (IEC60601-1)



(Ceramic capacitor)  
C1= 0.1  $\mu$ F

(Electrolytic capacitor)  
C2= 47  $\mu$ F

Figure C