

TEST DATA OF WDA90F-48

Regulated DC Power Supply
August 17, 2022

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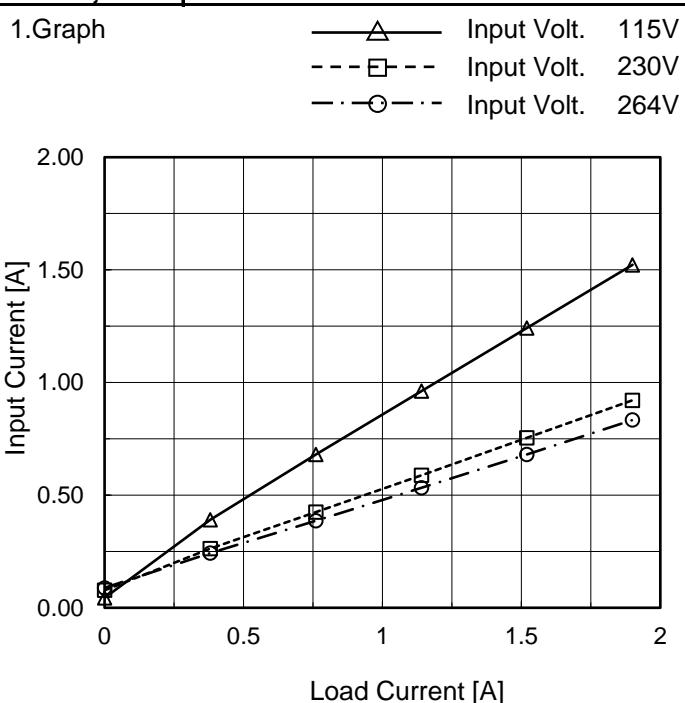
COSEL CO.,LTD.

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Model	WDA90F-48
Item	Input Current (by Load Current)
Object	+48V1.9A

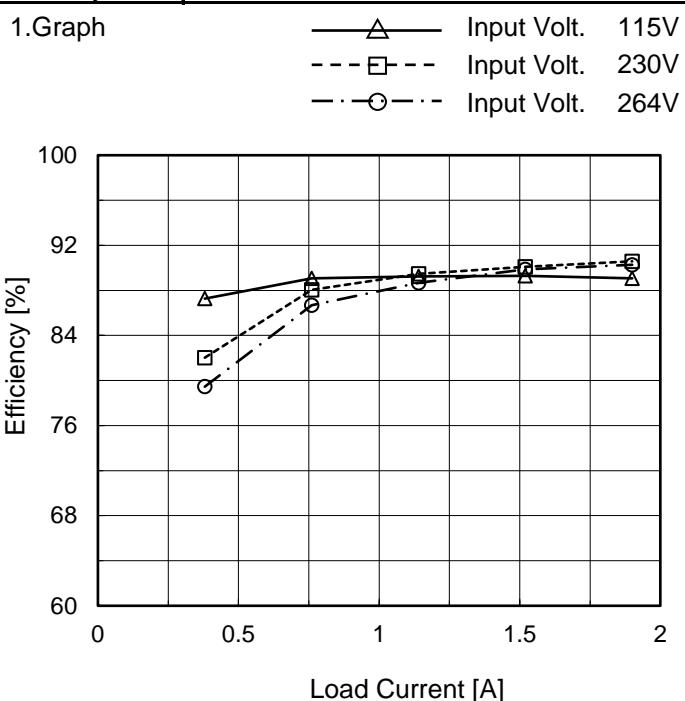


Temperature 25°C
Testing Circuitry Figure A

2.Values

Load Current [A]	Input Current [A]		
	Input Volt. 115[V]	Input Volt. 230[V]	Input Volt. 264[V]
0.00	0.044	0.078	0.088
0.38	0.391	0.262	0.243
0.76	0.680	0.425	0.386
1.14	0.961	0.588	0.532
1.52	1.241	0.754	0.680
1.90	1.522	0.921	0.833
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-

Model	WDA90F-48
Item	Efficiency (by Load Current)
Object	+48V1.9A

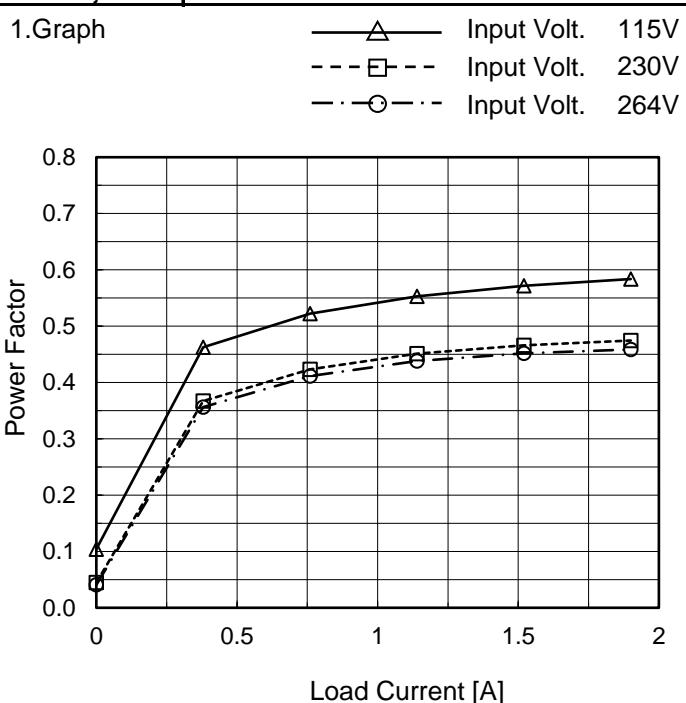


Temperature 25°C
Testing Circuitry Figure A

2.Values

Load Current [A]	Efficiency [%]		
	Input Volt. 115[V]	Input Volt. 230[V]	Input Volt. 264[V]
0.00	-	-	-
0.38	87.3	82.0	79.5
0.76	89.1	88.0	86.7
1.14	89.2	89.5	88.7
1.52	89.3	90.1	89.9
1.90	89.1	90.6	90.3
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-

Model	WDA90F-48
Item	Power Factor (by Load Current)
Object	+48V1.9A



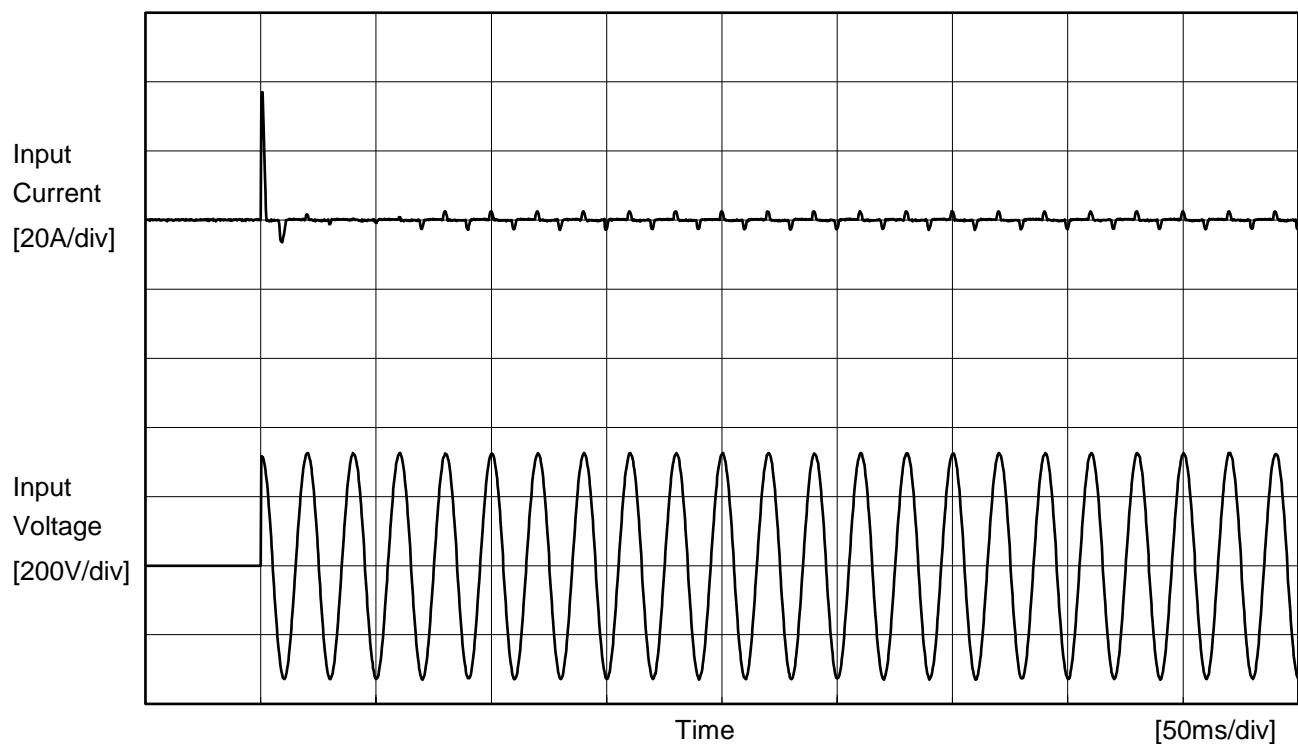
Temperature 25°C
Testing Circuitry Figure A

2.Values

Load Current [A]	Power Factor		
	Input Volt. 115[V]	Input Volt. 230[V]	Input Volt. 264[V]
0.00	0.104	0.045	0.041
0.38	0.463	0.367	0.356
0.76	0.522	0.423	0.412
1.14	0.553	0.451	0.438
1.52	0.571	0.466	0.452
1.90	0.584	0.475	0.459
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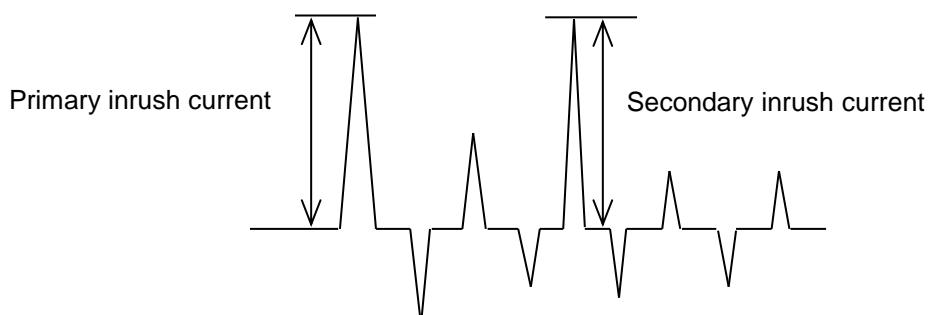
Model	WDA90F-48
Item	Inrush Current
Object	+48V1.9A

Temperature 25°C
Testing Circuitry Figure A



Input Voltage 230 V
Frequency 50 Hz
Load 100 %

Primary inrush current 36.9 A
Secondary inrush current 0.0 A



Model	WDA90F-48	Temperature	25°C
Item	Leakage Current	Testing Circuitry	Figure C
Object	+48V1.9A		

1. Results

[mA]

Standards	Testing Circuitry	Measuring Method	Input Volt.			Note
			115 [V]	240 [V]	264 [V]	
DEN-AN	Figure C-1	Both phases	0.20	0.44	0.49	Operation
		One of phases	0.36	0.79	0.87	Stand by
IEC62368-1	Figure C-2	Both phases	0.19	0.42	0.46	Operation
		One of phases	0.35	0.76	0.84	Stand by
	Figure C-3	Both phases	0.19	0.41	0.45	Operation
		One of phases	0.34	0.74	0.82	Stand by

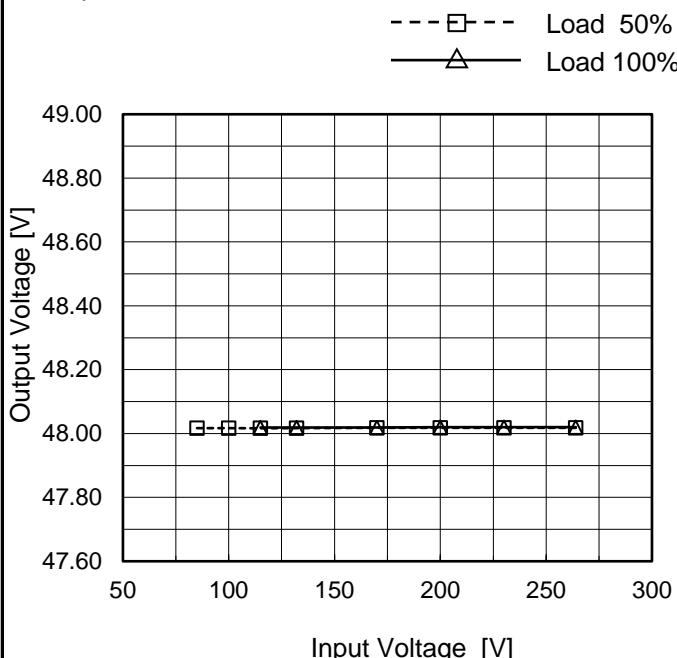
The value for "One of phases" is the reference value only.

2. Condition

Leakage current value is concluded after measuring both phases of AC input and by choosing the larger one.

Model	WDA90F-48
Item	Line Regulation
Object	+48V1.9A

Temperature 25°C
Testing Circuitry Figure A

1.Graph**2.Values**

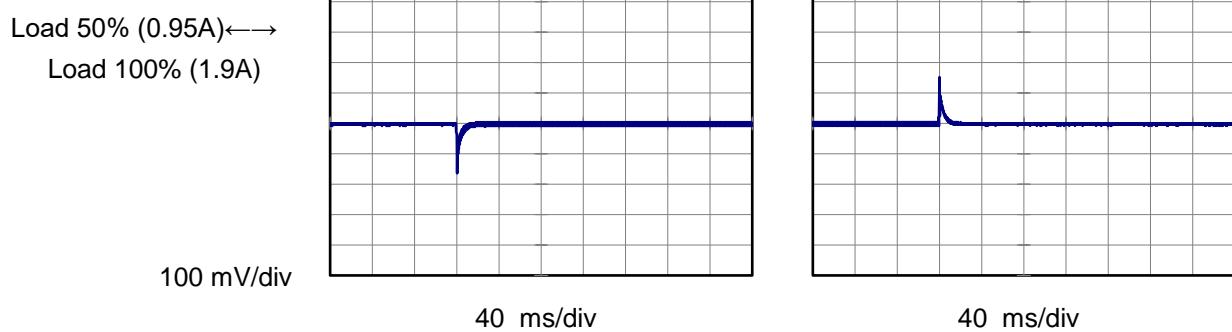
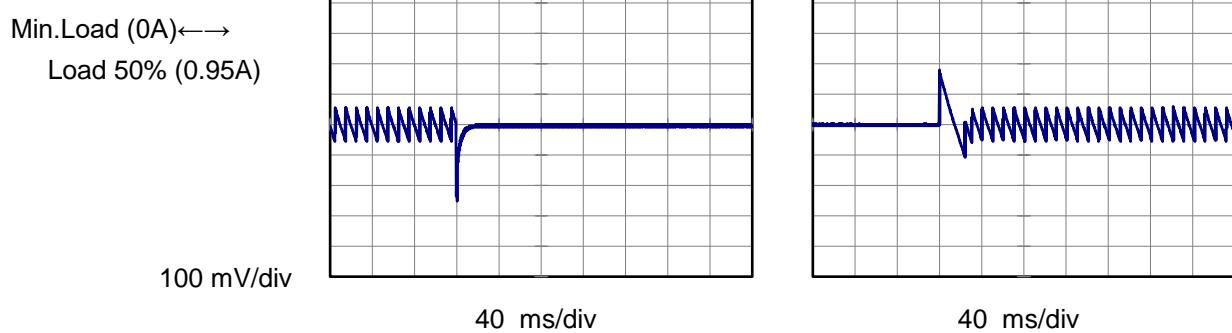
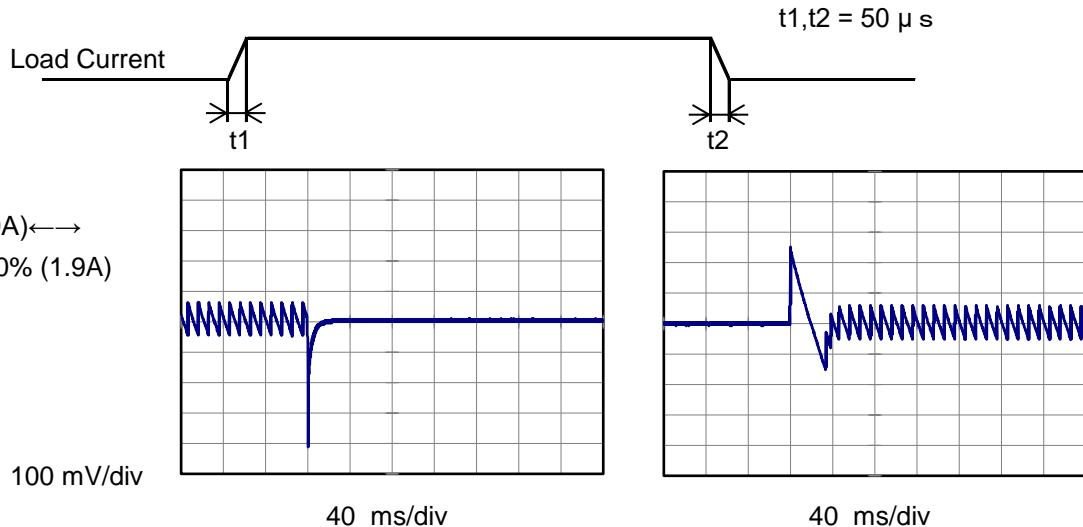
Input Voltage [V]	Output Voltage [V]	
	Load 50%	Load 100%
85	48.017	-
100	48.017	-
115	48.016	48.019
132	48.017	48.019
170	48.018	48.019
200	48.018	48.020
230	48.018	48.020
264	48.018	48.020
--	-	-

Model	WDA90F-48	Temperature	25°C																																																			
Item	Load Regulation	Testing Circuitry	Figure A																																																			
Object	+48V1.9A																																																					
1.Graph		2.Values																																																				
		<table border="1"> <thead> <tr> <th rowspan="2">Load Current [A]</th> <th colspan="3">Output Voltage [V]</th> </tr> <tr> <th>Input Volt. 115[V]</th> <th>Input Volt. 230[V]</th> <th>Input Volt. 264[V]</th> </tr> </thead> <tbody> <tr><td>0.00</td><td>48.034</td><td>48.040</td><td>48.041</td></tr> <tr><td>0.38</td><td>48.032</td><td>48.032</td><td>48.032</td></tr> <tr><td>0.76</td><td>48.031</td><td>48.031</td><td>48.030</td></tr> <tr><td>1.14</td><td>48.030</td><td>48.030</td><td>48.029</td></tr> <tr><td>1.52</td><td>48.029</td><td>48.029</td><td>48.028</td></tr> <tr><td>1.90</td><td>48.029</td><td>48.027</td><td>48.027</td></tr> <tr><td>--</td><td>--</td><td>--</td><td>--</td></tr> <tr><td>--</td><td>--</td><td>--</td><td>--</td></tr> <tr><td>--</td><td>--</td><td>--</td><td>--</td></tr> <tr><td>--</td><td>--</td><td>--</td><td>--</td></tr> <tr><td>--</td><td>--</td><td>--</td><td>--</td></tr> </tbody> </table>		Load Current [A]	Output Voltage [V]			Input Volt. 115[V]	Input Volt. 230[V]	Input Volt. 264[V]	0.00	48.034	48.040	48.041	0.38	48.032	48.032	48.032	0.76	48.031	48.031	48.030	1.14	48.030	48.030	48.029	1.52	48.029	48.029	48.028	1.90	48.029	48.027	48.027	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--
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Item		Temperature 25°C																																																				
Object		Testing Circuitry Figure B																																																				
1.Graph																																																						
Input Voltage 230V Load 100%																																																						

Model	WDA90F-48
Item	Dynamic Load Response
Object	+48V1.9A

Temperature 25°C
Testing Circuitry Figure A

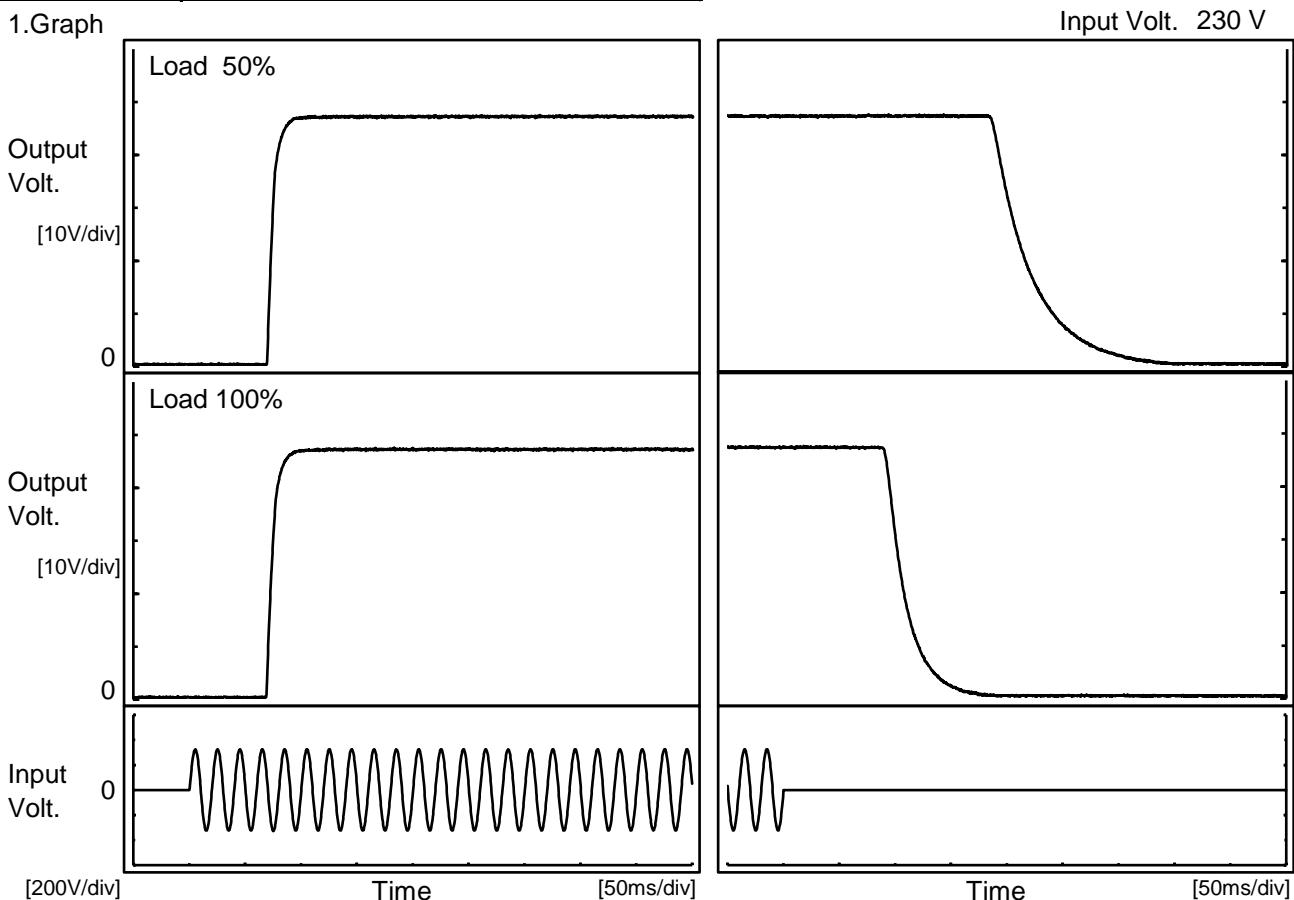
Input Volt. 230 V
Cycle 1000 ms



Model	WDA90F-48
Item	Rise and Fall Time
Object	+48V1.9A

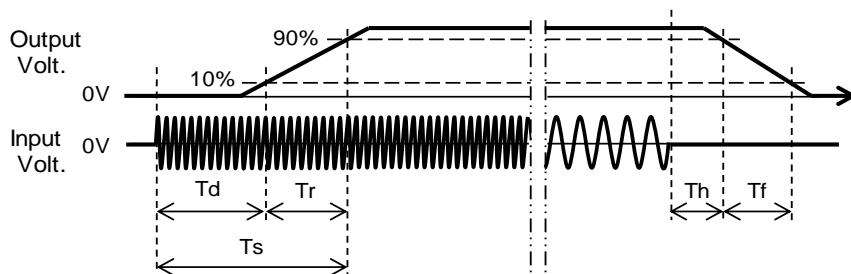
Temperature 25°C
Testing Circuitry Figure A

1. Graph



2. Values

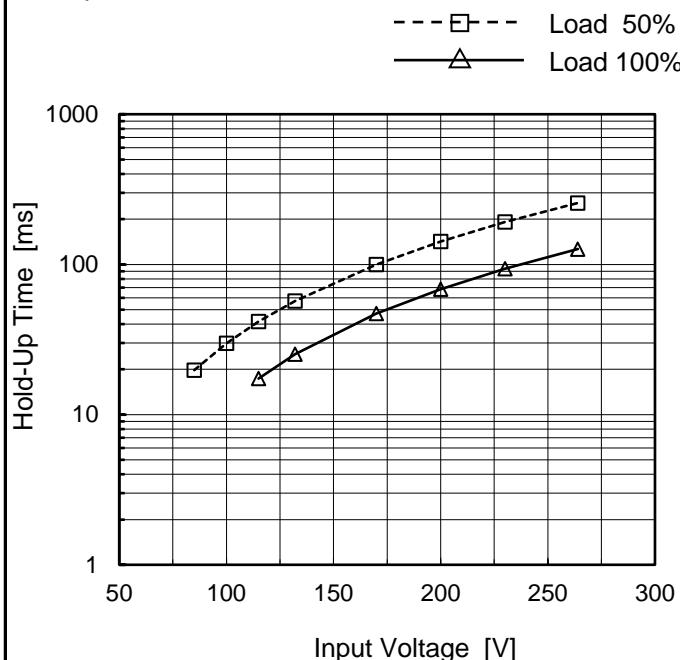
Load	Time	Td	Tr	Ts	Th	Tf
50 %		70.3	12.8	83.1	189.3	76.3
100 %		69.8	12.8	82.6	92.8	40.8



Model	WDA90F-48
Item	Hold-Up Time
Object	+48V1.9A

Temperature 25°C
Testing Circuitry Figure A

1.Graph

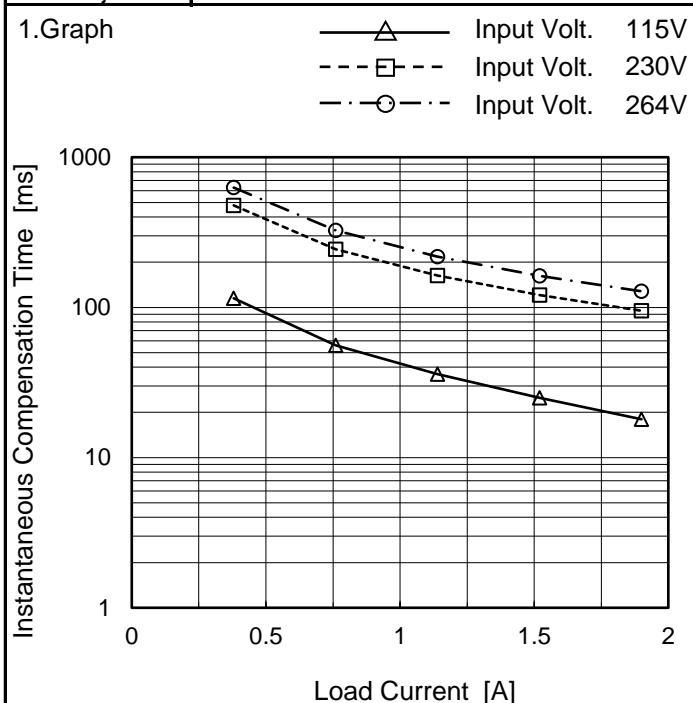


2.Values

Input Voltage [V]	Hold-Up Time [ms]	
	Load 50%	Load 100%
85	20	-
100	30	-
115	42	17
132	57	25
170	100	47
200	142	68
230	192	94
264	257	127
--	-	-

This duration covers from Shut-off of input voltage to the moment when output voltage descends to the rated range of voltage accuracy.

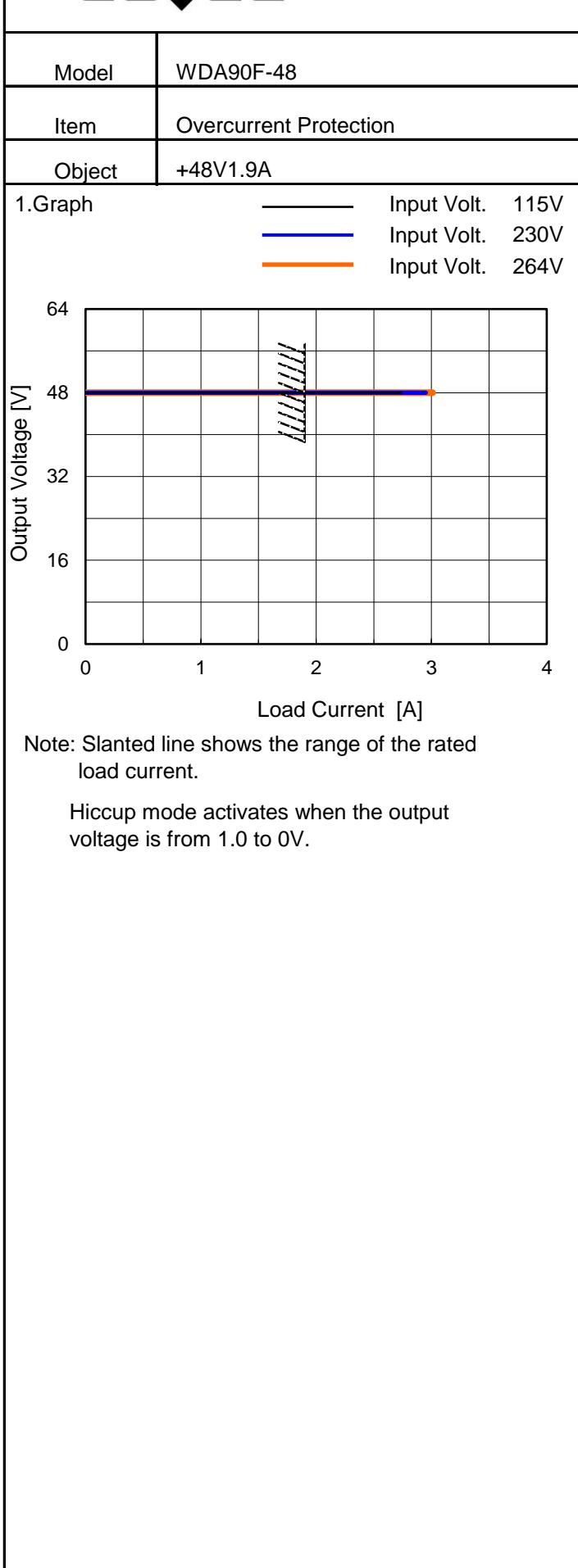
Model	WDA90F-48
Item	Instantaneous Interruption Compensation
Object	+48V1.9A



Temperature 25°C
Testing Circuitry Figure A

2.Values

Load Current [A]	Time [ms]		
	Input Volt. 115[V]	Input Volt. 230[V]	Input Volt. 264[V]
0.00	-	-	-
0.38	115	478	628
0.76	56	244	325
1.14	36	163	218
1.52	25	121	162
1.90	18	95	128
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-



Temperature 25°C
Testing Circuitry Figure A

2.Values

Output Voltage [V]	Load Current [A]		
	Input Volt. 115[V]	Input Volt. 230[V]	Input Volt. 264[V]
48	2.74	2.95	3.01
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-

Model	WDA90F-48	
Item	Ambient Temperature Drift	Testing Circuitry Figure A
Object	+48V1.9A	

1.Values

Load 100%

Ambient Temperature[°C]	Output Voltage [V]		
	Input Volt. 115V	Input Volt. 230V	Input Volt. 264V
-20	47.923	47.923	47.925
25	48.021	48.021	48.021
50	48.028	48.030	48.030

Item	Minimum Input Voltage for Regulated Output Voltage	Testing Circuitry Figure A
Object	+48V1.9A	

1.Values

Ambient Temperature[°C]	Input Voltage [V]	
	Load 50%	Load 100%
-20	41	67
25	41	66
50	41	66

Item	Overvoltage Protection	Testing Circuitry Figure A
Object	+48V1.9A	

1.Values

Load 0%

Ambient Temperature[°C]	Operating Point [V]	
	Input Volt. 115V	Input Volt. 264V
-20	59.02	59.02
25	57.84	60.71
50	61.81	61.96

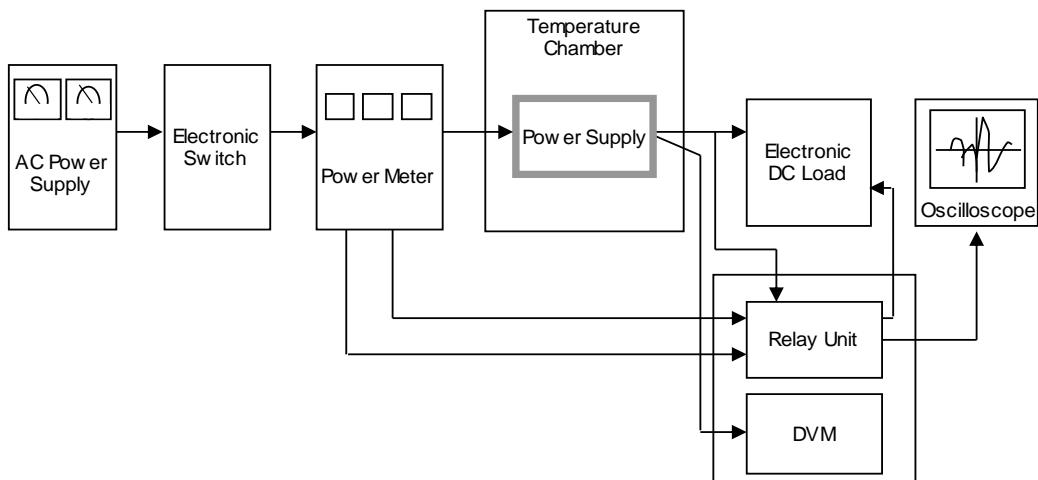


Figure A

Data Acquisition/Control Unit

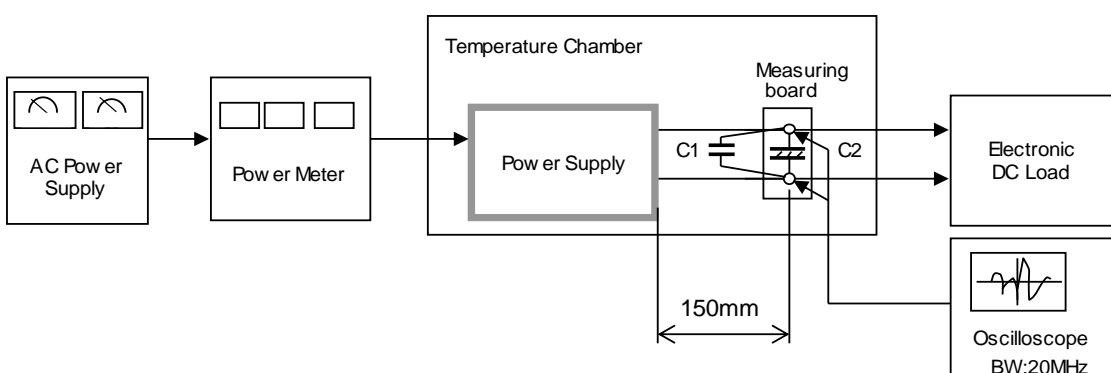


Figure B

 $C1 = 0.1 \mu F$
(Ceramic capacitor) $C2 = 47 \mu F$
(Electrolytic capacitor)

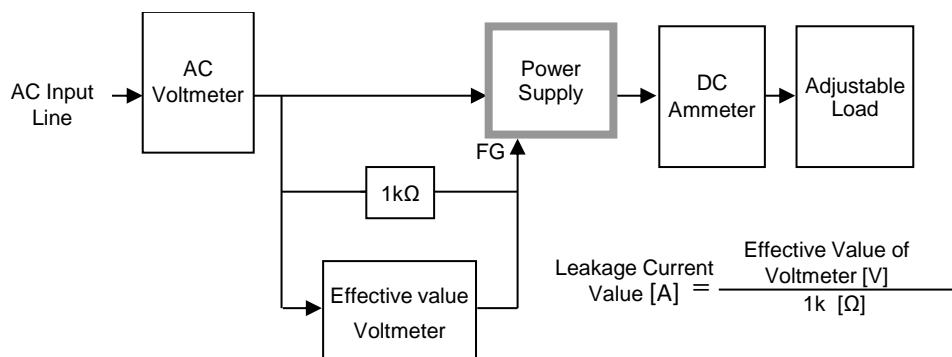


Figure C-1 (DEN-AN)

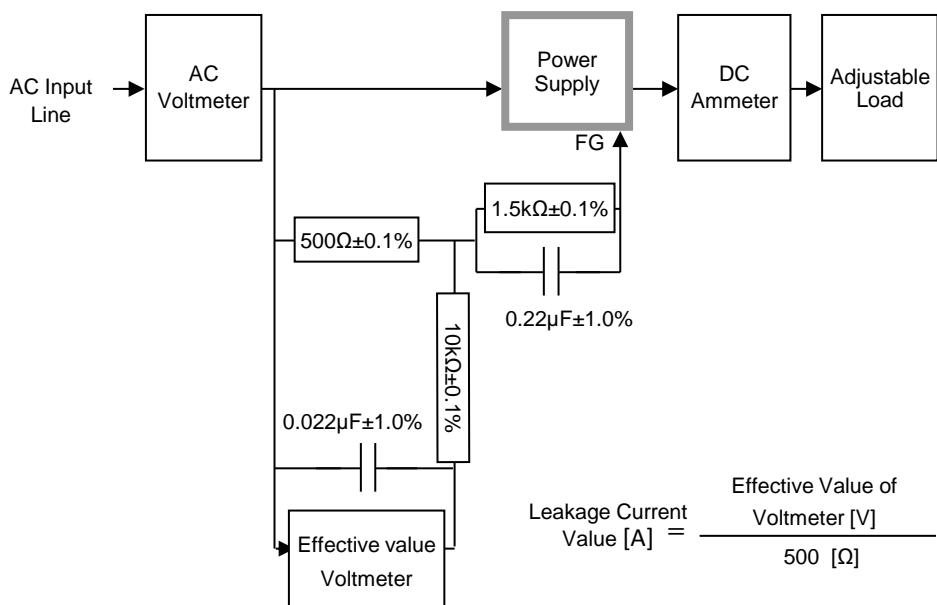


Figure C-2 (IEC62368-1 refer to IEC60990 Fig.4)

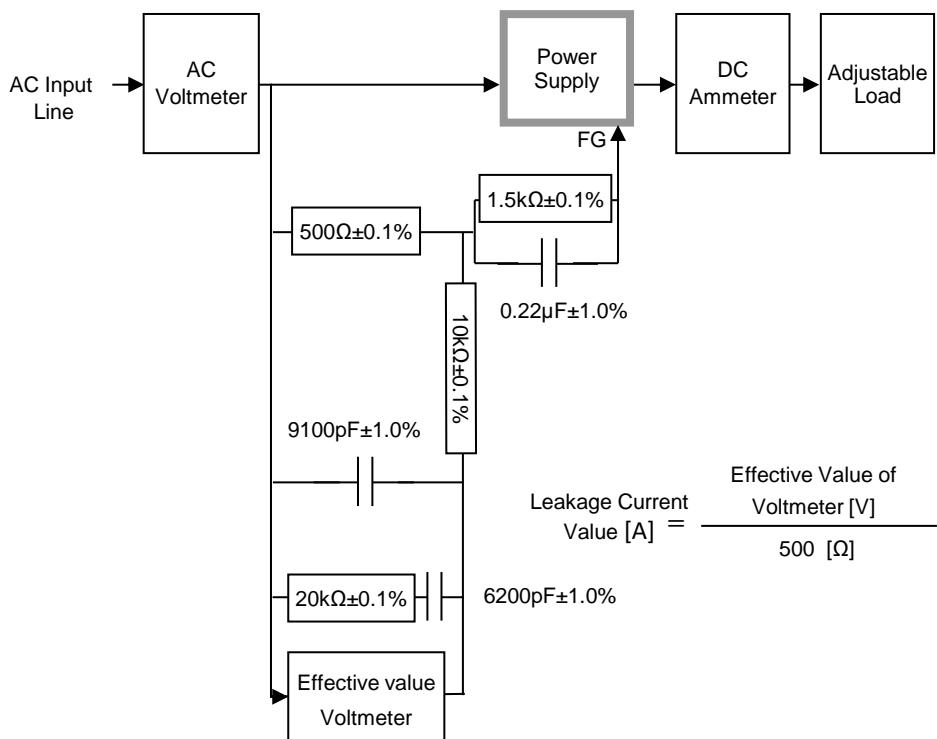


Figure C-3 (IEC62368-1 refer to IEC60990 Fig.5)