Reference

TEST DATA OF WDA30F-24

Regulated DC Power Supply
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Approved by : *Takashi Kajii*
Design Manager

Prepared by : *Jeonghoon Yi*
Design Engineer

COSEL CO.,LTD.

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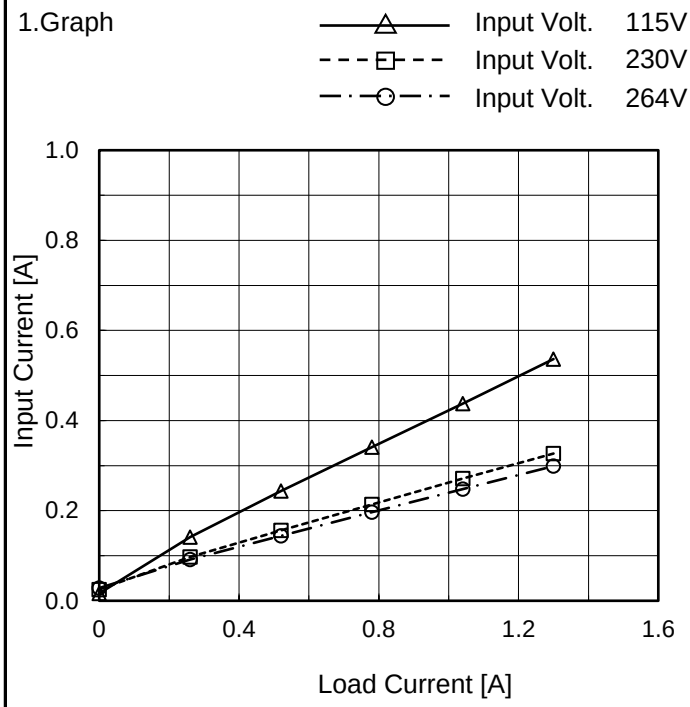
Model	WDA30F-24
Item	Input Current (by Load Current)
Object	+24V1.3A

Temperature 25°C
Testing Circuitry Figure A

1.Graph



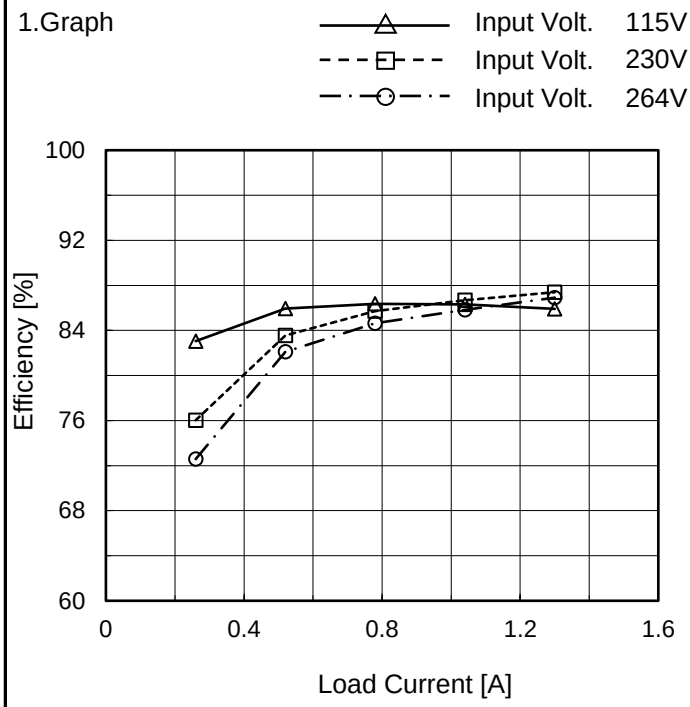
2.Values

Load Current [A]	Input Current [A]		
	Input Volt. 115[V]	Input Volt. 230[V]	Input Volt. 264[V]
0.00	0.016	0.025	0.028
0.26	0.142	0.097	0.092
0.52	0.243	0.156	0.144
0.78	0.341	0.214	0.196
1.04	0.438	0.271	0.248
1.30	0.537	0.327	0.299
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-

Model	WDA30F-24
Item	Efficiency (by Load Current)
Object	+24V1.3A

Temperature 25°C
Testing Circuitry Figure A

1.Graph



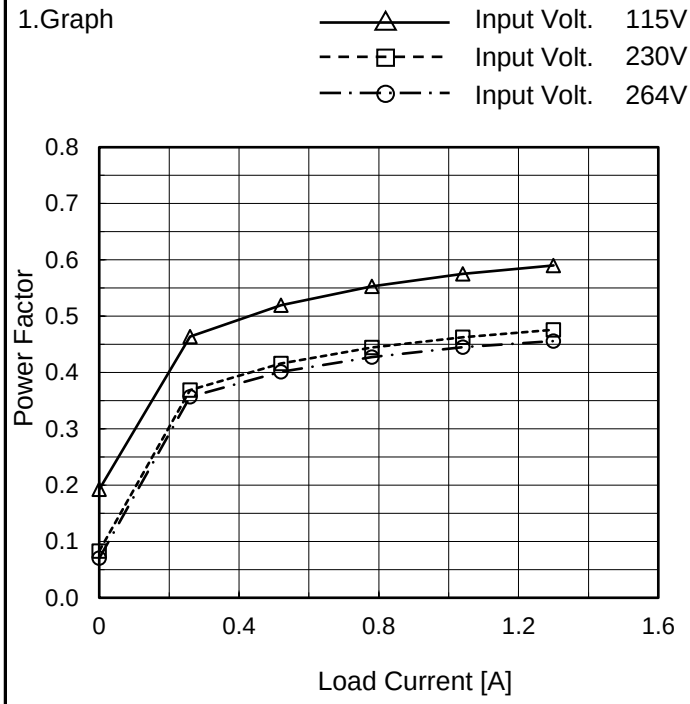
2.Values

Load Current [A]	Efficiency [%] Input Volt. 115[V]	Input Volt. 230[V]	Input Volt. 264[V]
0.00	-	-	-
0.26	83.1	76.0	72.6
0.52	85.9	83.6	82.1
0.78	86.4	85.7	84.6
1.04	86.3	86.7	85.8
1.30	85.9	87.4	86.9
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-

Model	WDA30F-24
Item	Power Factor (by Load Current)
Object	+24V1.3A

Temperature 25°C
Testing Circuitry Figure A

1.Graph



2.Values

Load Current [A]	Power Factor Input Volt. 115[V]	Input Volt. 230[V]	Input Volt. 264[V]
0.00	0.192	0.083	0.070
0.26	0.464	0.369	0.357
0.52	0.520	0.416	0.401
0.78	0.553	0.444	0.427
1.04	0.575	0.463	0.445
1.30	0.590	0.476	0.456
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-
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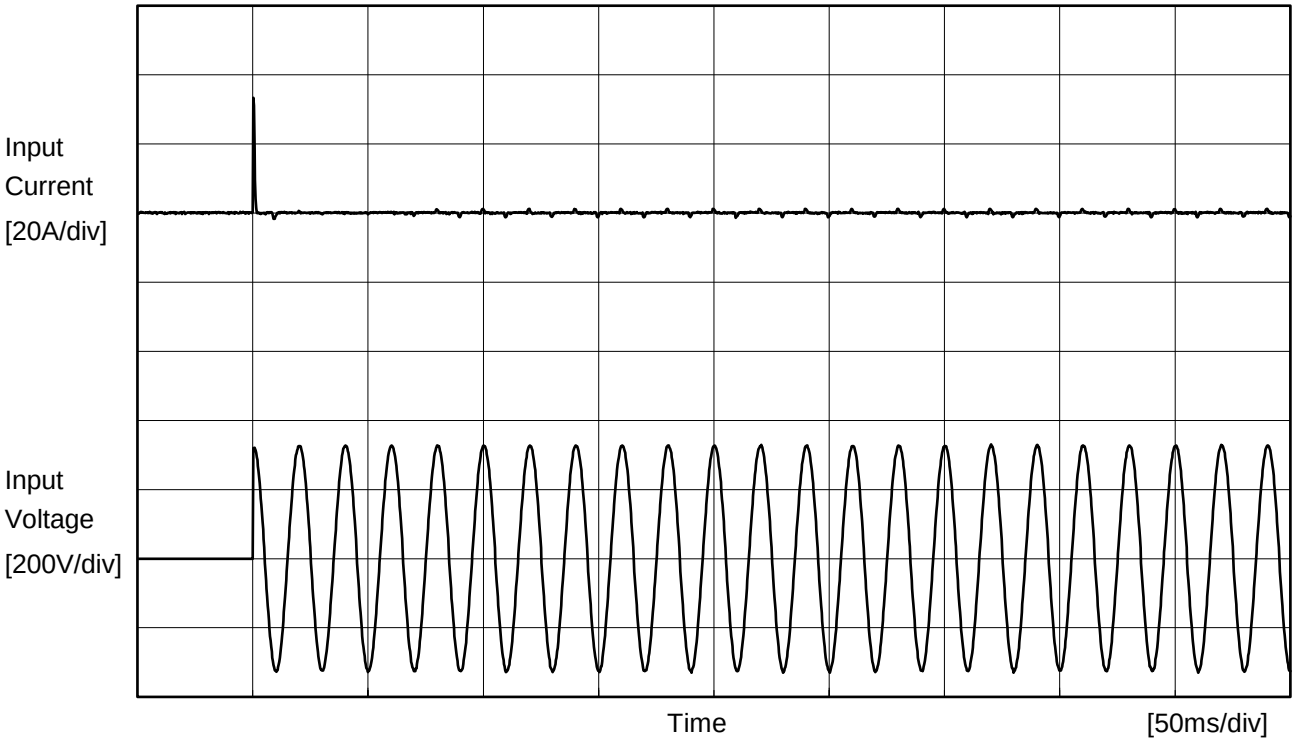
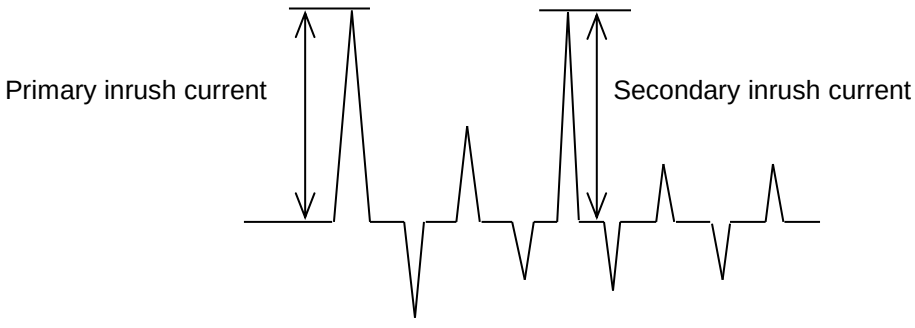
Model	WDA30F-24
Item	Inrush Current
Object	+24V1.3A

Temperature 25°C
Testing Circuitry Figure A



Input Voltage	230 V
Frequency	50 Hz
Load	100 %
Primary inrush current	33.2 A
Secondary inrush current	0.0 A



Model	WDA30F-24	
Item	Leakage Current	Temperature 25°C Testing Circuitry Figure C
Object	+24V1.3A	

1.Results

[mA]

Standards	Testing Circuitry	Measuring Method	Input Volt.			Note
			115 [V]	240 [V]	264 [V]	
DEN-AN	Figure C-1	Both phases	0.14	0.33	0.37	Operation
		One of phases	0.27	0.62	0.69	Stand by
IEC62368-1	Figure C-2	Both phases	0.14	0.32	0.35	Operation
		One of phases	0.27	0.60	0.67	Stand by
	Figure C-3	Both phases	0.14	0.35	0.35	Operation
		One of phases	0.26	0.67	0.66	Stand by

The value for "One of phases" is the reference value only.

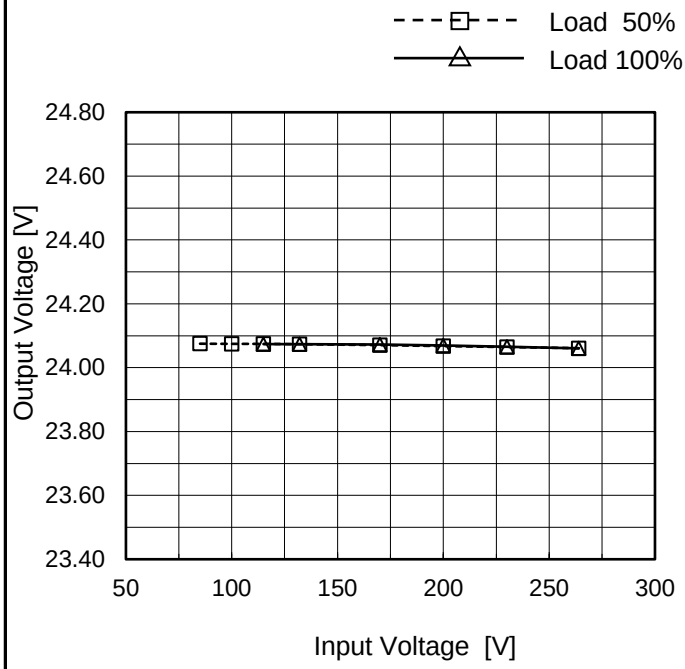
2.Condition

Leakage current value is concluded after measuring both phases of AC input and by choosing the larger one.

Model	WDA30F-24
Item	Line Regulation
Object	+24V1.3A

Temperature 25°C
Testing Circuitry Figure A

1.Graph



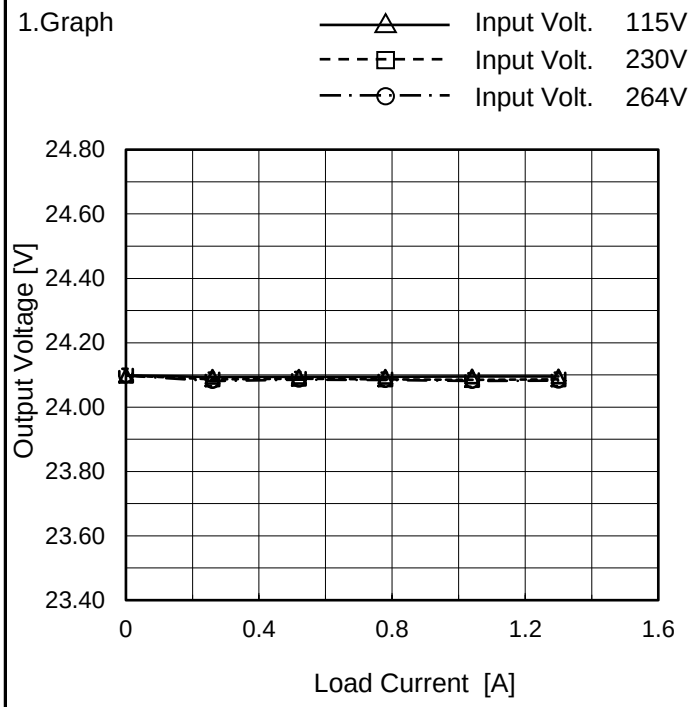
2.Values

Input Voltage [V]	Output Voltage [V]	
	Load 50%	Load 100%
85	24.075	-
100	24.075	-
115	24.075	24.074
132	24.074	24.074
170	24.071	24.072
200	24.068	24.070
230	24.065	24.065
264	24.061	24.061
--	-	-

Model	WDA30F-24
Item	Load Regulation
Object	+24V1.3A

Temperature 25°C
Testing Circuitry Figure A

1.Graph



2.Values

Load Current [A]	Output Voltage [V]		
	Input Volt. 115[V]	Input Volt. 230[V]	Input Volt. 264[V]
0.00	24.098	24.098	24.098
0.26	24.094	24.086	24.081
0.52	24.094	24.088	24.085
0.78	24.094	24.087	24.084
1.04	24.095	24.085	24.082
1.30	24.095	24.086	24.082
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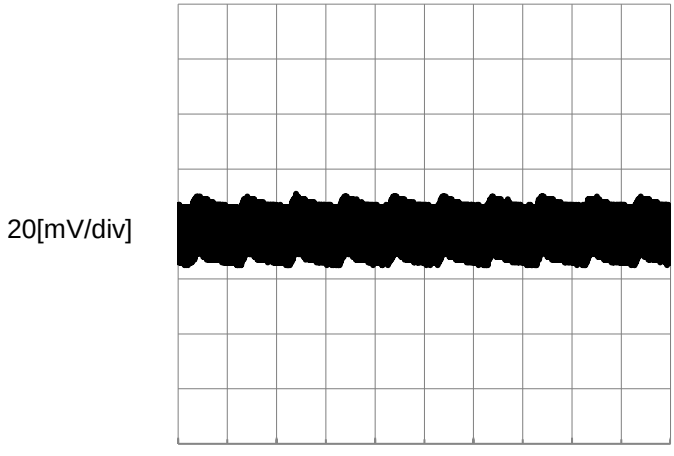
--	--	--	--
--	--	--	--
--	--	--	--

Item	Ripple-Noise
Object	+24V1.3A

Temperature 25°C
Testing Circuitry Figure B

1.Graph

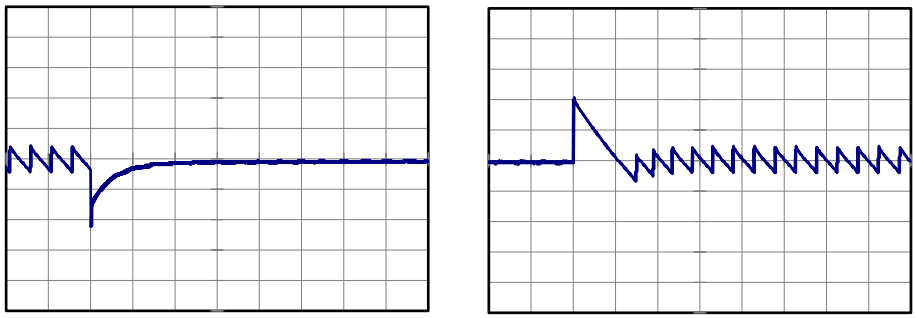
Input Voltage 230V
Load 100%



Model	WDA30F-24	
Item	Dynamic Load Response	Temperature 25°C Testing Circuitry Figure A
Object	+24V1.3A	

Input Volt. 230 V
Cycle 1000 ms

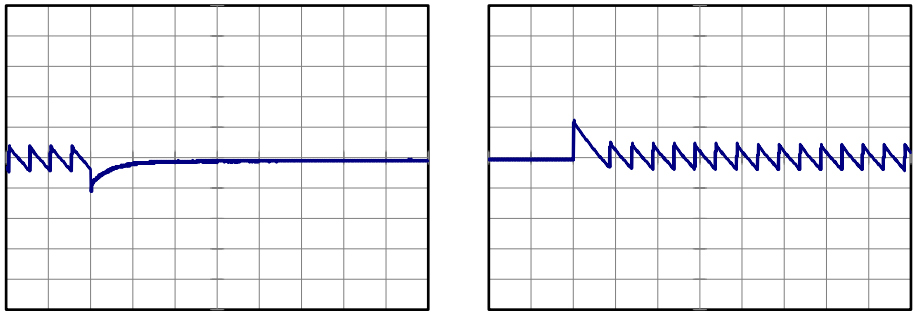
Min.Load (0A)←→
Load 100% (1.3A)



100 mV/div

20 ms/div 20 ms/div

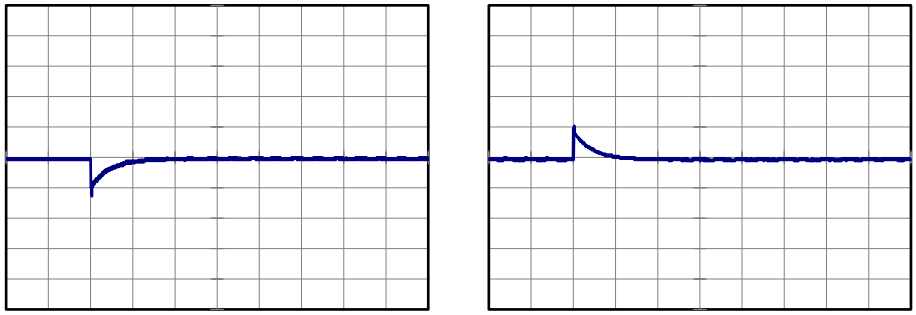
Min.Load (0A)←→
Load 50% (0.65A)



100 mV/div

20 ms/div 20 ms/div

Load 50% (0.65A)←→
Load 100% (1.3A)

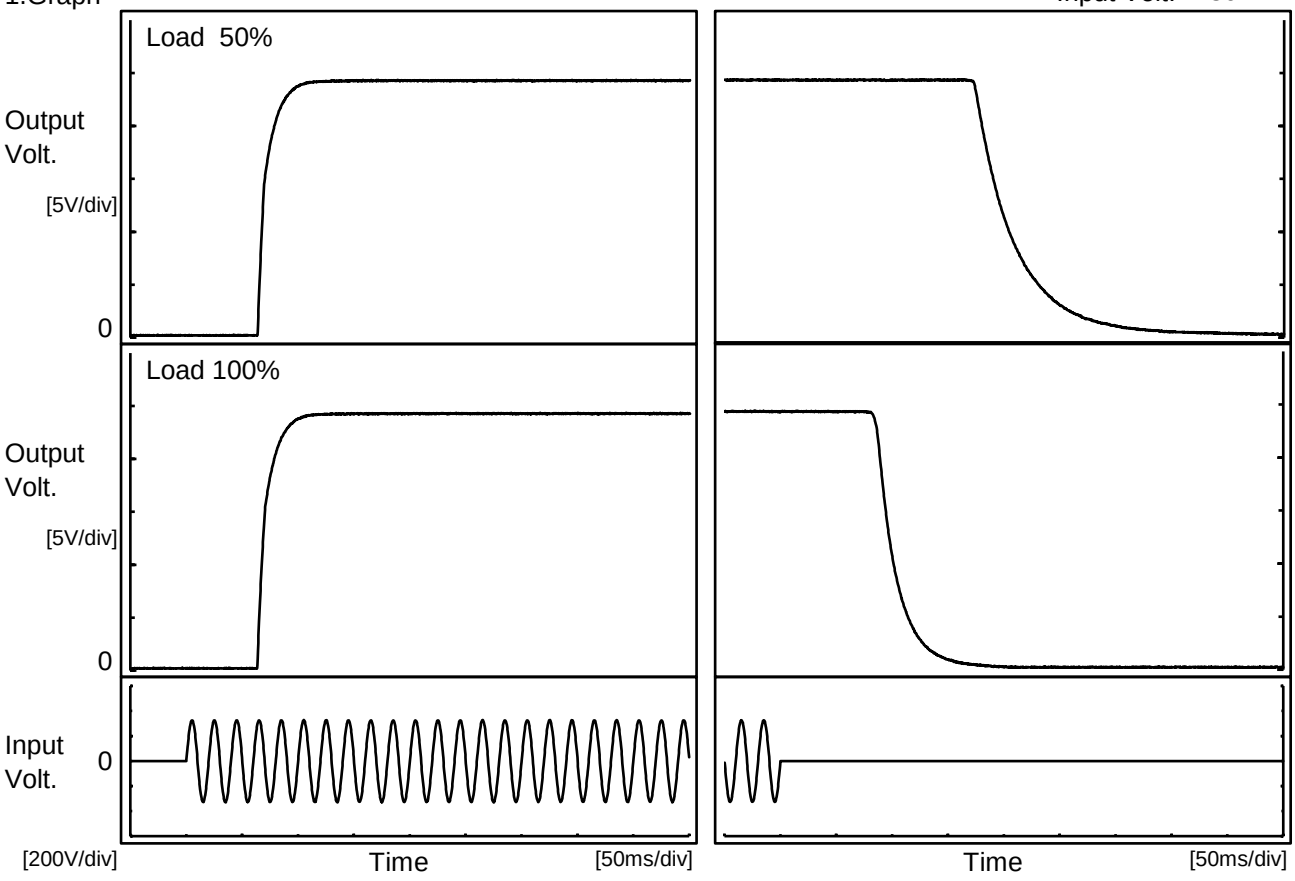


100 mV/div

20 ms/div 20 ms/div

Model	WDA30F-24	
Item	Rise and Fall Time	Temperature 25°C Testing Circuitry Figure A
Object	+24V1.3A	

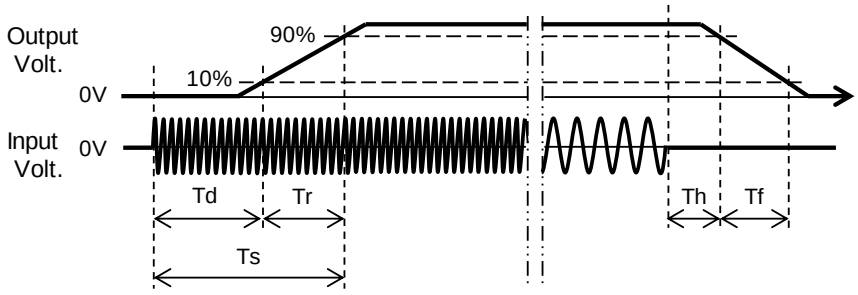
1.Graph



2.Values

[ms]

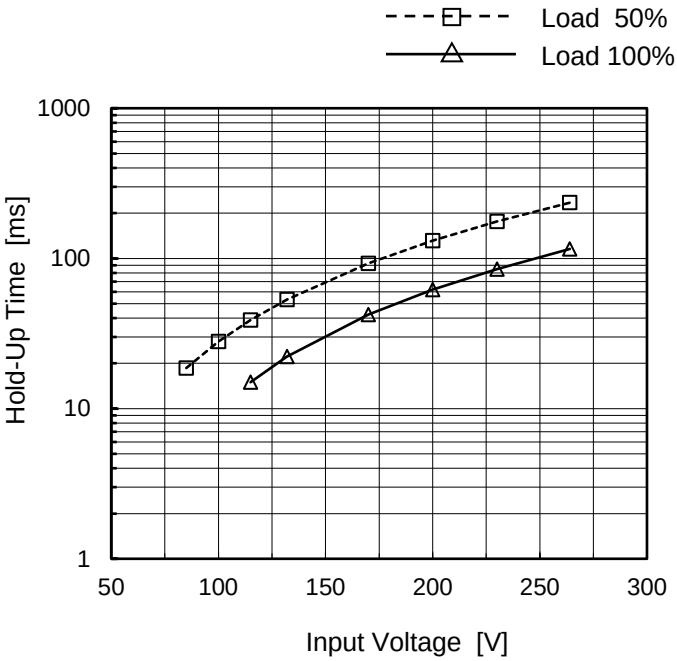
Load \ Time	Td	Tr	Ts	Th	Tf
50 %	64.5	20.0	84.5	176.8	79.8
100 %	64.3	20.3	84.6	87.3	39.3



Model	WDA30F-24
Item	Hold-Up Time
Object	+24V1.3A

Temperature 25°C
Testing Circuitry Figure A

1.Graph



This duration covers from Shut-off of input voltage to the moment when output voltage descends to the rated range of voltage accuracy.

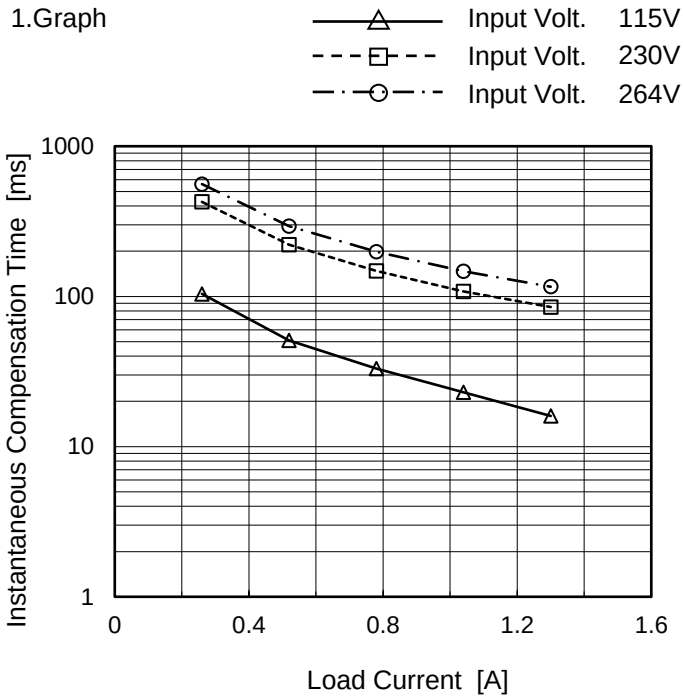
2.Values

Input Voltage [V]	Hold-Up Time [ms]	
	Load 50%	Load 100%
85	19	-
100	28	-
115	39	15
132	53	22
170	93	42
200	131	62
230	176	85
264	235	116
--	-	-

Model	WDA30F-24
Item	Instantaneous Interruption Compensation
Object	+24V1.3A

Temperature 25°C
Testing Circuitry Figure A

1.Graph



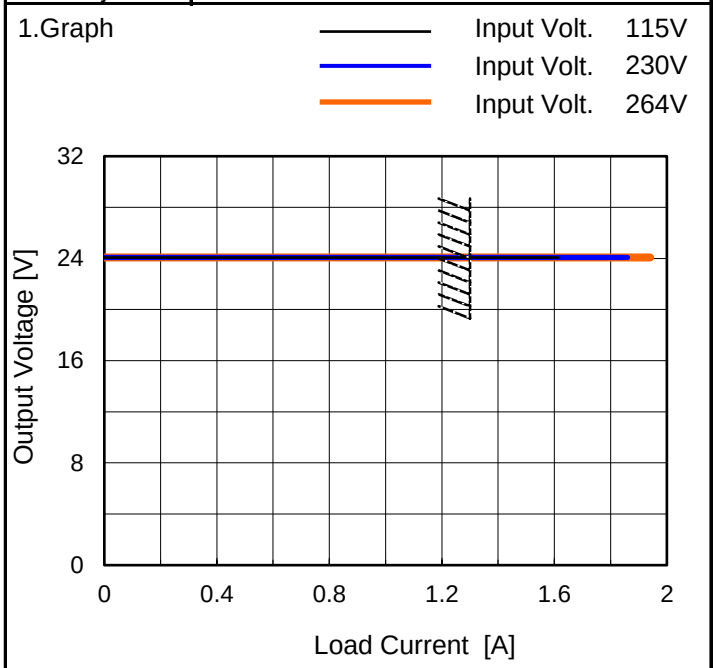
2.Values

Load Current [A]	Time [ms]		
	Input Volt. 115[V]	Input Volt. 230[V]	Input Volt. 264[V]
0.00	-	-	-
0.26	104	426	560
0.52	51	221	294
0.78	33	148	198
1.04	23	108	147
1.30	16	85	116
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-

Model	WDA30F-24
Item	Overcurrent Protection
Object	+24V1.3A

Temperature 25°C
Testing Circuitry Figure A

1.Graph



Note: Slanted line shows the range of the rated load current.

Hiccup mode activates when the output voltage is from 1.0 to 0V.

2.Values

Output Voltage [V]	Load Current [A]		
	Input Volt. 115[V]	Input Volt. 230[V]	Input Volt. 264[V]
24	1.61	1.86	1.94
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-

Model	WDA30F-24	
Item	Ambient Temperature Drift	Testing Circuitry Figure A
Object	+24V1.3A	

1.Values

Load 100%

Ambient Temperature[℃]	Output Voltage [V]		
	Input Volt. 115V	Input Volt. 230V	Input Volt. 264V
-20	24.025	24.016	24.012
25	24.071	24.059	24.055
50	24.066	24.056	24.052

Item	Minimum Input Voltage for Regulated Output Voltage	Testing Circuitry Figure A
Object	+24V1.3A	

1.Values

Ambient Temperature[℃]	Input Voltage [V]	
	Load 50%	Load 100%
-20	38	76
25	37	75
50	38	75

Item	Overvoltage Protection	Testing Circuitry Figure A
Object	+24V1.3A	

1.Values

Load 0%

Ambient Temperature[℃]	Operating Point [V]	
	Input Volt. 115V	Input Volt. 264V
-20	30.53	30.41
25	31.41	31.41
50	32.06	32.06

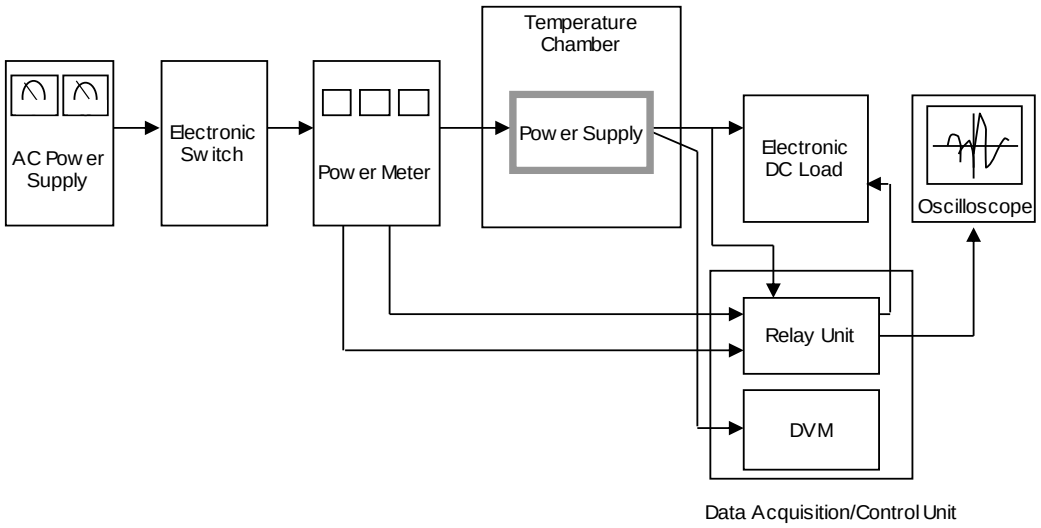
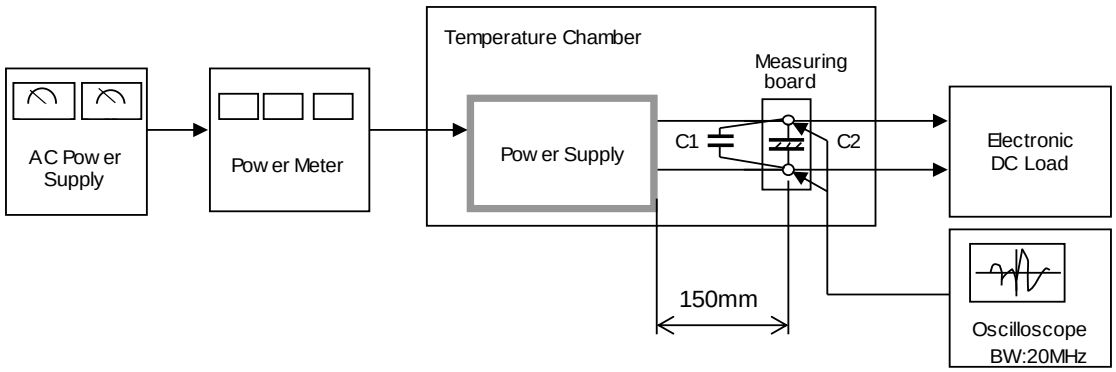


Figure A



C1= 0.1 μF
(Ceramic capacitor)

C2= 47 μF
(Electrolytic capacitor)

Figure B

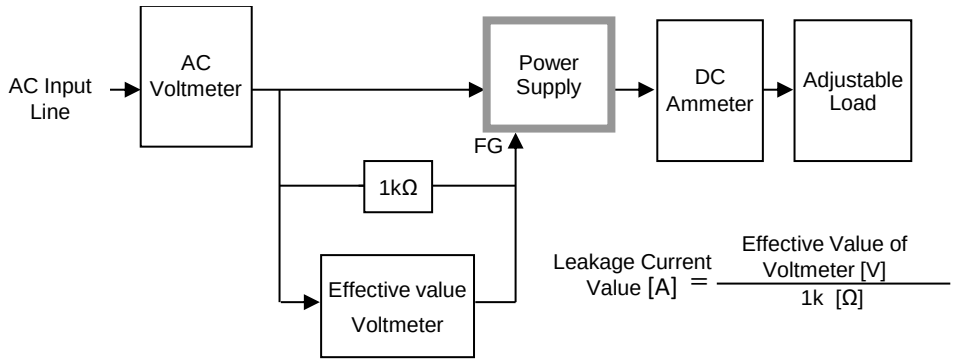


Figure C-1 (DEN-AN)

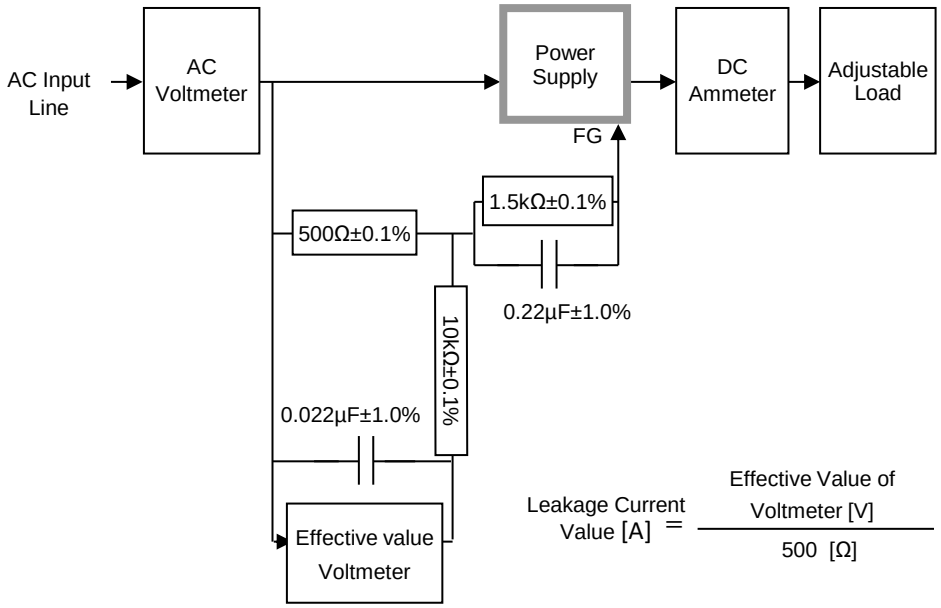


Figure C-2 (IEC62368-1 refer to IEC60990 Fig.4)

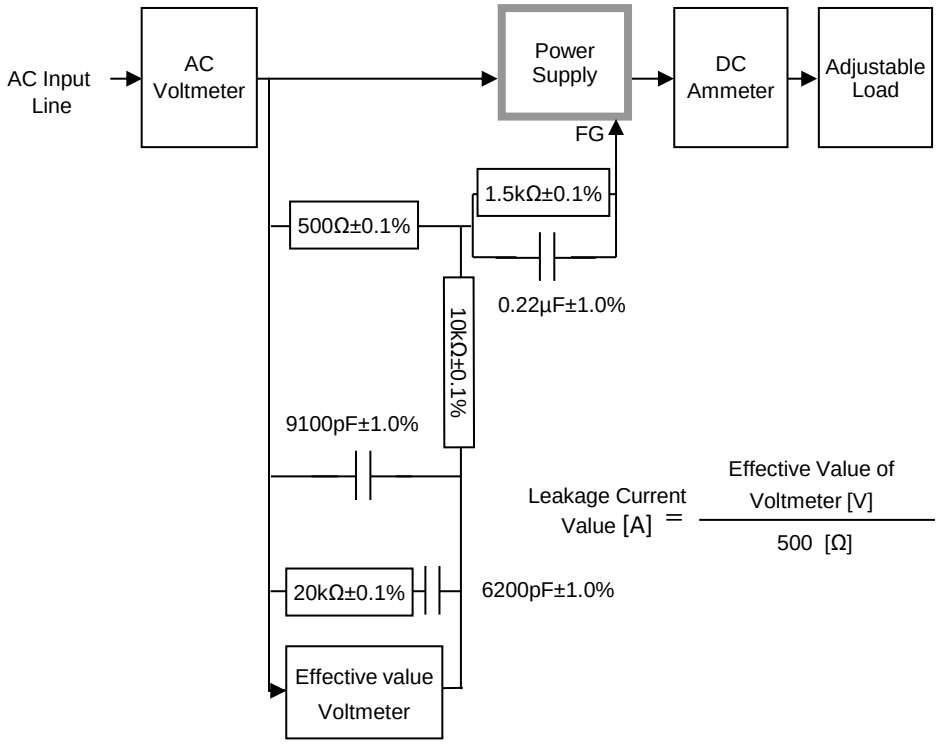


Figure C-3 (IEC62368-1 refer to IEC60990 Fig.5)