参考資料

COSEL

TEST DATA OF VAF1005
(100V INPUT)

Regulated DC Power Supply

Date : May 28. 1999

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Design Manager

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Design Engineer

コーセル株式会社
COSEL CO.,LTD.

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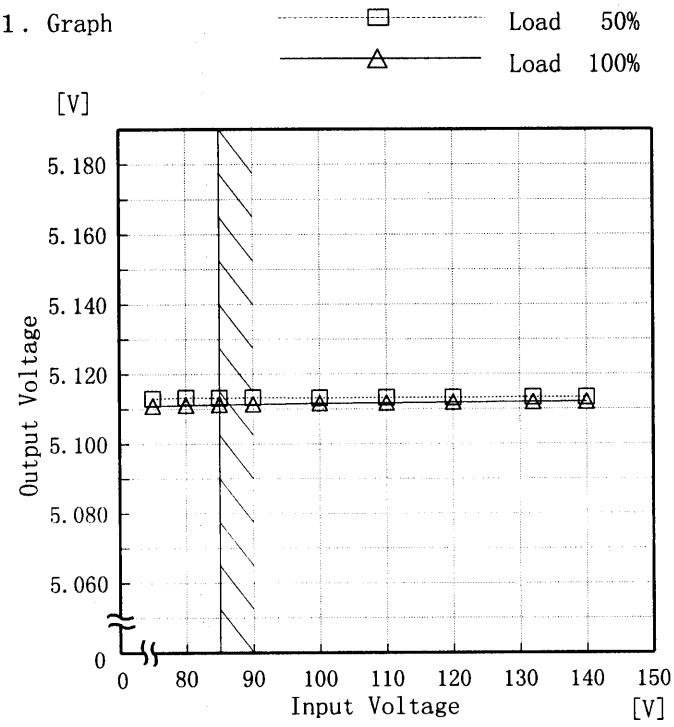

Note: Slanted line shows the range of the rated input voltage.

(注)斜線は定格入力電圧範囲を示す。

2. Values

Input Voltage [V]	Output Voltage [V]	
	Load 50%	Load 100%
75	5.113	5.111
80	5.113	5.111
85	5.113	5.111
90	5.113	5.111
100	5.113	5.112
110	5.113	5.112
120	5.113	5.112
132	5.113	5.112
140	5.113	5.112

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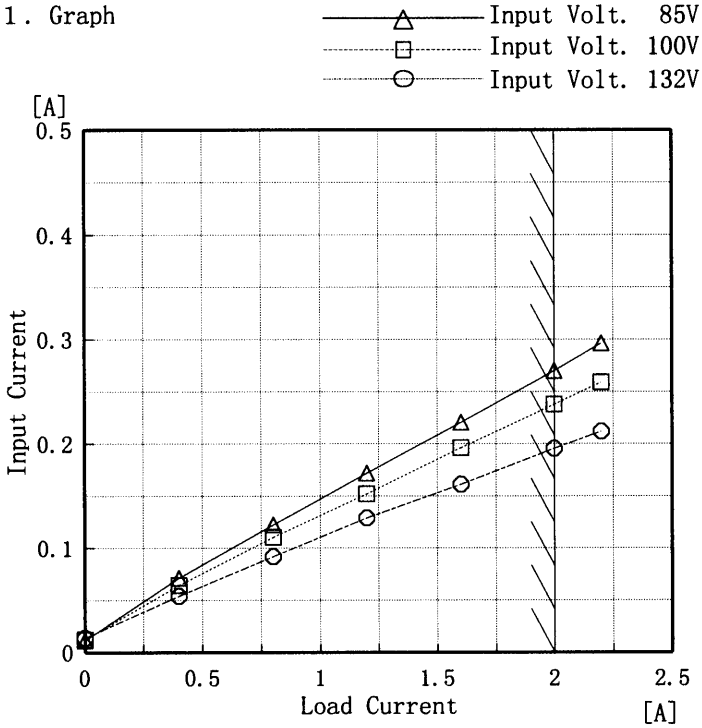
Model	VAF1005
Item	Input Current (by Load Current) 入力電流（負荷特性）
Output	————

Temperature 25℃
Testing Circuitry Figure A

1. Graph



Note: Slanted line shows the range of the rated load current

(注)斜線は定格負荷電流範囲を示す。

2. Values

Load Current [A]	Input Current [A]		
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
0.0	0.012	0.013	0.014
0.4	0.071	0.064	0.054
0.8	0.122	0.110	0.092
1.2	0.172	0.152	0.129
1.6	0.221	0.196	0.161
2.0	0.270	0.238	0.196
2.2	0.296	0.259	0.212
—	—	—	—
—	—	—	—
—	—	—	—
—	—	—	—
—	—	—	—

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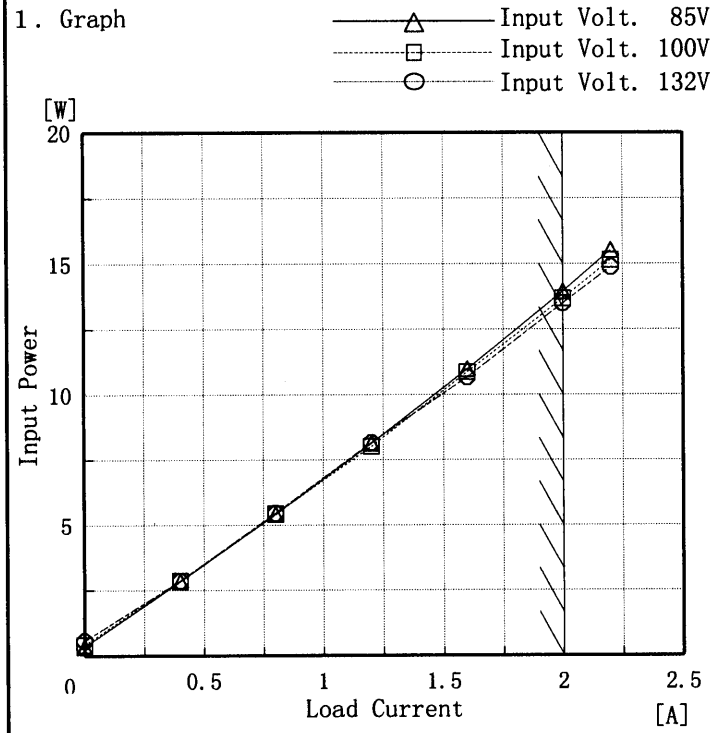
Model	VAF1005
Item	Input Power (by Load Current) 入力電力（負荷特性）
Output	————

Temperature 25℃
Testing Circuitry Figure A

1. Graph



Note: Slanted line shows the range of the rated load current

(注)斜線は定格負荷電流範囲を示す。

2. Values

Load Current [A]	Input Power [W]		
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
0.0	0.33	0.41	0.57
0.4	2.84	2.86	2.87
0.8	5.42	5.44	5.46
1.2	8.14	8.02	8.16
1.6	10.97	10.86	10.66
2.0	13.94	13.68	13.50
2.2	15.52	15.15	14.88
—	—	—	—
—	—	—	—
—	—	—	—
—	—	—	—
—	—	—	—

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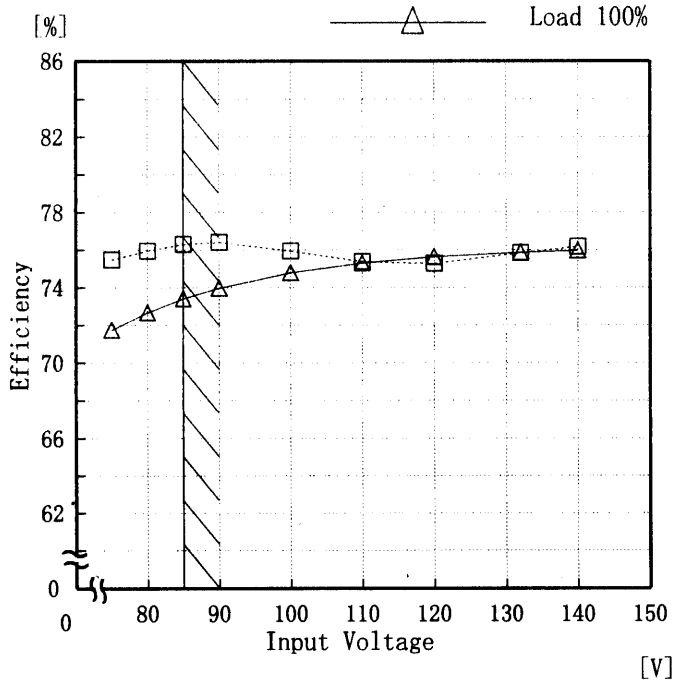
Model	VAF1005
Item	Efficiency 効率
Object	――――――――――

Temperature 25℃
Testing Circuitry Figure A

1. Graph

□ Load 50%
△ Load 100%



Note: Slanted line shows the range of the rated input voltage.

(注)斜線は定格入力電圧範囲を示す。

2. Values

Input Voltage [V]	Efficiency [%]	
	Load 50%	Load 100%
75	75.5	71.8
80	76.0	72.7
85	76.3	73.4
90	76.4	74.0
100	76.0	74.8
110	75.4	75.3
120	75.3	75.6
132	75.8	75.9
140	76.2	76.0

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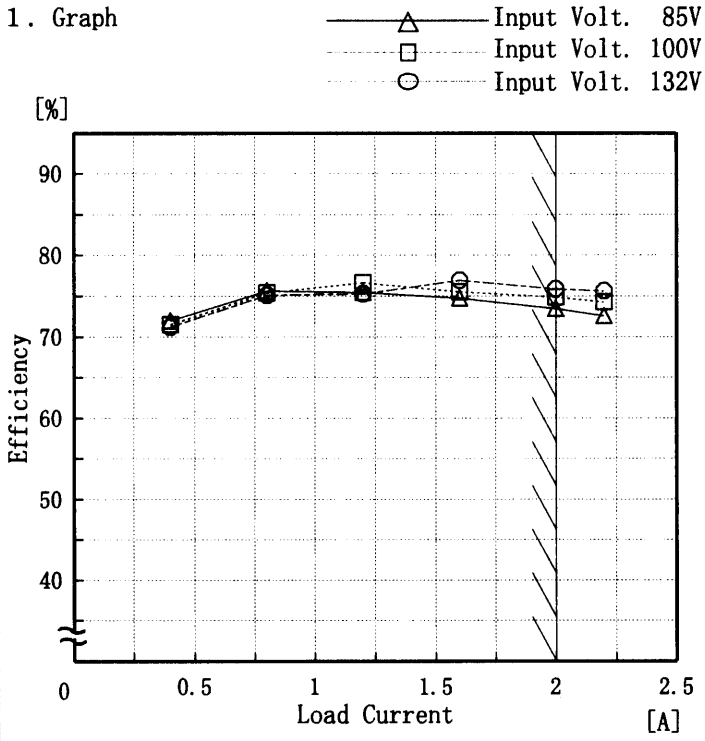
Model	VAF1005
Item	Efficiency (by Load Current) 効率 (負荷電流特性)
Output	————

Temperature 25℃
Testing Circuitry Figure A

1. Graph



Note: Slanted line shows the range of the rated load current

(注)斜線は定格負荷電流範囲を示す。

2. Values

Load Current [A]	Efficiency [%]		
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
0.4	72.0	71.5	71.2
0.8	75.7	75.4	75.1
1.2	75.5	76.6	75.3
1.6	74.7	75.5	76.9
2.0	73.5	74.9	75.9
2.2	72.5	74.3	75.7
—	—	—	—
—	—	—	—
—	—	—	—
—	—	—	—
—	—	—	—
—	—	—	—

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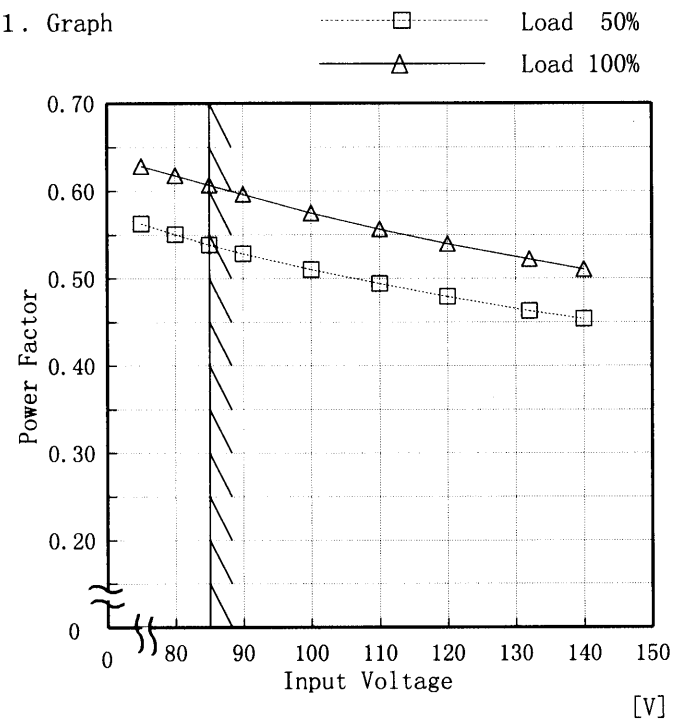
Model	VAF1005	
Item	Power Factor (by Input Voltage) 力率（入力電圧特性）	Temperature 25℃ Testing Circuitry Figure A
Object	———————————	

1. Graph



Note: Slanted line shows the range of the rated input voltage.

(注)斜線は定格入力電圧範囲を示す。

2. Values

Input Voltage [V]	Power Factor	
	Load 50%	Load 100%
75	0.56	0.63
80	0.55	0.62
85	0.54	0.61
90	0.53	0.60
100	0.51	0.58
110	0.49	0.56
120	0.48	0.54
132	0.46	0.52
140	0.45	0.51

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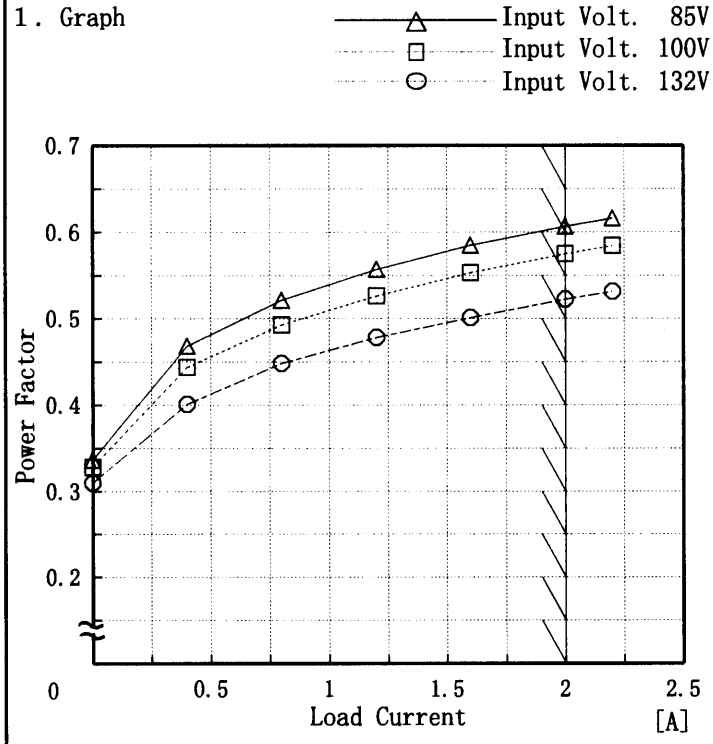
Model	VAF1005
Item	Power Factor (by Load Current) 力率 (負荷電流特性)
Output	————

Temperature 25℃
Testing Circuitry Figure A

1. Graph



Note: Slanted line shows the range of the rated load current

(注)斜線は定格負荷電流範囲を示す。

2. Values

Load Current [A]	Power Factor		
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
0.0	0.34	0.33	0.31
0.4	0.47	0.44	0.40
0.8	0.52	0.49	0.45
1.2	0.56	0.53	0.48
1.6	0.58	0.55	0.50
2.0	0.61	0.57	0.52
2.2	0.62	0.58	0.53
—	—	—	—
—	—	—	—
—	—	—	—
—	—	—	—
—	—	—	—

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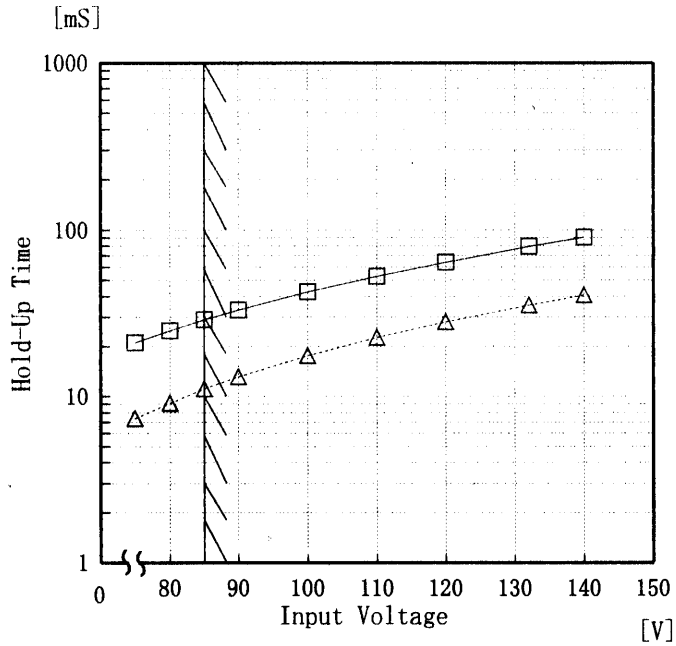
Model	VAF1005	Temperature	25℃
Item	Hold-Up Time 出力保持時間	Testing Circuitry	Figure A
Object	+5.0V2A		

1. Graph

□ Load 50%

△ Load 100%



This duration covers from Shut-off of input voltage to the moment when output voltage descends to the rated range of voltage accuracy.

Note: Slanted line shows the range of the rated input voltage.

出力保持時間とは、入力電圧断から出力電圧が、定電圧精度の規格範囲を保持しているところまでの時間。

(注)斜線は定格入力電圧範囲を示す。

2. Values

Input Voltage [V]	Hold-Up Time [mS]	
	Load 50%	Load 100%
75	21	7
80	25	9
85	29	11
90	33	13
100	43	18
110	53	23
120	64	28
132	79	35
140	90	41

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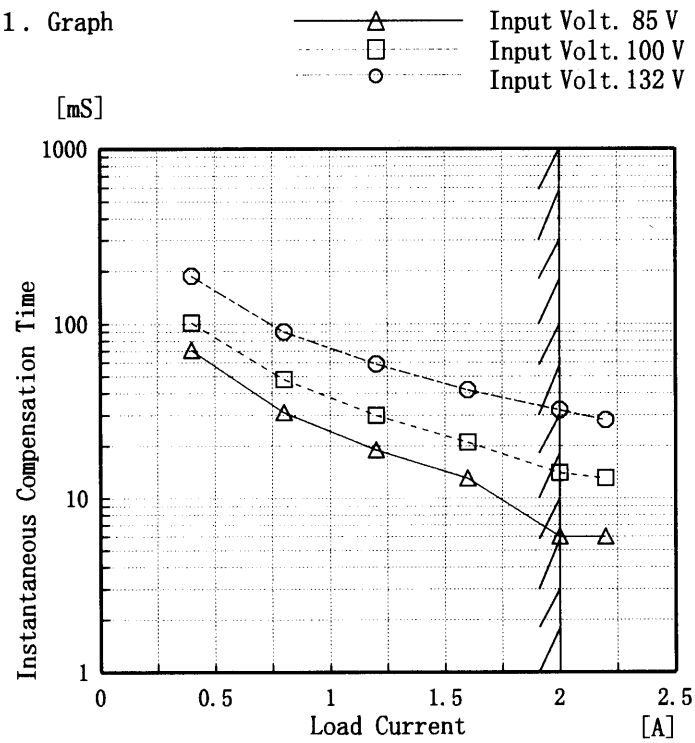
Model	VAF1005
Item	Instantaneous Interruption Compensation 瞬時停電保障
Object	+5.0V2A

Temperature 25℃
Testing Circuitry Figure A

1. Graph



This duration covers from Shut-off of input voltage to the moment when output voltage descends to the rated range of voltage accuracy.
Note:Slanted line shows the range of the rated load current.

瞬時停電保障時間とは、出力電圧が定電圧精度の規格範囲を保持している瞬時停電時間をいう。
(注)斜線は定格負荷電流範囲を示す。

2. Values

Load Current [A]	Time [mS]		
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
0.0	—	—	—
0.4	71	102	188
0.8	31	48	90
1.2	19	30	59
1.6	13	21	42
2.0	6	14	32
2.2	6	13	28
—	—	—	—
—	—	—	—
—	—	—	—
—	—	—	—

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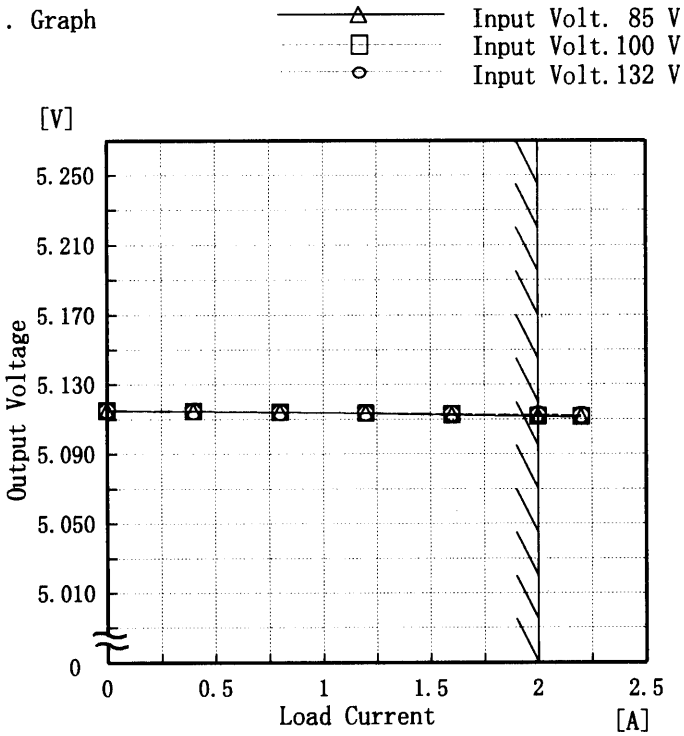
Model	VAF1005
Item	Load Regulation 静的負荷変動
Object	+5.0V2A

Temperature 25℃
Testing Circuitry Figure A

1. Graph



Note: Slanted line shows the range of the rated load current.

(注)斜線は定格負荷電流範囲を示す。

2. Values

Load Current [A]	Output Voltage [V] Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
0.0	5.115	5.115	5.115
0.4	5.115	5.115	5.115
0.8	5.114	5.114	5.114
1.2	5.113	5.114	5.114
1.6	5.112	5.113	5.113
2.0	5.112	5.112	5.112
2.2	5.111	5.112	5.112
—	—	—	—
—	—	—	—
—	—	—	—

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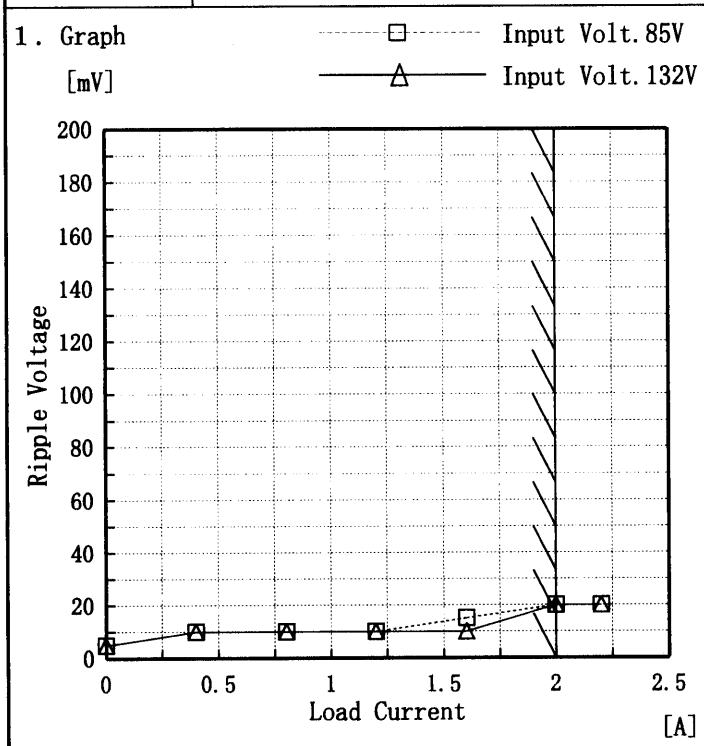
Model	VAF1005		
Item	Ripple Voltage(by Load Current) リップル電圧(負荷電流特性)	Temperature Testing Circuitry	25℃ Figure A
Object	+5.0V2A		

1. Graph



Ripple Voltage is shown as p-p in the figure below.

Note: Slanted line shows the range of the rated load current.

リップル電圧は、下図ｐ－ｐ値で示される。

(注)斜線は定格負荷電流範囲を示す。

T1: Due to AC Input Line
入力商用周期
T2: Due to Switching
ｽｲｯﾁﾝｸﾞ周期

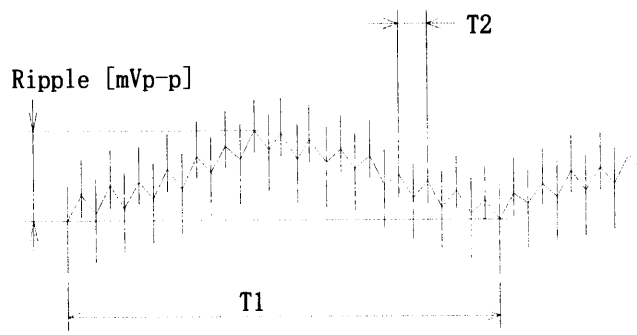


Fig. Complex Ripple Wave Form
図　リップル波形詳細図

2. Values

Load Current [A]	Ripple Voltage [mV]	
	Input Volt. 85 [V]	Input Volt. 132 [V]
0.0	5	5
0.4	10	10
0.8	10	10
1.2	10	10
1.6	15	10
2.0	20	20
2.2	20	20
—	—	—
—	—	—
—	—	—
—	—	—

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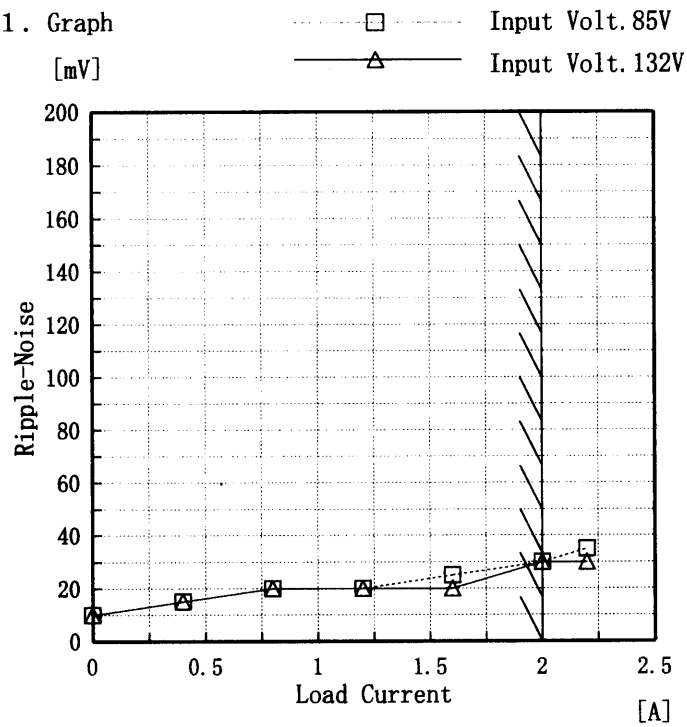
Model	VAF1005	Temperature	25℃
Item	Ripple-Noise リップルノイズ	Testing Circuitry	Figure A
Object	+5.0V2A		

1. Graph



Ripple-Noise is shown as p-p in the figure below.
Note: Slanted line shows the range of the rated load current.

リップルノイズは、下図ｐ－ｐ値で示される。
(注)斜線は定格負荷電流範囲を示す。

T1: Due to AC Input Line
入力商用周期
T2: Due to Switching
ｽｲｯﾁﾝｸﾞ周期

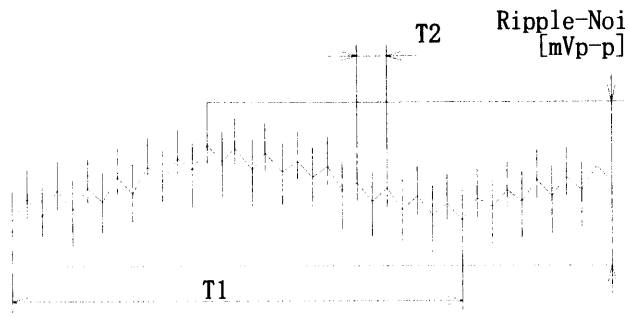


Fig. Complex Ripple Wave Form
図 リップル波形詳細図

2. Values

Load Current [A]	Ripple-Noise [mV]	
	Input Volt. 85 [V]	Input Volt. 132 [V]
0.0	10	10
0.4	15	15
0.8	20	20
1.2	20	20
1.6	25	20
2.0	30	30
2.2	35	30
—	—	—
—	—	—
—	—	—
—	—	—

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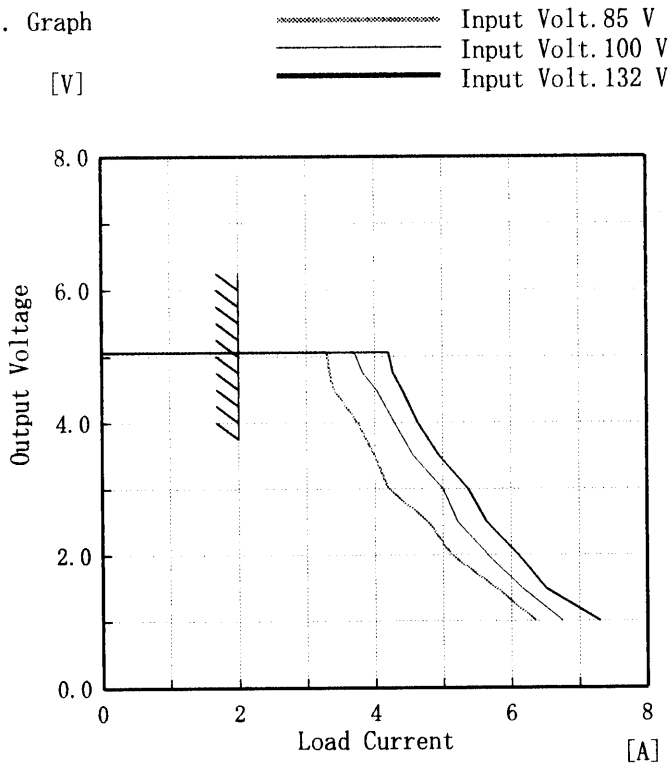
Model	VAF1005
Item	Overcurrent Protection 過電流保護
Object	+5.0V2A

Temperature 25℃
Testing Circuitry Figure A

1. Graph



Note1: Slanted line shows the range of the rated load current.

Note2: The lines shows peak current of intermittent operation of power supply when output voltage drops less than rated voltage value at overcurrent.

(注１)斜線は定格負荷電流範囲を示す。

(注２)垂下部分は間欠モード時のピーク電流を示す。

2. Values

Output Voltage [V]	Load Current [A]		
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
5.00	3.30	3.70	4.20
4.75	3.35	3.82	4.26
4.50	3.40	4.02	4.40
4.00	3.75	4.28	4.63
3.50	4.00	4.56	4.94
3.00	4.20	5.00	5.37
2.50	4.78	5.21	5.64
2.00	5.12	5.66	6.10
1.50	5.78	6.16	6.50
1.00	6.36	6.76	7.31
0.50	—	—	—
0.00	—	—	—

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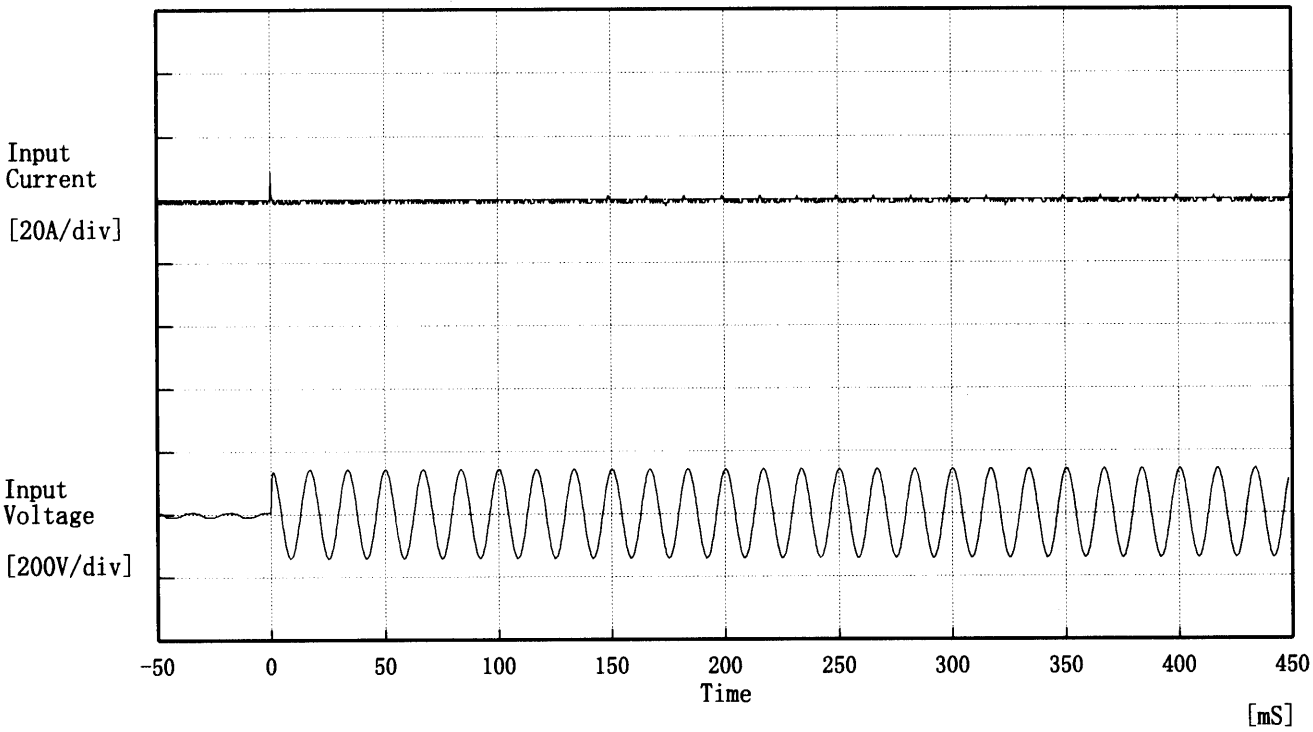
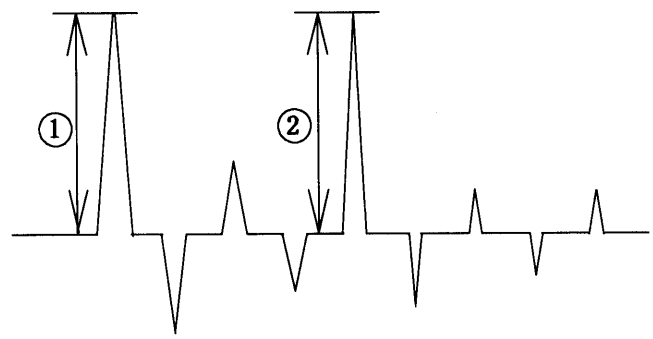
Model	VAF1005
Item	Inrush Current 突入電流
Object	――――――――

Temperature 25℃
Testing Circuitry Figure A



Input Voltage 100 V
Frequency 60 Hz
Load 100 %
Inrush Current
① 9.01 [A]
② 2.21 [A]



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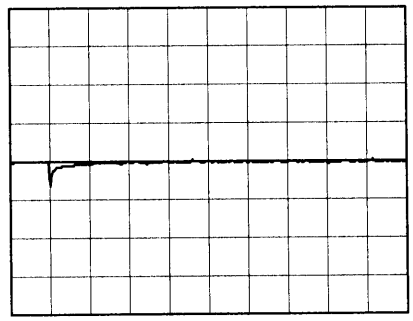
Model	VAF1005
Item	Dynamic Load Responce 動的負荷変動
Object	+5.0V2A

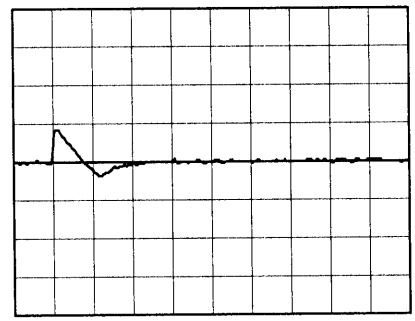
Temperature 25℃
Testing Circuitry Figure A

Input Volt. 100 V
Cycle 1000 mS

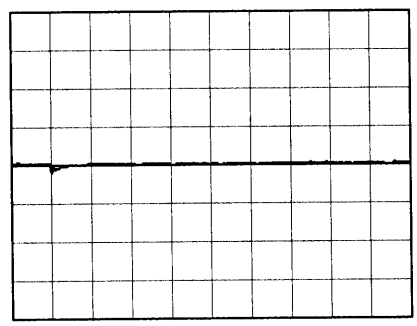
Load Current

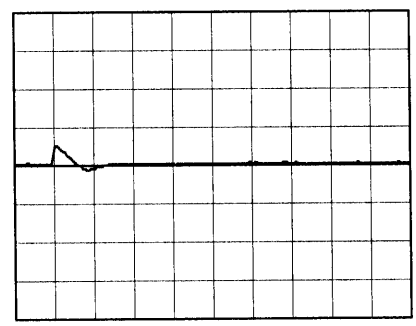
Min. Load ←→
Load 100 %





Min. Load ←→
Load 50 %





100 mV/div

20 mS/div

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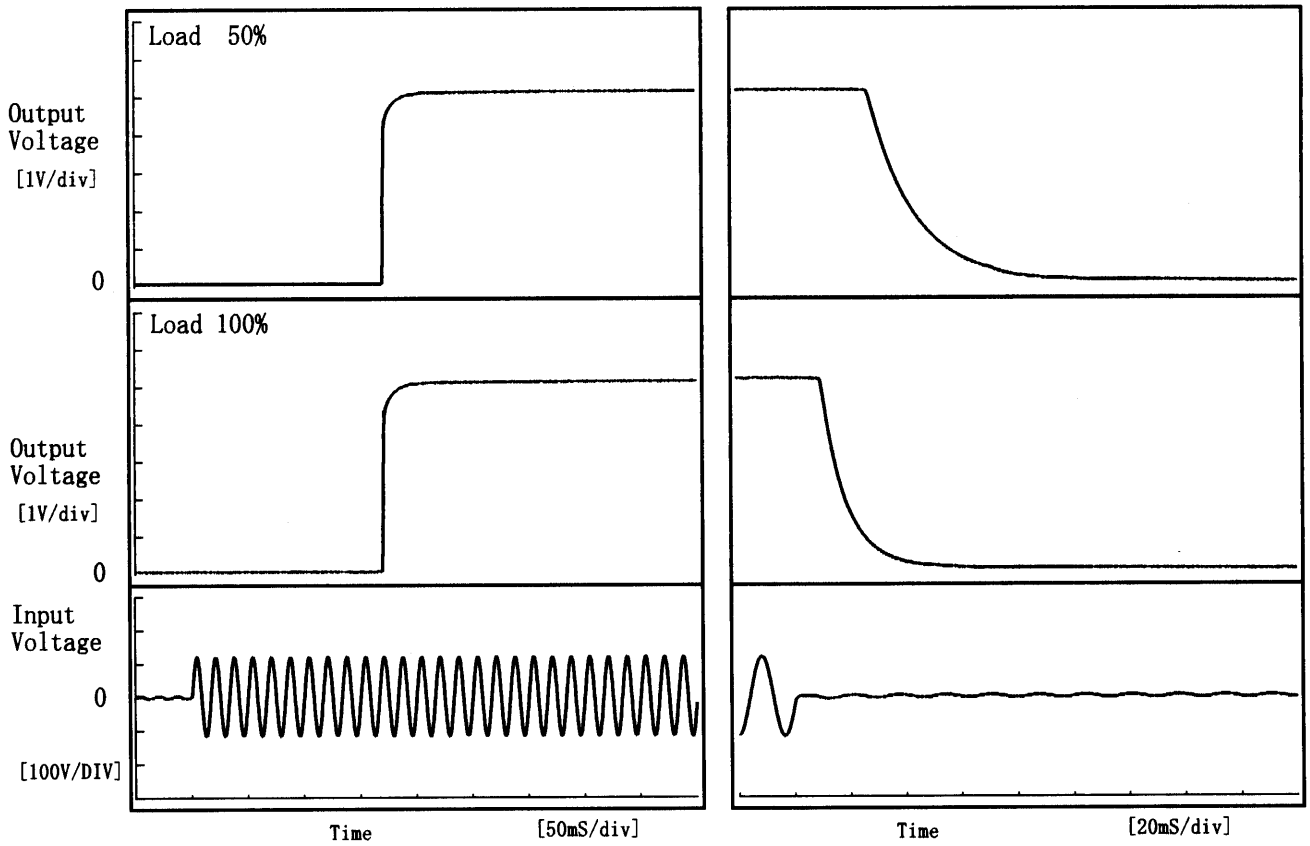
Model	VAF1005		
Item	Rise and Fall Time　立上り、立下り時間	Temperature	25℃
		Testing Circuitry	Figure A
Object	+5.0V2A		

1. Graph

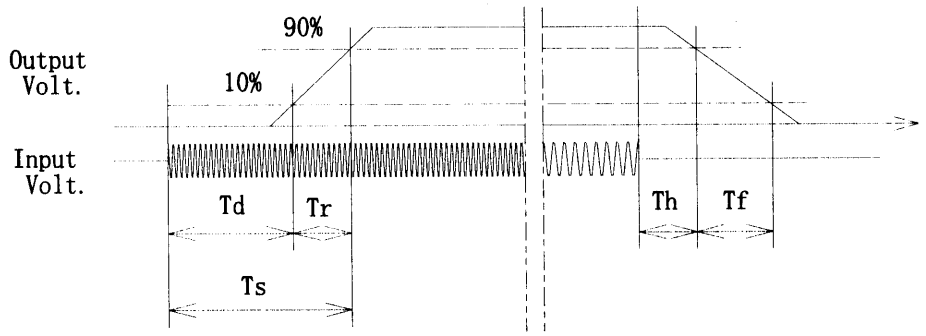
Input Volt. 85 V



2. Values

[mS]

Load \ Time	T d	T r	T s	T h	T f
50 %	170.3	3.8	174.0	29.1	39.3
100 %	170.3	4.0	174.3	11.2	20.9

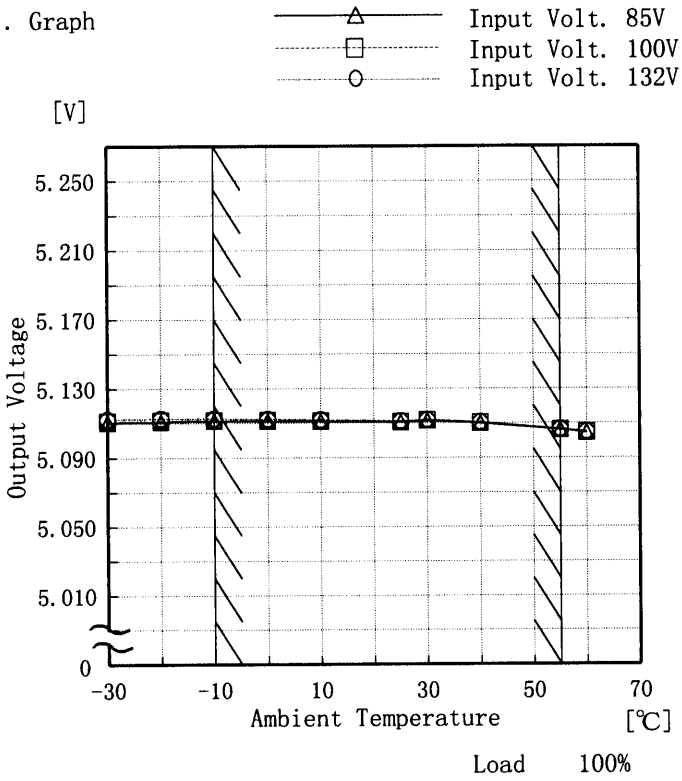


COSEL

Model	VAF1005
Item	Ambient Temperature Drift 周囲温度変動
Object	+5.0V2A

Testing Circuitry Figure A

1. Graph



Note: Slanted line shows the range of the rated ambient temperature.

(注)斜線は定格周囲温度範囲を示す。

2. Values

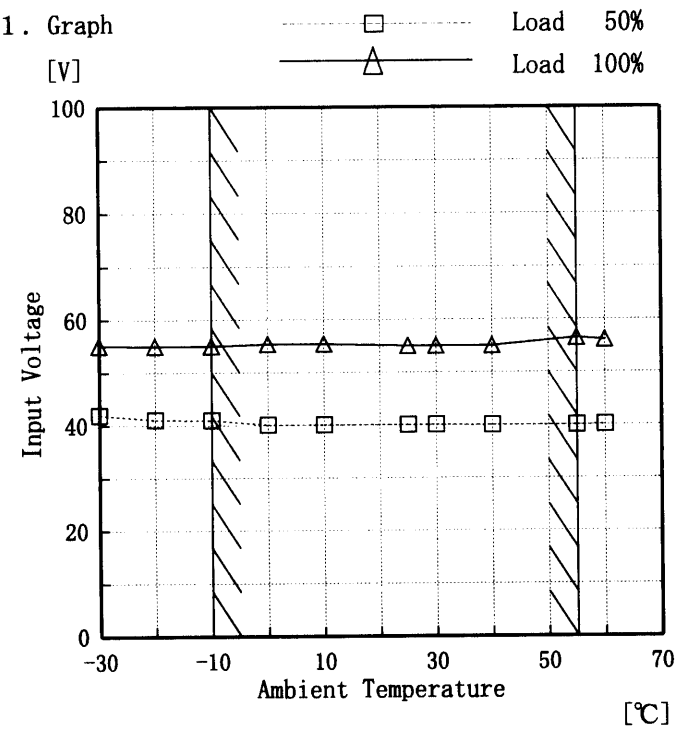
Temperature [℃]	Output Voltage [V]		
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
-30	5.110	5.111	5.112
-20	5.111	5.112	5.113
-10	5.111	5.112	5.113
0	5.111	5.112	5.112
10	5.111	5.111	5.112
25	5.110	5.111	5.111
30	5.111	5.112	5.112
40	5.110	5.110	5.111
55	5.106	5.106	5.107
60	5.104	5.105	5.105
—	—	—	—

COSEL

Model	VAF1005
Item	Minimum Input Voltage for Regulated Output Voltage 最低レギュレーション電圧
Object	+5.0V2A

Testing Circuitry Figure A

1. Graph



Note: Slanted line shows the range of the rated ambient temperature.

(注)斜線は定格周囲温度範囲を示す。

2. Values

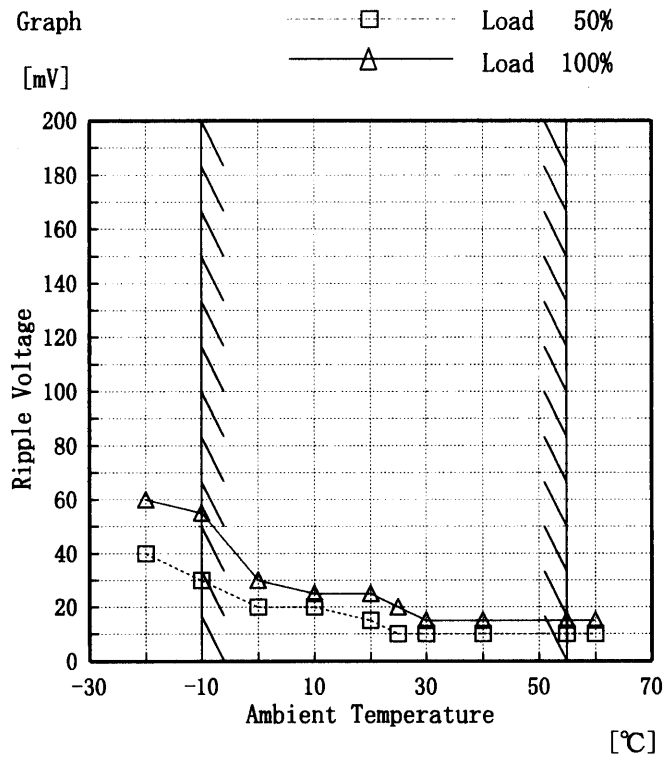
Ambient Temperature [℃]	Input Voltage [V]	
	Load 50%	Load 100%
-30	42	55
-20	41	55
-10	41	55
0	40	55
10	40	55
25	40	55
30	40	55
40	40	55
55	40	56
60	40	56
—	—	—

COSEL

Model	VAF1005
Item	Ripple Voltage (by Ambient Temp.) リップル電圧 (周囲温度特性)
Object	+5.0V2A

Testing Circuitry Figure A

1. Graph



Input Volt. 100 V

Note: Slanted line shows the range of the rated ambient temperature.

(注)斜線は定格周囲温度範囲を示す。

2. Values

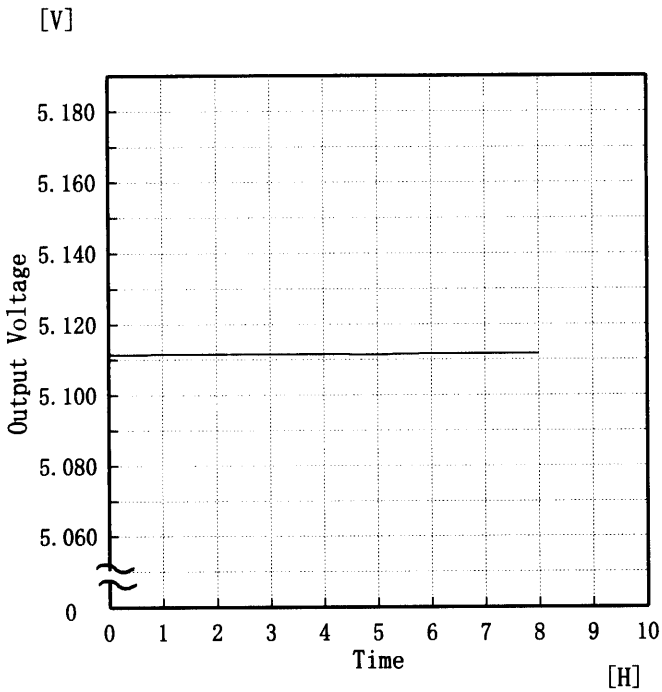
Ambient Temperature [℃]	Ripple Voltage [mV]	
	Load 50%	Load 100%
-20	40	60
-10	30	55
0	20	30
10	20	25
20	15	25
25	10	20
30	10	15
40	10	15
55	10	15
60	10	15
—	—	—

COSEL

Model	VAF1005
Item	Time Lapse Drift 経時ドリフト
Object	+5.0V2A

Temperature 25 ℃
Testing Circuitry Figure A

1. Graph



Input Volt. 100V
Load 100%

2. Values

Time since start [H]	Output Voltage [V]
0.0	5.112
0.5	5.111
1.0	5.111
2.0	5.111
3.0	5.112
4.0	5.112
5.0	5.111
6.0	5.112
7.0	5.112
8.0	5.112

COSEL

Model	VAF1005	
Item	Output Voltage Accuracy 定電圧精度	Testing Circuitry Figure A
Object	+5.0V2A	

Output Voltage Accuracy

This is defined as the value of the output voltage, regulation load, ambient temperature and input voltage varied at random in the range as specified below.

Temperature : -10～55 ℃
Input Voltage : 85～132 V
Load Current : 0～2A

* Output Voltage Accuracy = ±(Maximum of Output Voltage − Minimum of Output Voltage)／2

$$* \text{ Output Voltage Accuracy (Ration)} = \frac{\text{Output Voltage Accuracy}}{\text{Rated Output Voltage}} \times 100$$

定電圧精度

周囲温度、入力電圧、負荷を下記仕様内で、任意に変動させたときの出力電圧の変動をいう。

周囲温度 -10～55 ℃
入力電圧 85～132 V
負荷電流 0～2A

* 定電圧精度(変動値) ＝ ±(出力電圧の最高値−出力電圧の最低値)／2

$$* \text{ 定電圧精度(変動率)} = \frac{\text{変動値}}{\text{定格出力電圧}} \times 100$$

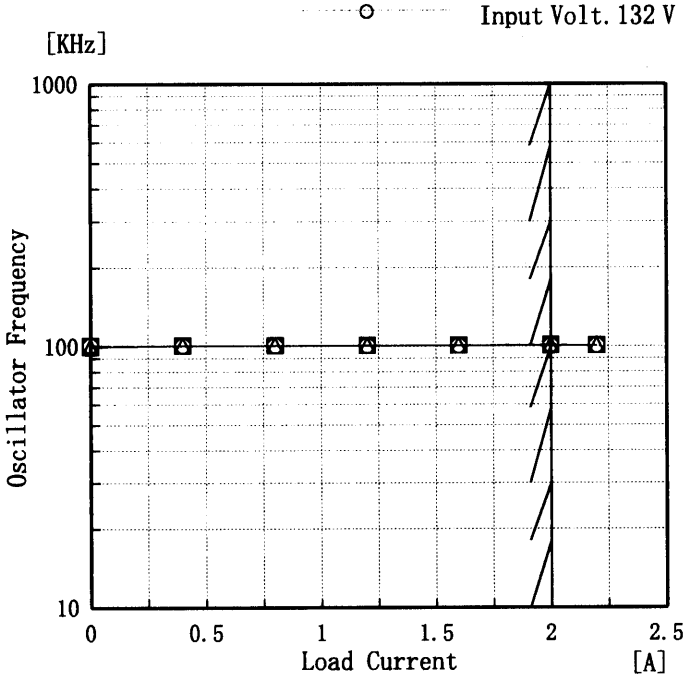
Item	Temperature [℃]	Input Voltage [V]	Output Current [A]	Output Voltage [V]	Output Voltage Accuracy [mV]	Output Voltage Accuracy(Ration) [%]
Maximum Voltage	-10	132	0	5.115	±5	±0.1
Minimum Voltage	55	100	2	5.106		

COSEL

Model	VAF1005
Item	Oscillator Frequency 発振周波数
Object	+5.0V2A

Temperature 25℃
Testing Circuitry Figure A

1. Graph



Note:Slanted line shows the range of the rated load current.

(注)斜線は定格負荷電流範囲を示す。

2. Values

Load Current [A]	Oscillator Frequency [KHz]		
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
0.0	100	101	101
0.4	101	101	101
0.8	101	101	101
1.2	101	101	101
1.6	101	101	101
2.0	101	101	101
2.2	101	101	101
—	—	—	—
—	—	—	—
—	—	—	—
—	—	—	—

COSEL

Model	VAF1005	
Item	Condensation 結露特性	Testing Circuitry Figure A
Object	+5.0V2A	

1. Condensation test

Testing procedure is as follows.

① Keeping and cooling the unit in a tank at -10℃ for an hour with the input off.

② Taking it out of the tank and dewing itself in a room where the temperature is 25℃ and the humidity is 40%RH.

③ Testing electrical characteristics of the unit to confirm there be no fault.

1．結露特性試験

入力を切った状態で、恒温槽で－１０℃に冷却しておき、約１時間後に恒温槽から取り出し、室温２５℃、湿度４０%RHの状態におき結露させ、その電気的特性の測定を行い、異常のないことを確認する。

2．Values

Item	Data	Testing Conditions
Output Voltage [V]	5.101	Input Volt.: 100V, Load Current:2A
Line Regulation [mV]	3	Input Volt.: 85～132V, Load Current:2A
Load Regulation [mV]	7	Input Volt.: 100V, Load Current:0～2A

COSEL

Model	VAF1005		
Item	Leakage Current 漏洩電流	Temperature Testing Circuitry	25℃ Figure B
Object	————————		

1. Results

Standards	Leakage Current [mA]		
	Input Volt. 85 [V]	Input Volt. 100 [V]	Input Volt. 132 [V]
(A)DENTORI	0.07	0.09	0.11
(B)IEC60950	0.07	0.09	0.11

Standards	Leakage Current [mA]		
	Input Volt. 170 [V]	Input Volt. 230 [V]	Input Volt. 264 [V]
(B)IEC60950	—	—	—

2. Condition

Leakage current value is concluded after measuring both phases of AC input and by choosing the larger one.

交流入力の両相について測定し、その大きい方を漏洩電流測定値とする。

COSEL

Model	VAF1005		
Item	Line Noise Tolerance 入力雑音耐量	Temperature Testing Circuitry	25℃ Figure C
Object	+5.0V2A		

1. Results

Pulse Width [nS]	MODE	No protection failure should occur 保護回路の誤動作がない	DC-like Regulation of Output Voltage 出力電圧の直流的変動
50	COMMON	OK	no fluctuation
	NORMAL	OK	no fluctuation
1000	COMMON	OK	no fluctuation
	NORMAL	OK	no fluctuation

2. Conditions

Input Voltage : 100 V
Pulse Voltage : 2000 V
Pulse Cycle : 10 mS
Pulse Input Duration : 1 min. or more
Load : 100 %

COSEL

Model	VAF1005		
Item	Conducted Emission 雑音端子電圧	Temperature Testing Circuitry	25℃ Figure D
Object	————————		

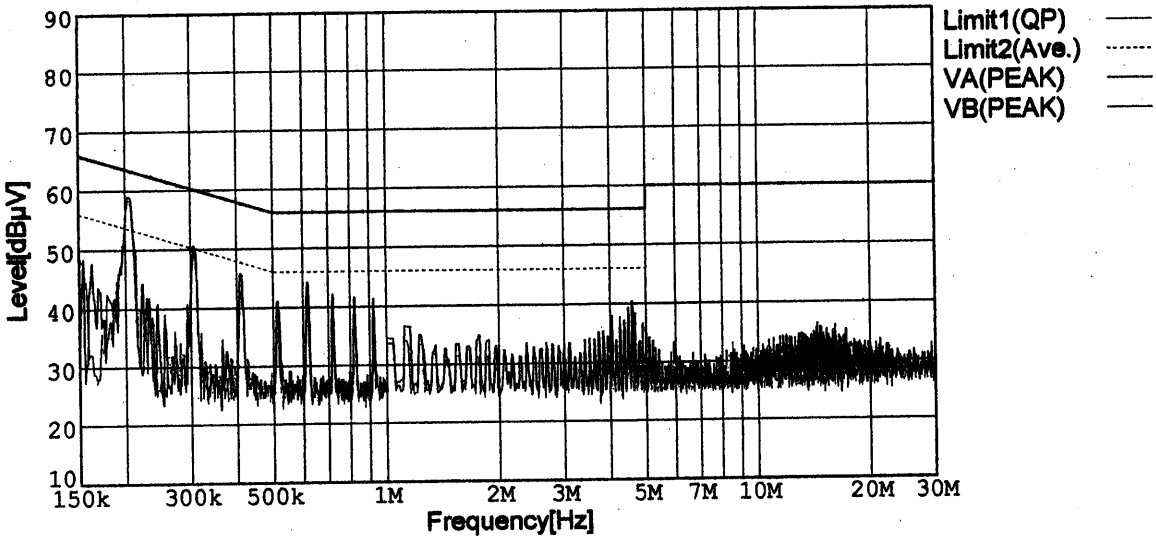
1. Graph

Remarks

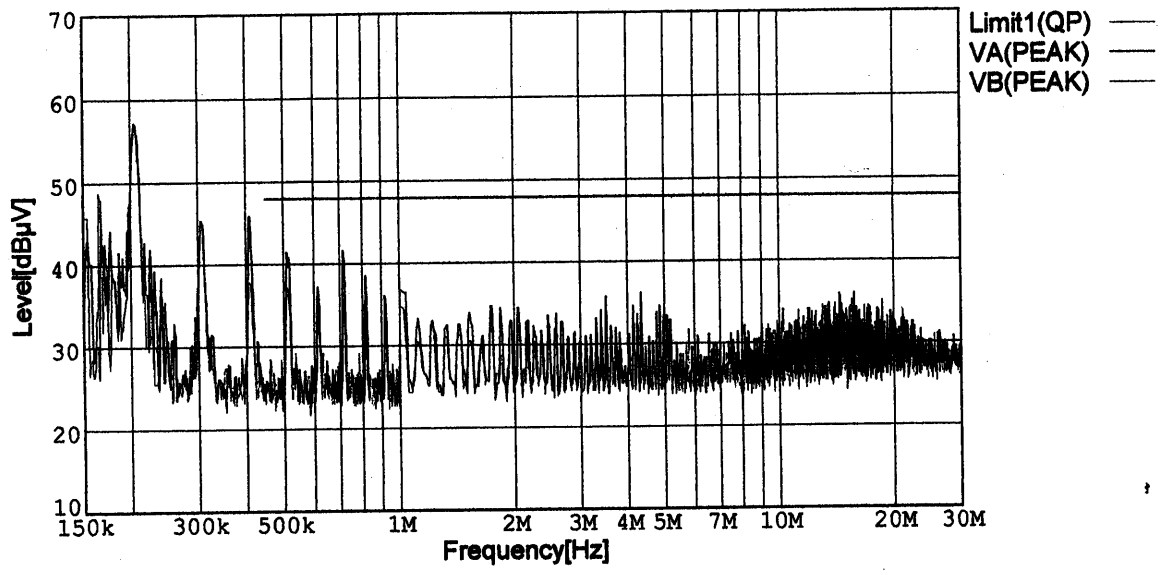
Input Volt. 100 V (VCCI Class B)
120 V (FCC Class B)

Load 100 %

Limit1: [VCCI] Class B(QP)
Limit2: [VCCI] Class B(Ave.)



Limit1: [FCC Part15] Class B



COSEL

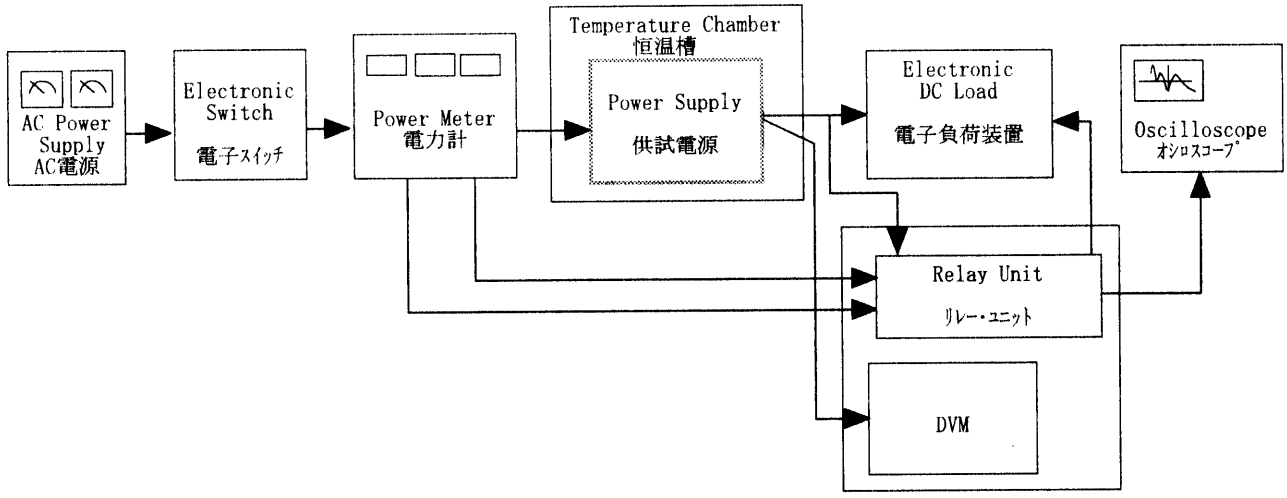


Figure A

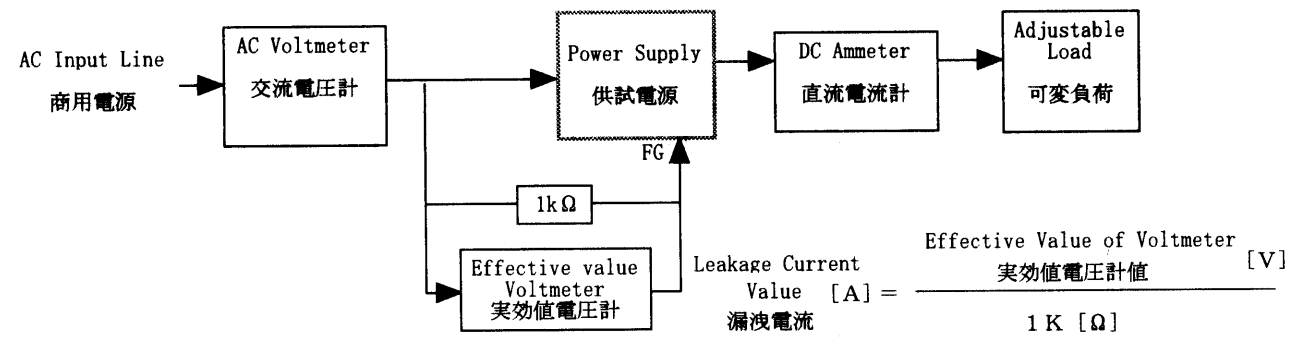


Figure B(DENTORI)

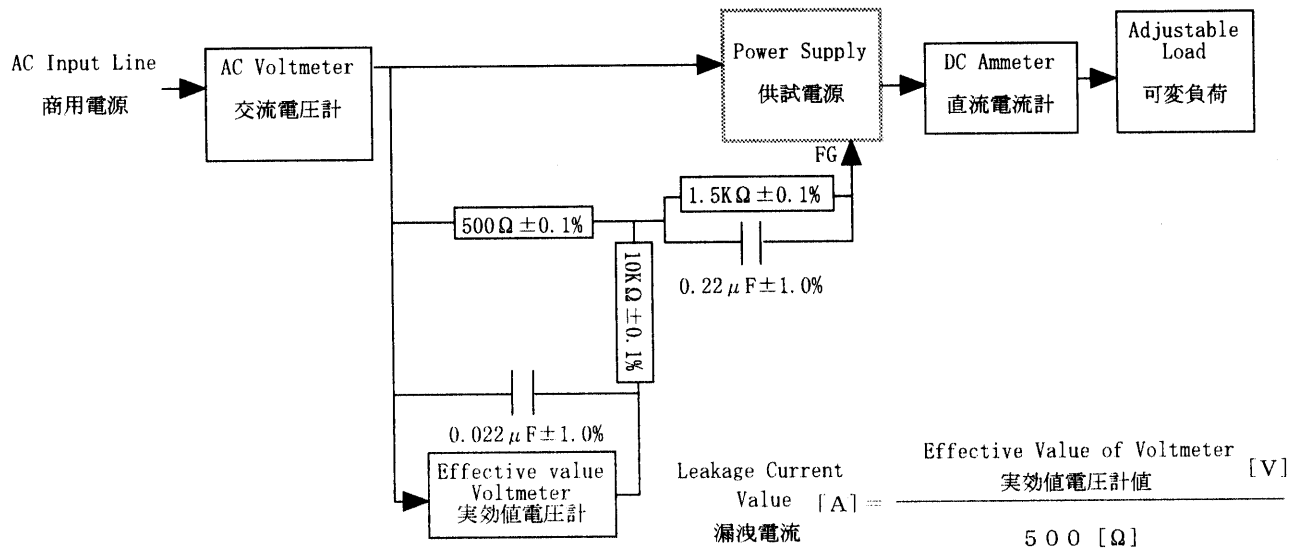


Figure B（IEC60950）

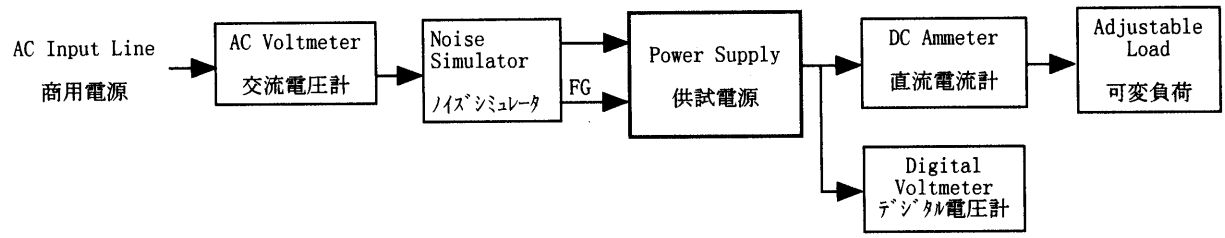


Figure C

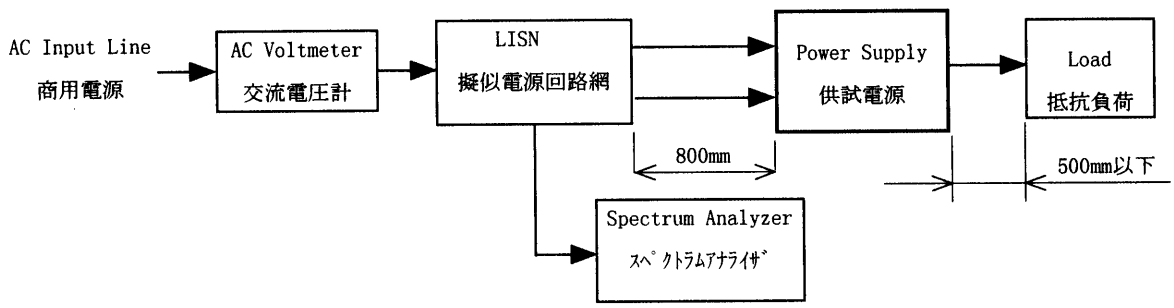


Figure D

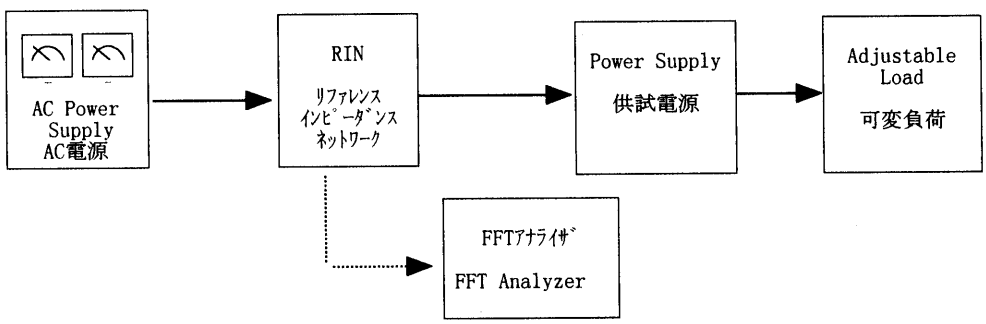


Figure E