

TEST DATA OF UMA120F-12-Y

Regulated DC Power Supply
November 6, 2024

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Design Manager

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Design Engineer

COSEL CO.,LTD.

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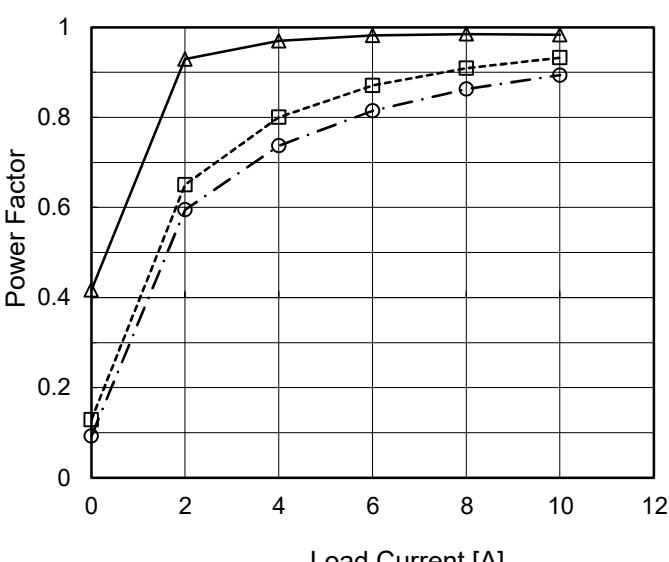
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Model		UMA120F-12-Y																																																																																																				
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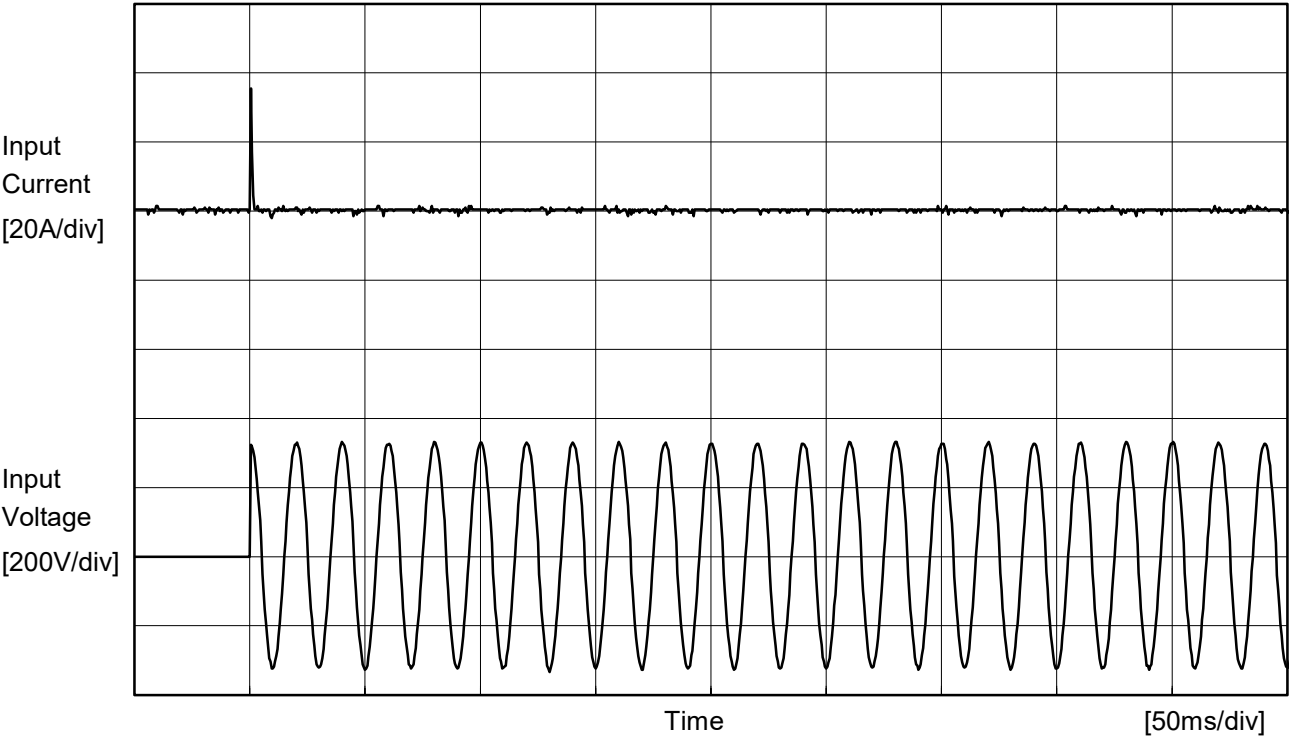
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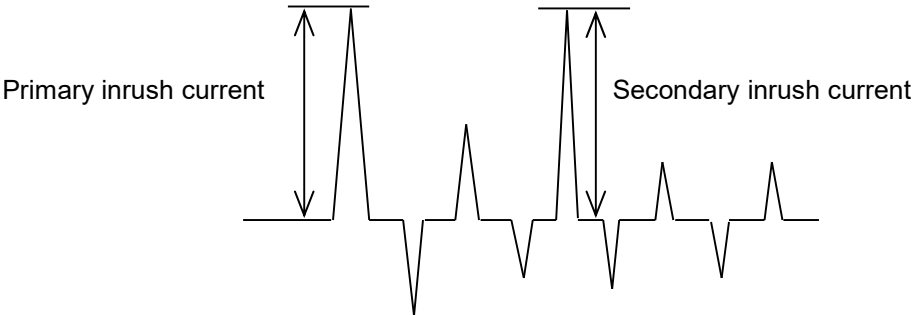
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Model		UMA120F-12-Y	Temperature 25°C Testing Circuitry Figure A
Item		Inrush Current	
Object		+12V10A	



Input Voltage	230 V
Frequency	50 Hz
Load	100 %
Primary inrush current	35.5 A
Secondary inrush current	4.5 A





		Temperature 25°C Testing Circuitry Figure C
Model	UMA120F-12-Y	
Item	Leakage Current	
Object	+12V10A	

1.Results

[mA]

Standards	Testing Circuitry	Measuring Method	Input Volt.			Note
			115 [V]	230 [V]	264 [V]	
IEC60601-1	Figure C-1	Both phases	0.05	0.10	0.12	Operation
		One of phases	0.09	0.20	0.23	Stand by
IEC62368-1	Figure C-2	Both phases	0.05	0.10	0.12	Operation
		One of phases	0.09	0.20	0.23	Stand by
	Figure C-3	Both phases	0.05	0.10	0.12	Operation
		One of phases	0.09	0.20	0.23	Stand by

The value for "One of phases" is the reference value only.

2.Condition

Leakage current value is concluded after measuring both phases of AC input and by choosing the larger one.

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Model		UMA120F-12-Y		Temperature 25°C																																																	
Item		Load Regulation		Testing Circuitry Figure A																																																	
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<div><div>Input Voltage230V</div><div>Load100%</div><div><div>20[mV/div]</div><div>20[ms/div]</div></div></div>																																																					

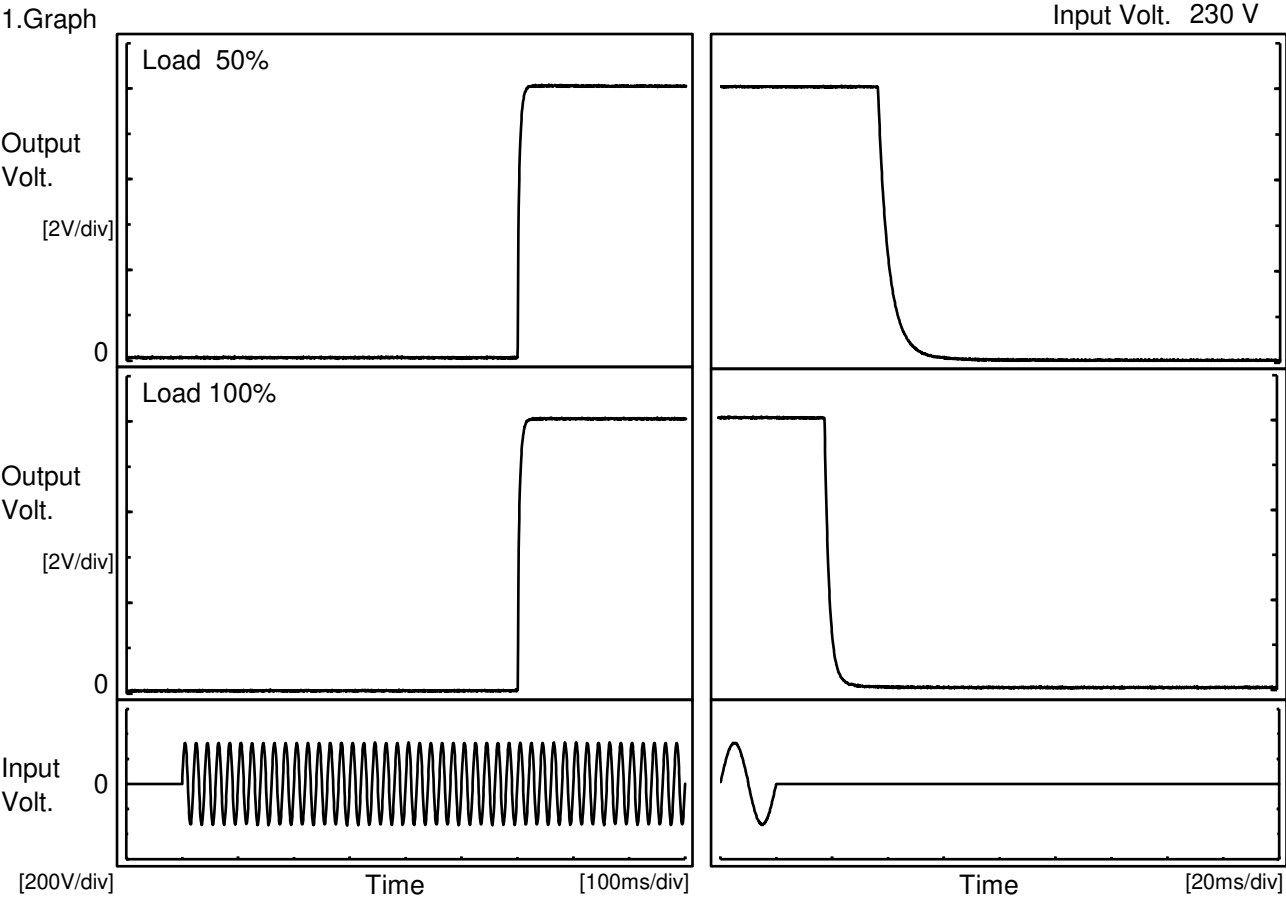
+12V10A

10 ms/div



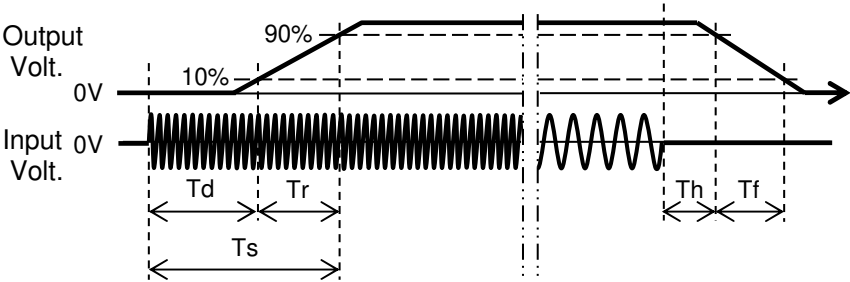
Model		UMA120F-12-Y	Temperature 25°C Testing Circuitry Figure A
Item		Rise and Fall Time	
Object		+12V10A	

1.Graph



2.Values

		[ms]				
Load	Time	Td	Tr	Ts	Th	Tf
50 %		600.0	7.0	607.0	36.6	8.9
100 %		600.0	7.0	607.0	18.3	3.8





Model	UMA120F-12-Y																																		
Item	Hold-Up Time	Temperature	25°C																																
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<div><div><div><div></div><div></div></div><div><div></div><div></div></div></div><div>Load 50%</div><div>Load 100%</div></div> <p>The graph shows Hold-Up Time [ms] on a logarithmic y-axis (1 to 1000) versus Input Voltage [V] on a linear x-axis (50 to 300). Two data series are plotted: Load 50% (dashed line with square markers) and Load 100% (solid line with triangle markers). The Load 50% series is relatively flat, ranging from approximately 36 ms to 37 ms. The Load 100% series is also flat, ranging from approximately 18 ms to 18 ms.</p> <table border="1"><thead><tr><th rowspan="2">Input Voltage [V]</th><th colspan="2">Hold-Up Time [ms]</th></tr><tr><th>Load 50%</th><th>Load 100%</th></tr></thead><tbody><tr><td>85</td><td>36</td><td>-</td></tr><tr><td>100</td><td>36</td><td>-</td></tr><tr><td>115</td><td>37</td><td>18</td></tr><tr><td>132</td><td>37</td><td>18</td></tr><tr><td>170</td><td>37</td><td>18</td></tr><tr><td>200</td><td>37</td><td>18</td></tr><tr><td>230</td><td>37</td><td>18</td></tr><tr><td>264</td><td>37</td><td>18</td></tr><tr><td>--</td><td>-</td><td>-</td></tr></tbody></table> <p>This duration covers from Shut-off of input voltage to the moment when output voltage descends to the rated range of voltage accuracy.</p>		Input Voltage [V]	Hold-Up Time [ms]		Load 50%	Load 100%	85	36	-	100	36	-	115	37	18	132	37	18	170	37	18	200	37	18	230	37	18	264	37	18	--	-	-		
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Load 100%

Hold-Up Time [ms]

1000

100

10

1

50

100

150

200

250

300

Input Voltage [V]

This duration covers from Shut-off of input voltage to the moment when output voltage descends to the rated range of voltage accuracy.

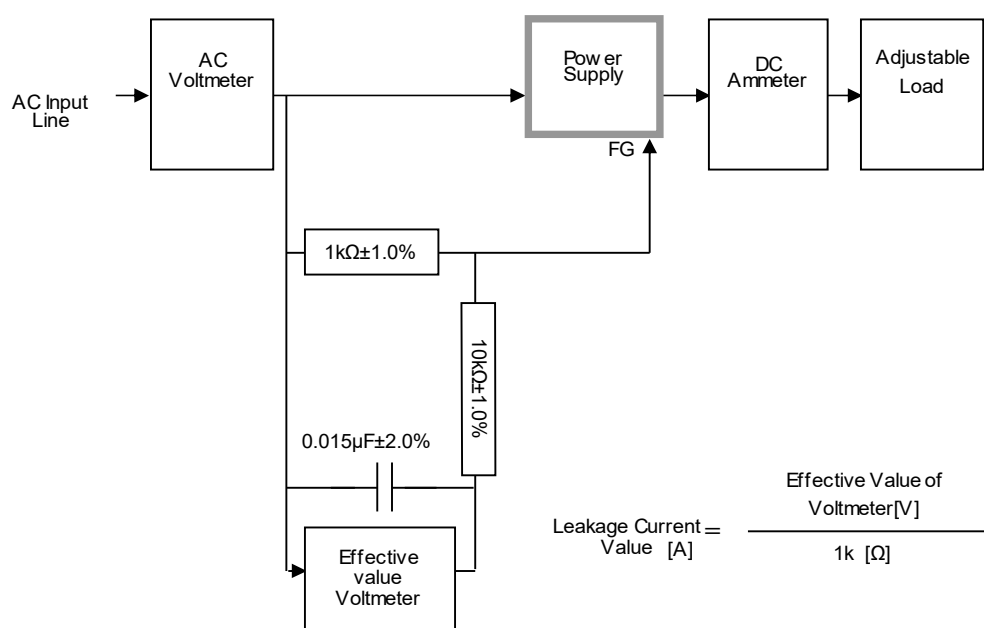
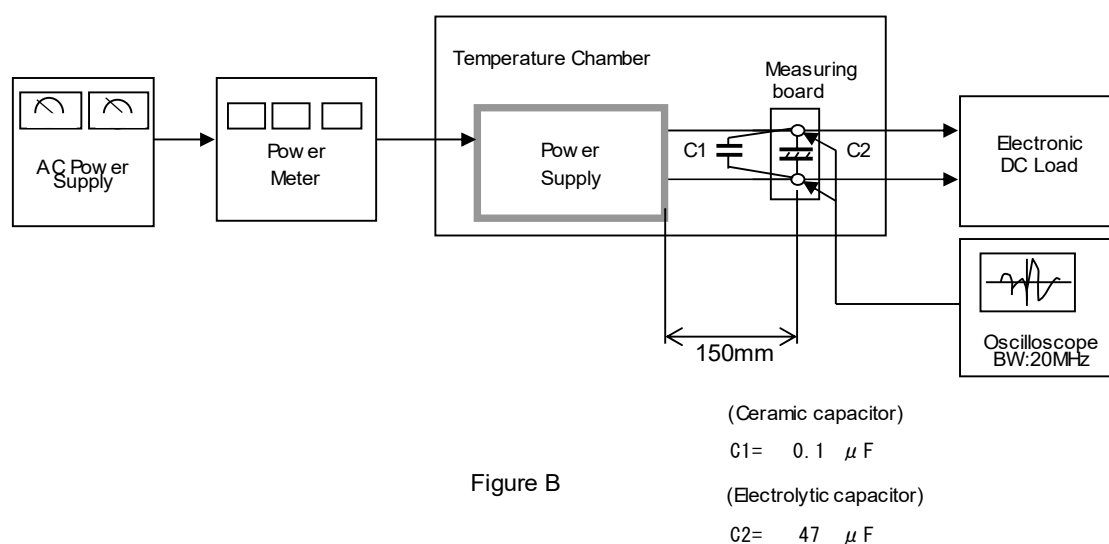
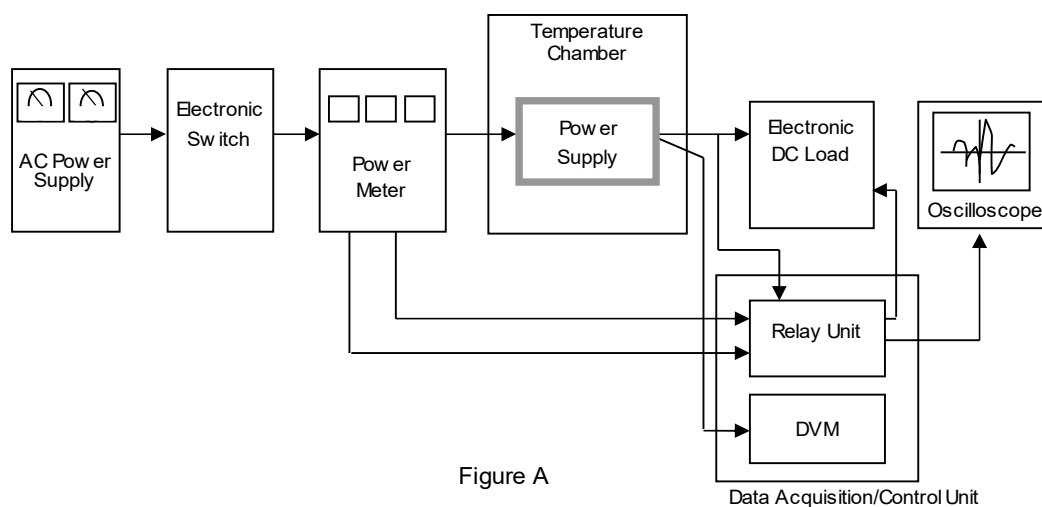


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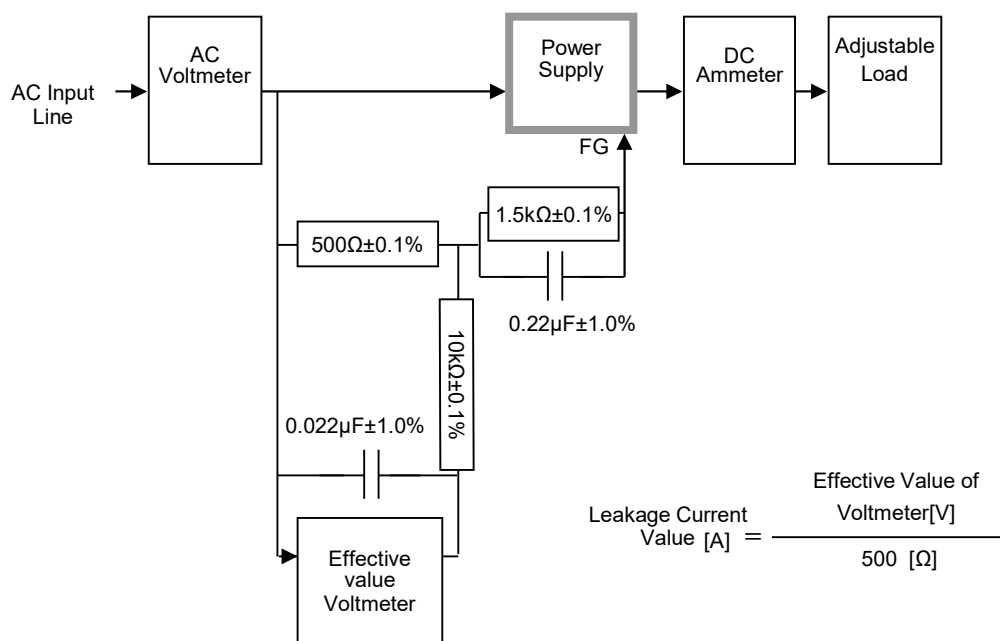


Figure C-2 (IEC62368-1 refer to IEC60990 Fig.4)

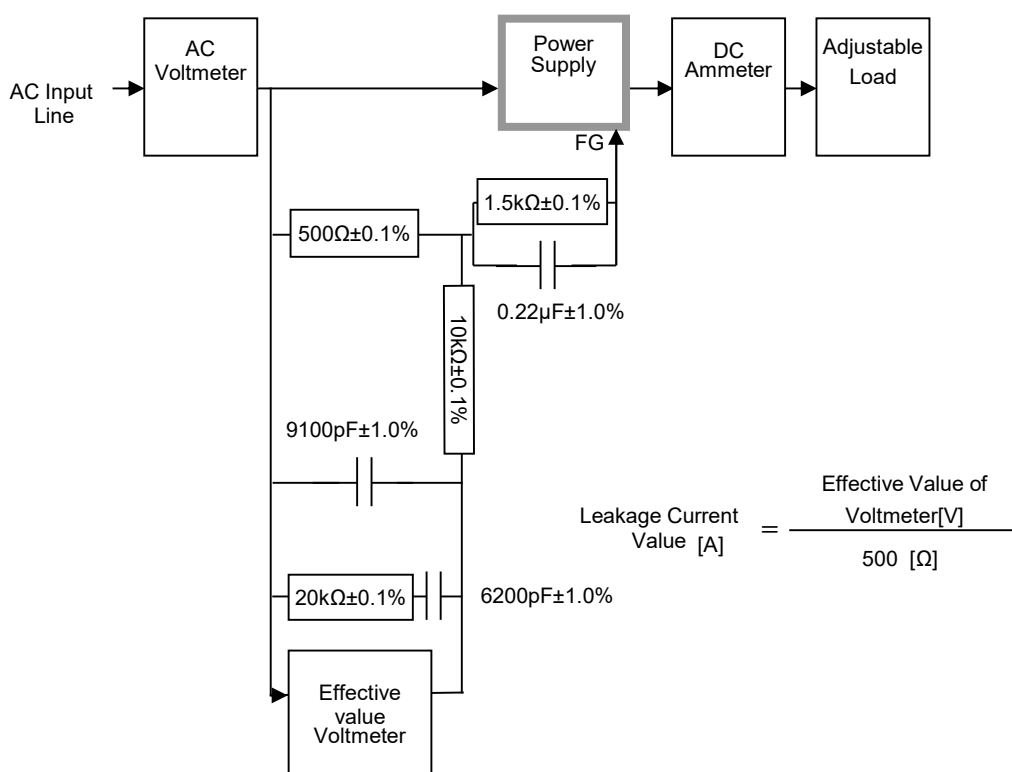


Figure C-3 (IEC62368-1 refer to IEC60990 Fig.5)