

TEST DATA OF TEPS20F24

Regulated DC Power Supply
February 20, 2025

Approved by : _____ Tetsuro Hirata

Design Manager

Prepared by : _____ Junichi Otsubo

Design Engineer

COSEL CO.,LTD.



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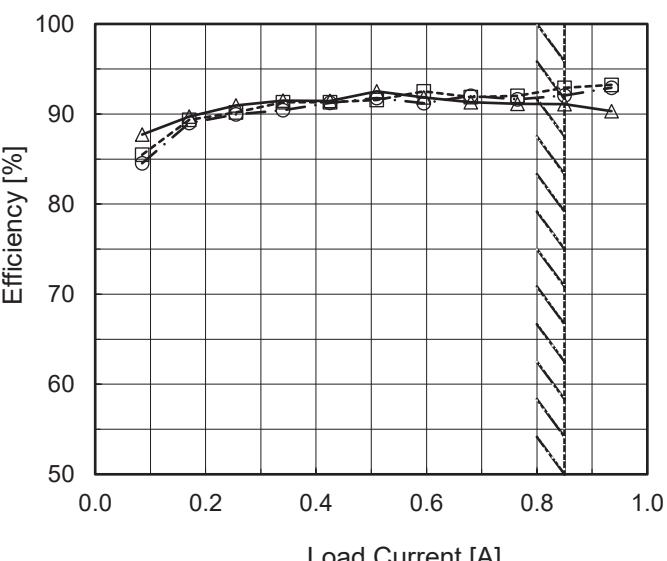
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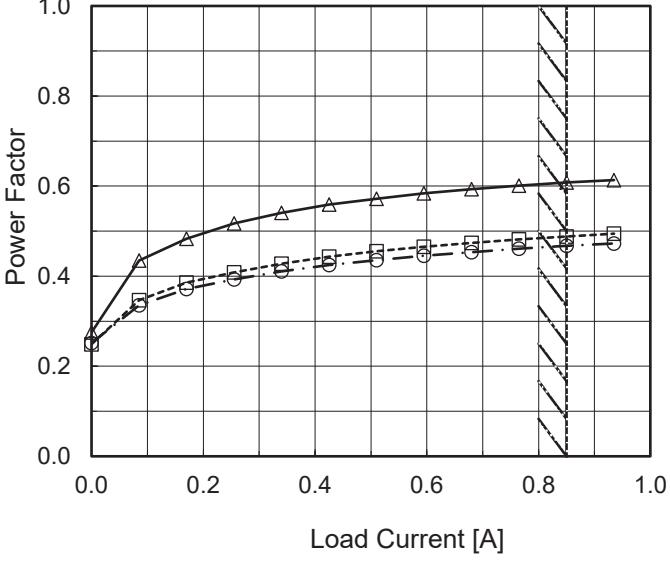
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<p>The graph shows the relationship between Input Current [A] on the Y-axis (0.00 to 0.50) and Load Current [A] on the X-axis (0.0 to 1.0). Three curves are plotted for different input voltages: 100V (solid line with open triangles), 200V (dashed line with open squares), and 230V (dash-dot line with open circles). All curves start at (0,0) and end at approximately (0.9, 0.41). A slanted line from the origin to (0.9, 0.41) represents the rated load current range.</p>		<table border="1"> <thead> <tr> <th rowspan="2">Load Current [A]</th> <th colspan="3">Input Current [A]</th> </tr> <tr> <th>Input Volt. 100[V]</th> <th>Input Volt. 200[V]</th> <th>Input Volt. 230[V]</th> </tr> </thead> <tbody> <tr><td>0.000</td><td>0.003</td><td>0.002</td><td>0.002</td></tr> <tr><td>0.085</td><td>0.056</td><td>0.036</td><td>0.033</td></tr> <tr><td>0.170</td><td>0.096</td><td>0.061</td><td>0.055</td></tr> <tr><td>0.255</td><td>0.132</td><td>0.084</td><td>0.077</td></tr> <tr><td>0.340</td><td>0.168</td><td>0.106</td><td>0.097</td></tr> <tr><td>0.425</td><td>0.203</td><td>0.128</td><td>0.116</td></tr> <tr><td>0.510</td><td>0.236</td><td>0.148</td><td>0.134</td></tr> <tr><td>0.595</td><td>0.271</td><td>0.168</td><td>0.154</td></tr> <tr><td>0.680</td><td>0.306</td><td>0.189</td><td>0.172</td></tr> <tr><td>0.765</td><td>0.340</td><td>0.209</td><td>0.191</td></tr> <tr><td>0.850</td><td>0.374</td><td>0.227</td><td>0.208</td></tr> <tr><td>0.935</td><td>0.409</td><td>0.246</td><td>0.224</td></tr> </tbody> </table>		Load Current [A]	Input Current [A]			Input Volt. 100[V]	Input Volt. 200[V]	Input Volt. 230[V]	0.000	0.003	0.002	0.002	0.085	0.056	0.036	0.033	0.170	0.096	0.061	0.055	0.255	0.132	0.084	0.077	0.340	0.168	0.106	0.097	0.425	0.203	0.128	0.116	0.510	0.236	0.148	0.134	0.595	0.271	0.168	0.154	0.680	0.306	0.189	0.172	0.765	0.340	0.209	0.191	0.850	0.374	0.227	0.208	0.935	0.409	0.246	0.224
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Note: Slanted line shows the range of the rated load current.

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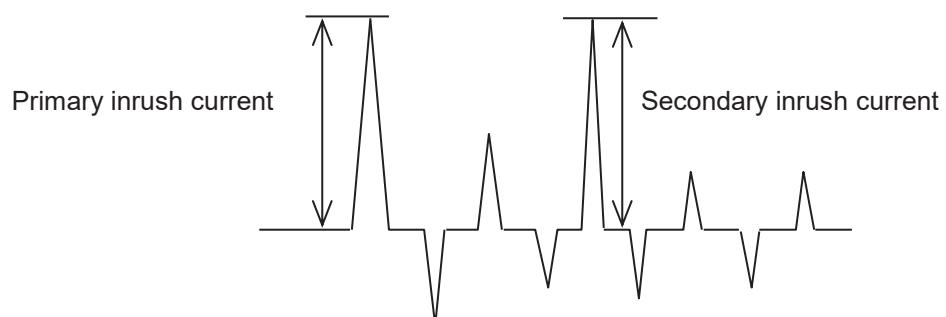
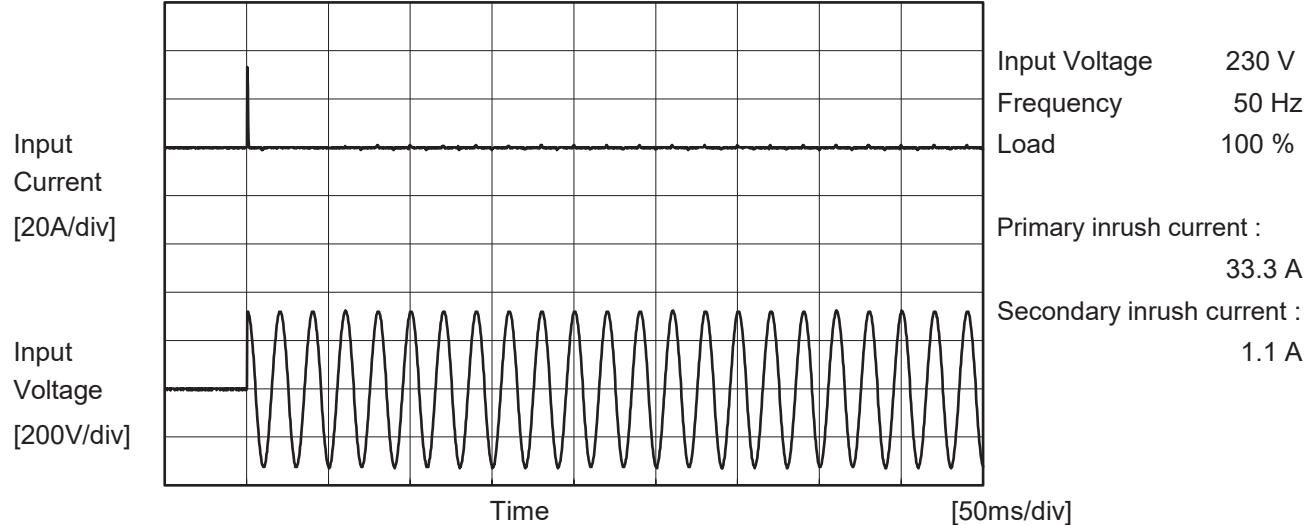
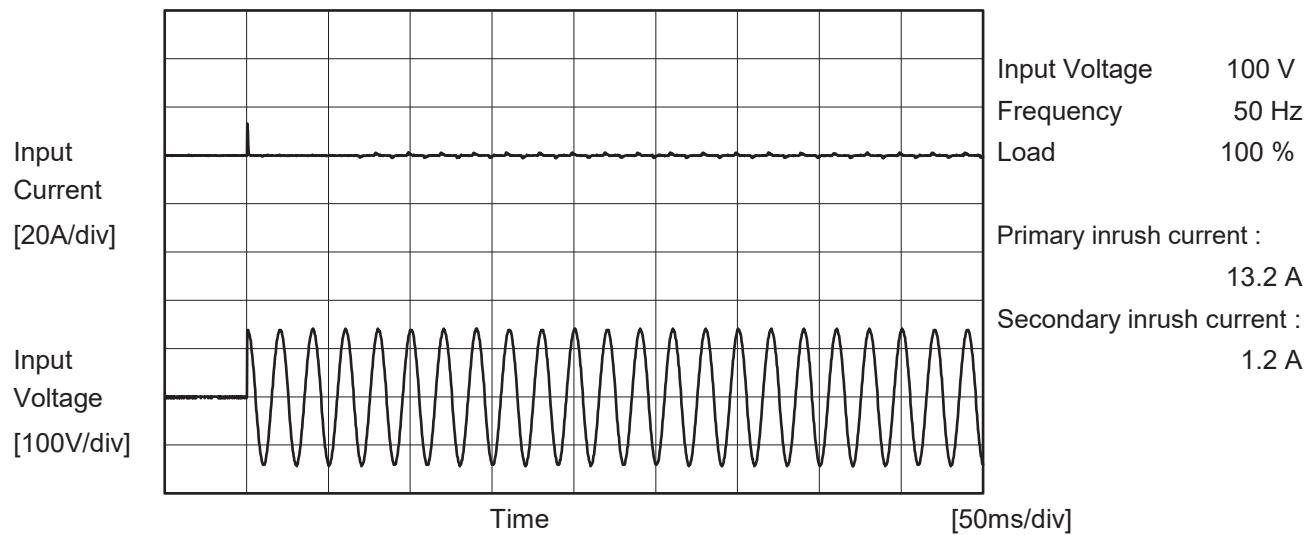
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Note: Slanted line shows the range of the rated load current.

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Model	TEPS20F24	Temperature	25°C
Item	Inrush Current	Testing Circuitry	Figure A
Object	_____		





Model	TEPS20F24	Temperature	25°C
Item	Leakage Current	Testing Circuitry	Figure C
Object	_____		

1. Results

[μ A]

Standards	Testing Circuitry	Measuring Method	Input Volt.			Note
			100 [V]	230 [V]	264 [V]	
DEN-AN	Figure C-1	Both phases	23	47	54	Operation
		One of phases	28	70	81	Stand by
IEC62368-1	Figure C-2	Both phases	19	44	52	Operation
		One of phases	28	69	80	Stand by
	Figure C-3	Both phases	19	45	52	Operation
		One of phases	28	69	81	Stand by

The value for "One of phases" is the reference value only.

2. Condition

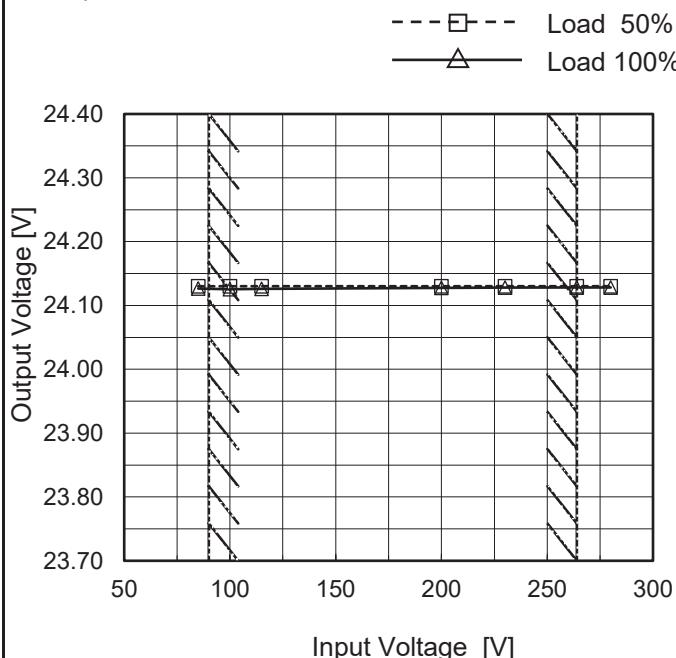
Leakage current value is concluded after measuring both phases of AC input and by choosing the larger one.

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Model	TEPS20F24
Item	Line Regulation
Object	+24V0.85A

 Temperature 25°C
 Testing Circuitry Figure A

1.Graph

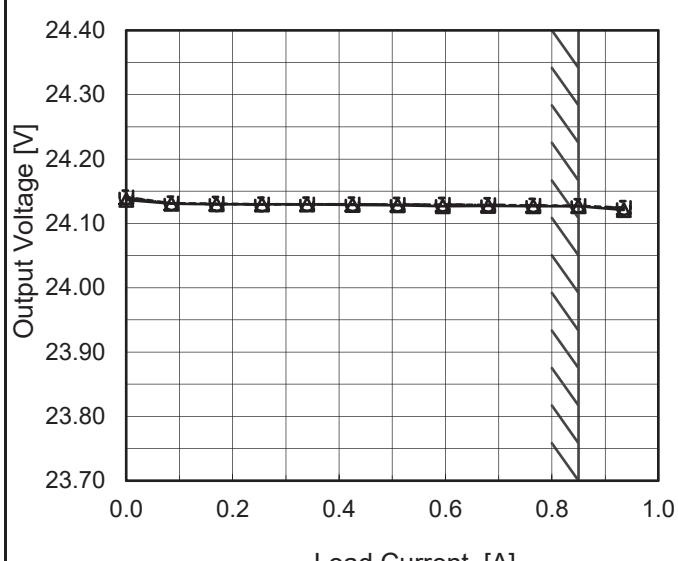
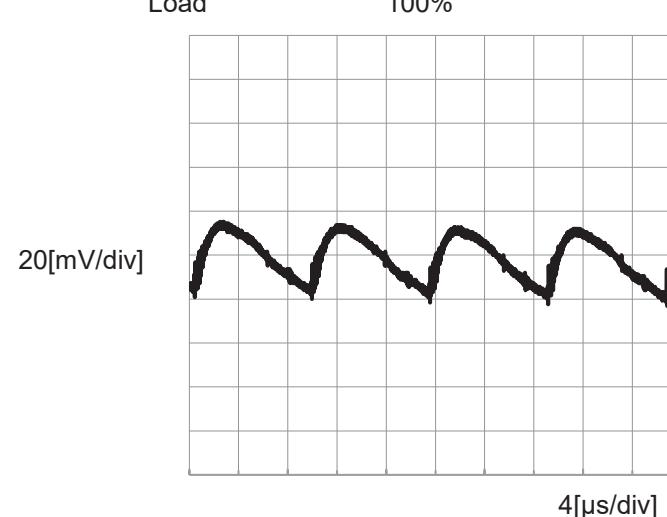


2.Values

Input Voltage [V]	Output Voltage [V]	
	Load 50%	Load 100%
85	24.130	24.127
100	24.130	24.125
115	24.130	24.126
200	24.130	24.128
230	24.130	24.128
264	24.130	24.128
280	24.130	24.128
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Note: Slanted line shows the range of the rated input voltage.

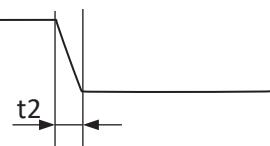
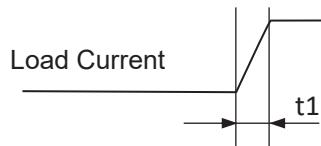
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Item	Ripple-Noise	Temperature	25°C																																																							
Object	+24V0.85A	Testing Circuitry	Figure B																																																							
1.Graph	<p>Input Voltage 230V Load 100%</p> 																																																									

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Model	TEPS20F24	Temperature Testing Circuitry	25°C Figure A
Item	Dynamic Load Response		
Object	+24V0.85A		

Input Volt. 230 V
 Cycle 1000 ms

Response. $t_1=t_2=50\mu\text{s}$. Typ

Load 0%(0A) \longleftrightarrow
 Load 100%(0.85A)

200[mV/div]

1[ms/div]

10[ms/div]

Load 50%(0.425A) \longleftrightarrow
 Load 100%(0.85A)

200[mV/div]

1[ms/div]

10[ms/div]

Load 0%(0A) \longleftrightarrow
 Load 50%(0.425A)

200[mV/div]

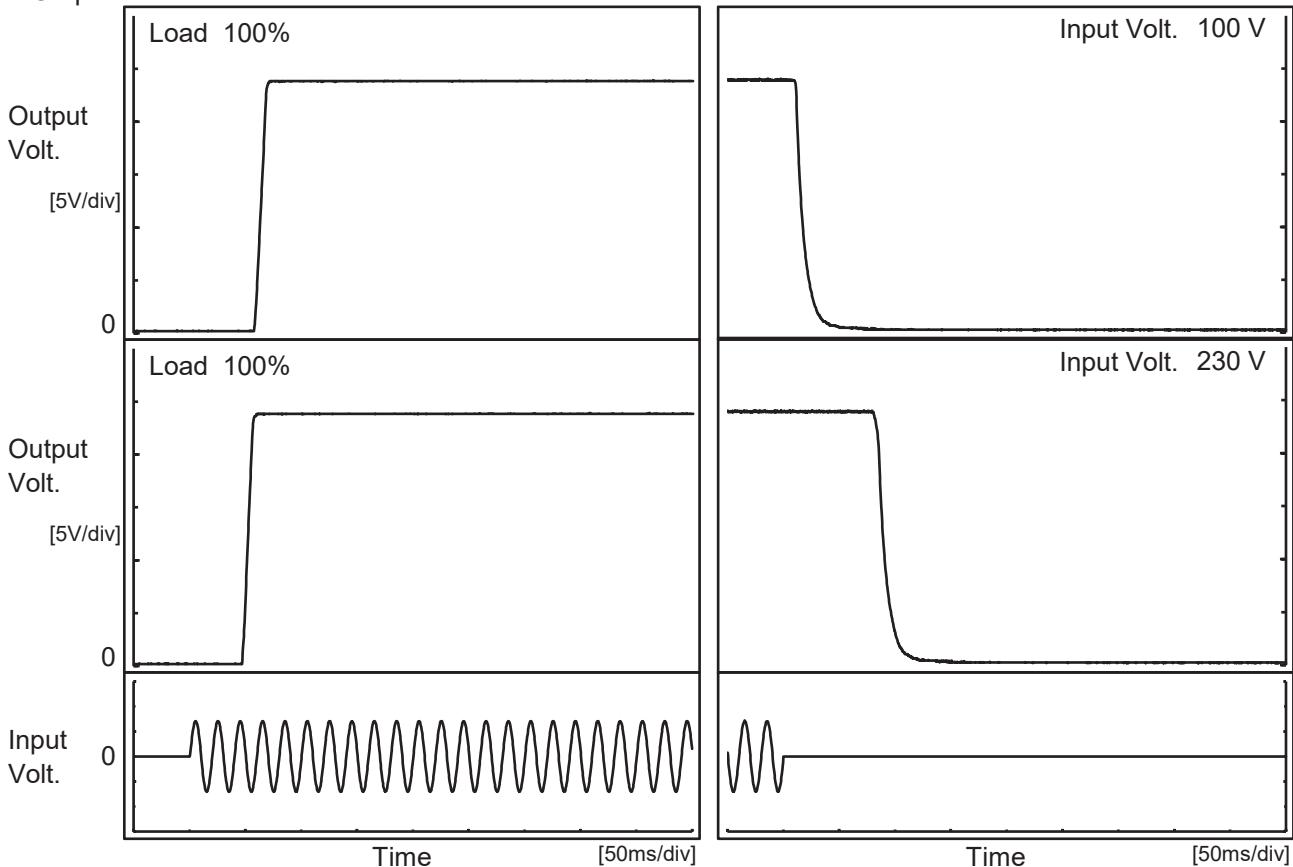
1[ms/div]

10[ms/div]

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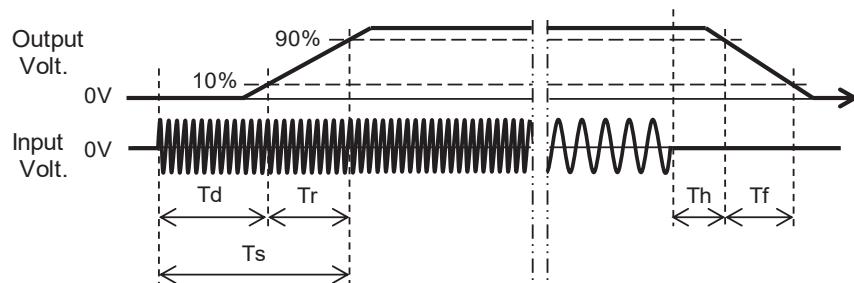
Model	TEPS20F24	Temperature	25°C
Item	Rise and Fall Time	Testing Circuitry	Figure A
Object	+24V0.85A		

1. Graph



2. Values

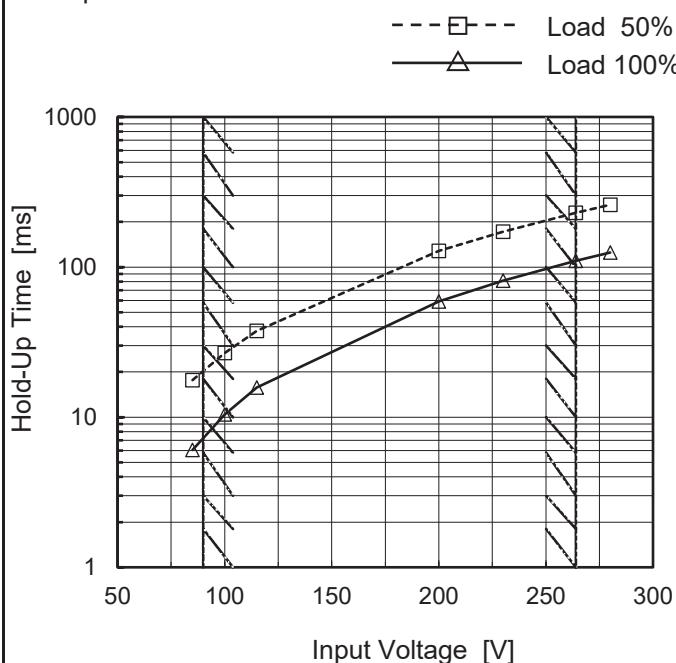
Input Volt.	Time	Td	Tr	Ts	Th	Tf	[ms]
100V		59.3	9.0	68.3	12.0	16.3	
230V		48.3	8.0	56.3	84.5	17.0	



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Model	TEPS20F24	Temperature	25°C
Item	Hold-Up Time	Testing Circuitry	Figure A
Object	+24V0.85A		

1.Graph



2.Values

Input Voltage [V]	Hold-Up Time [ms]	
	Load 50%	Load 100%
85	18	6
100	27	10
115	38	16
200	128	59
230	172	81
264	230	110
280	260	125
--	-	-
--	-	-

This duration covers from Shut-off of input voltage to the moment when output voltage descends to the rated range of voltage accuracy.
Note: Slanted line shows the range of the rated input voltage.

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Note: Slanted line shows the range of the rated load current.

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Model	TEPS20F24	Temperature	25°C																																																							
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Model	TEPS20F24	Testing Circuitry Figure A
Item	Ambient Temperature Drift	
Object	+24V0.85A	

1.Values

Load 100%

Ambient Temperature[°C]	Output Voltage [V]		
	Input Volt. 100V	Input Volt. 200V	Input Volt. 230V
-20	24.061	24.062	24.062
25	24.105	24.108	24.108
60	24.112	24.115	24.116

Item	Minimum Input Voltage for Regulated Output Voltage	Testing Circuitry Figure A
Object	+24V0.85A	

1.Values

Ambient Temperature[°C]	Input Voltage [V]	
	Load 50%	Load 100%
-20	38	58
25	38	55
60	37	55

Item	Overvoltage Protection	Testing Circuitry Figure A
Object	+24V0.85A	

1.Values

Load 0%

Ambient Temperature[°C]	Operating Point [V]	
	Input Volt. 100V	Input Volt. 230V
-20	30.60	30.65
25	31.33	31.33
60	32.35	32.20

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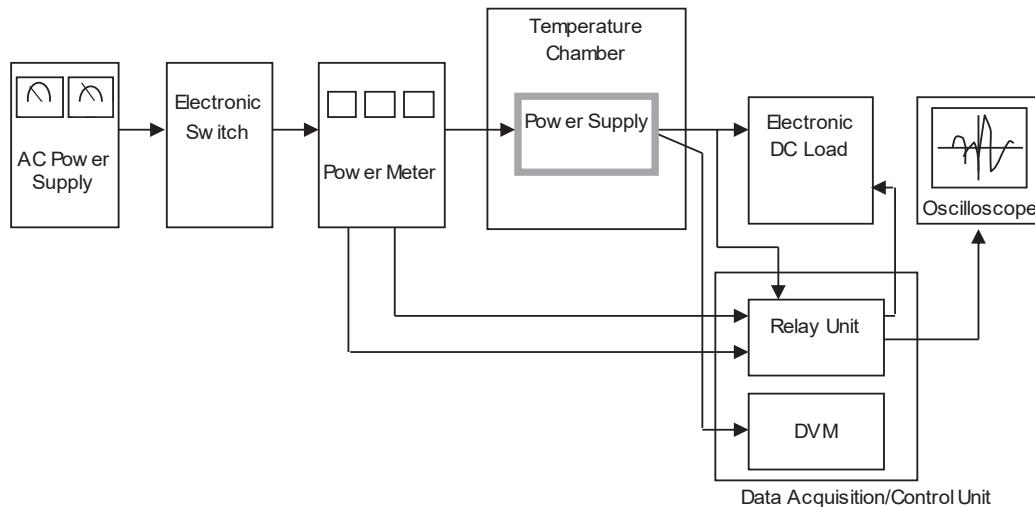


Figure A

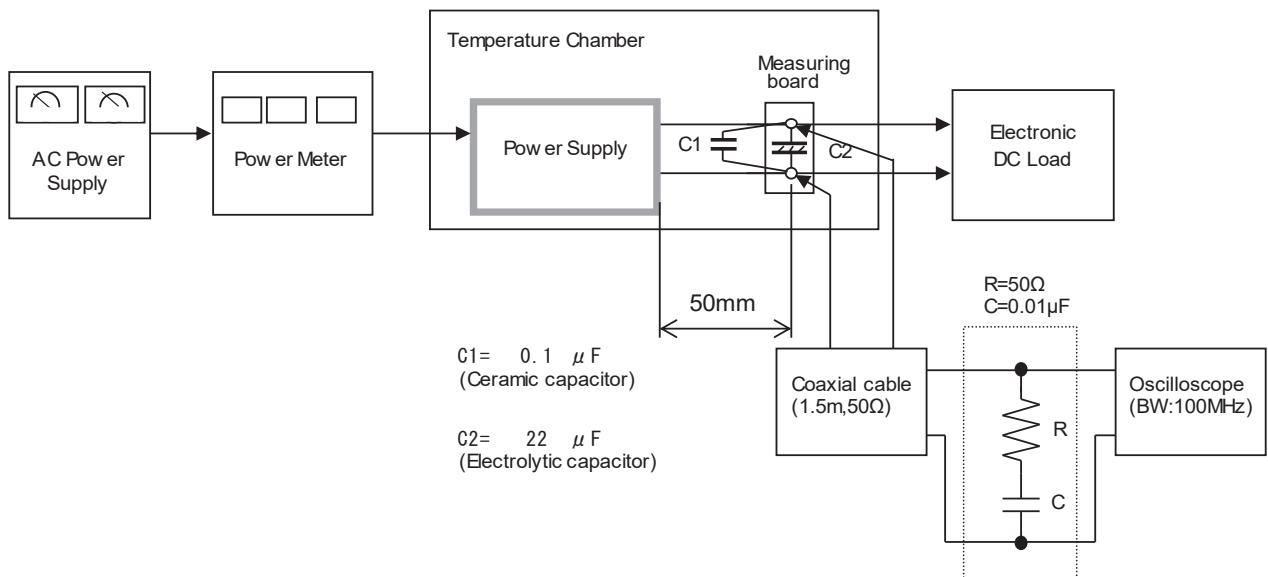


Figure B

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