

TEST DATA OF TECS10F-5

Regulated DC Power Supply
February 28, 2025

Approved by : _____ Tetsuro Hirata

Design Manager

Prepared by : _____ Junichi Otsubo

Design Engineer

COSEL CO.,LTD.

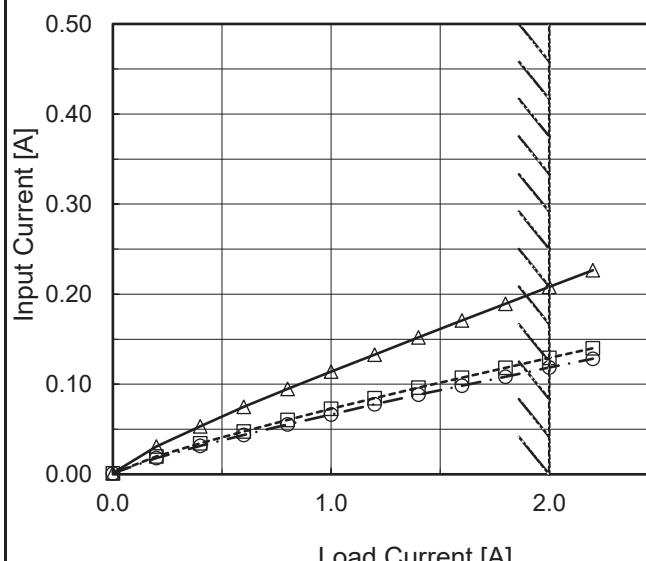


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(Final Page 15)

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Model	TECS10F-5	Temperature	25°C																																																							
Item	Input Current (by Load Current)	Testing Circuitry	Figure A																																																							
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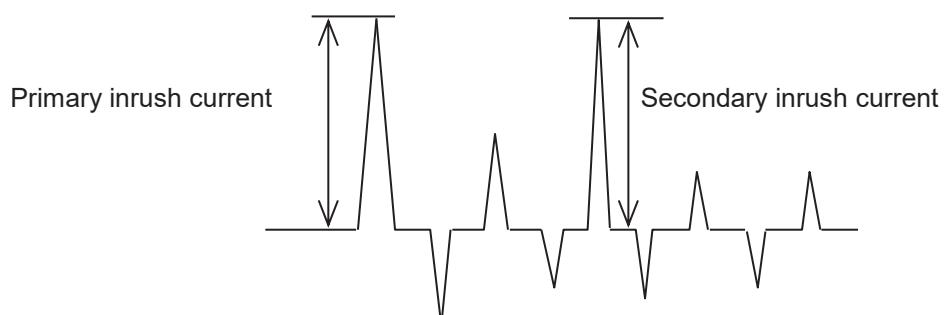
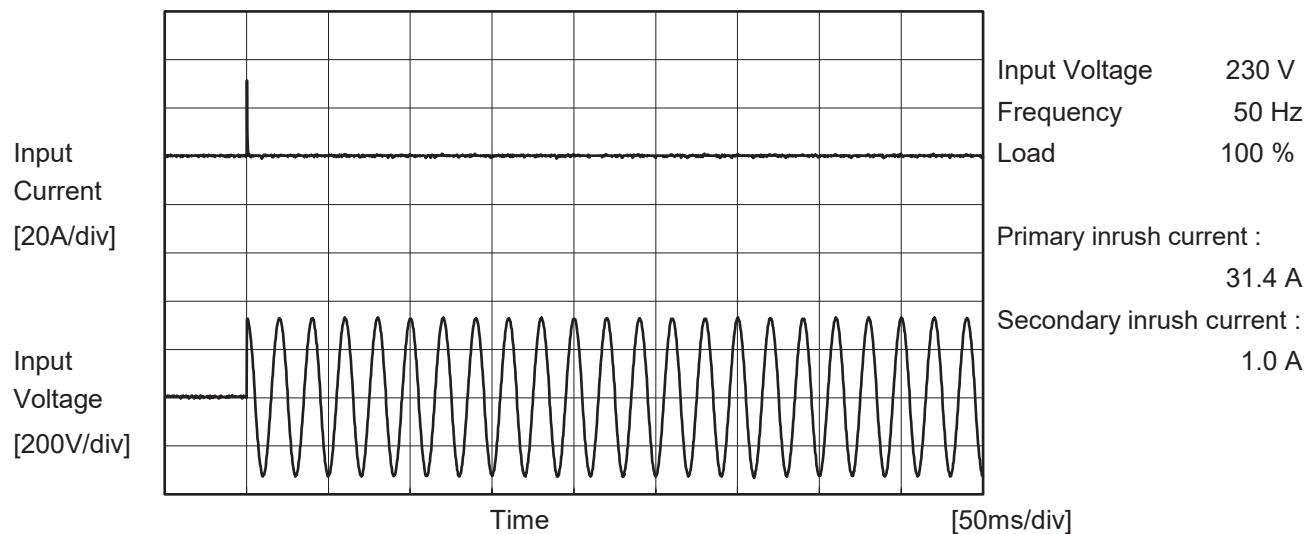
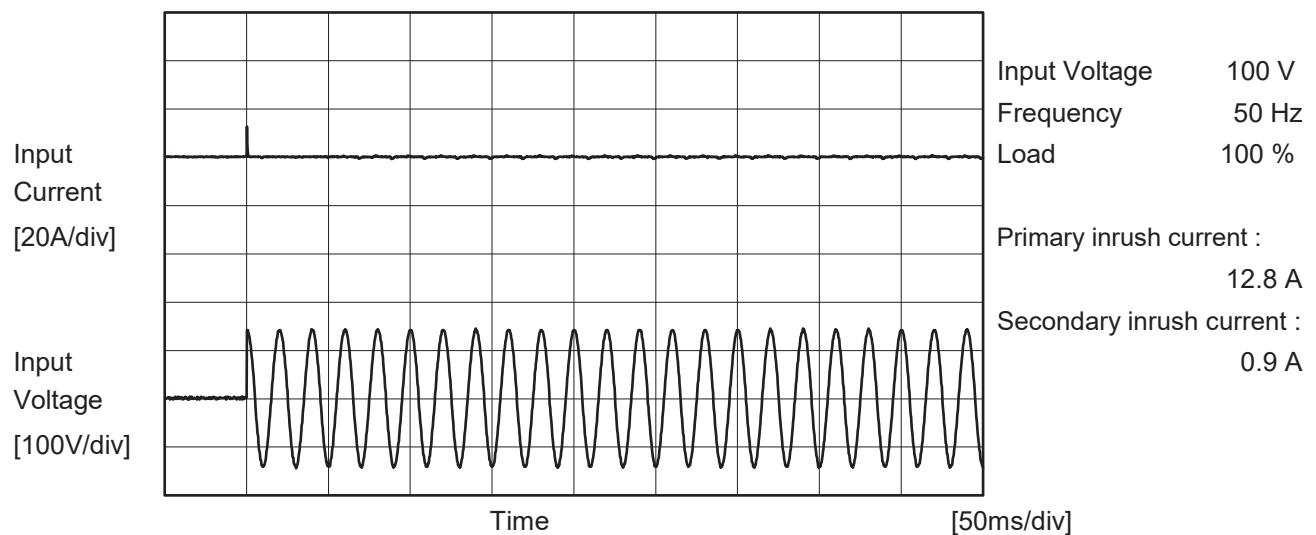
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Model	TECS10F-5	Temperature Testing Circuitry Figure A	25°C
Item	Inrush Current		
Object	_____		





Model	TECS10F-5	Temperature Testing Circuitry	25°C Figure C
Item	Leakage Current		
Object	<hr/>		

1. Results

[μ A]

Standards	Testing Circuitry	Measuring Method	Input Volt.			Note
			100 [V]	230 [V]	264 [V]	
DEN-AN	Figure C-1	Both phases	24	47	53	Operation
		One of phases	28	69	81	Stand by
IEC62368-1	Figure C-2	Both phases	19	44	52	Operation
		One of phases	28	69	81	Stand by
	Figure C-3	Both phases	19	45	52	Operation
		One of phases	28	69	81	Stand by

The value for "One of phases" is the reference value only.

2. Condition

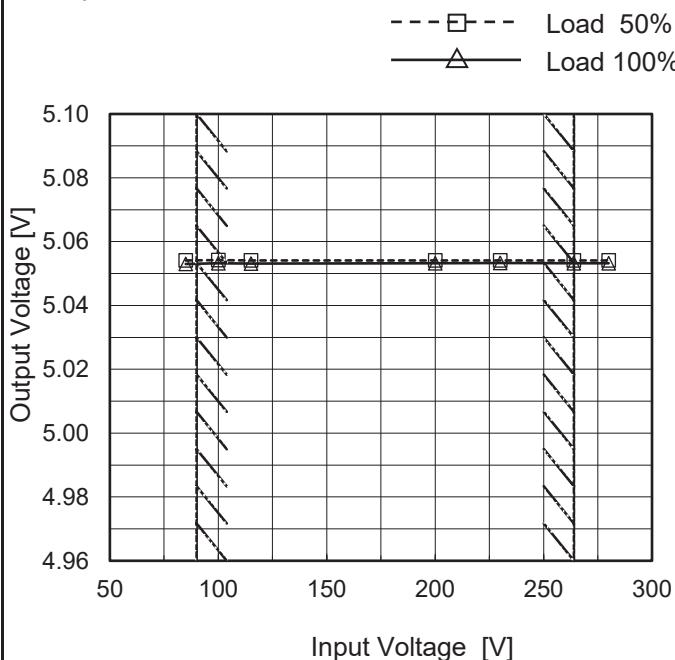
Leakage current value is concluded after measuring both phases of AC input and by choosing the larger one.

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Model	TECS10F-5
Item	Line Regulation
Object	+5V2A

 Temperature 25°C
 Testing Circuitry Figure A

1.Graph



2.Values

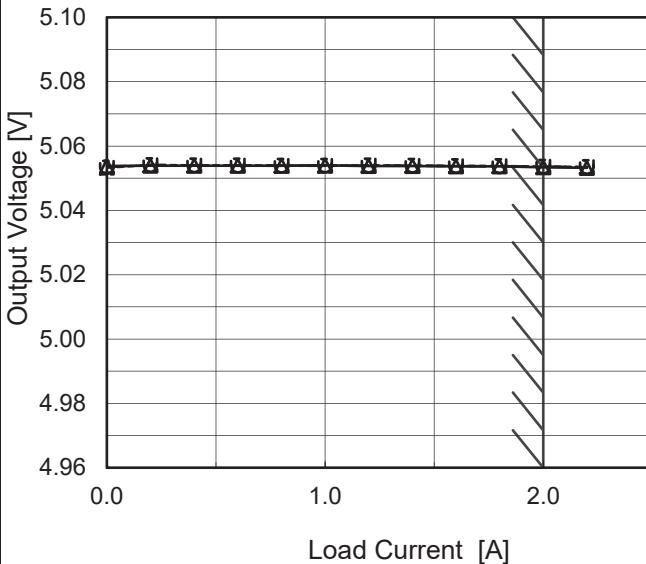
Input Voltage [V]	Output Voltage [V]	
	Load 50%	Load 100%
85	5.054	5.053
100	5.054	5.053
115	5.054	5.053
200	5.054	5.053
230	5.054	5.053
264	5.054	5.053
280	5.054	5.053
--	-	-
--	-	-

Note: Slanted line shows the range of the rated input voltage.

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Model	TECS10F-5
Item	Load Regulation
Object	+5V2A

- 1.Graph
- △— Input Volt. 100V
 - -□--- Input Volt. 200V
 - ·○--- Input Volt. 230V



Note: Slanted line shows the range of the rated load current.

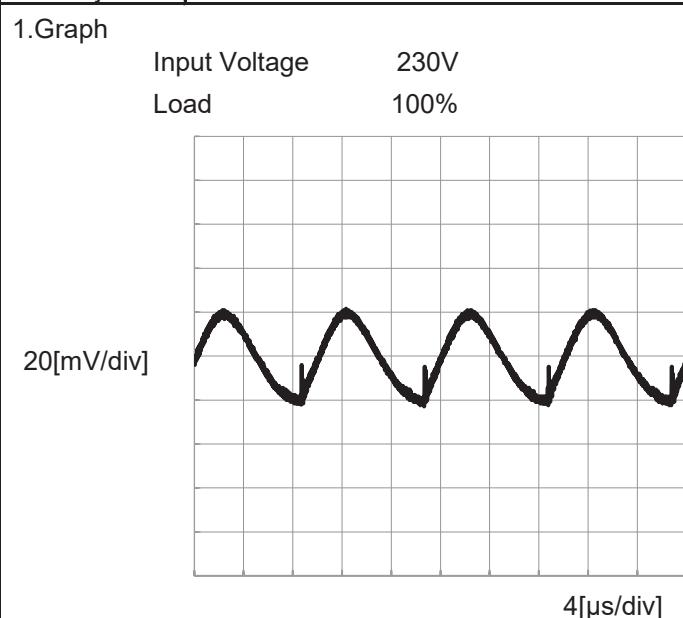
Temperature 25°C
Testing Circuitry Figure A

2.Values

Load Current [A]	Output Voltage [V]		
	Input Volt. 100[V]	Input Volt. 200[V]	Input Volt. 230[V]
0.0	5.054	5.053	5.054
0.2	5.054	5.054	5.054
0.4	5.054	5.054	5.054
0.6	5.054	5.054	5.054
0.8	5.054	5.054	5.054
1.0	5.054	5.054	5.054
1.2	5.054	5.054	5.054
1.4	5.054	5.054	5.054
1.6	5.054	5.054	5.054
1.8	5.054	5.054	5.054
2.0	5.053	5.054	5.054
2.2	5.053	5.053	5.054

Item	Ripple-Noise
Object	+5V2A

Temperature 25°C
Testing Circuitry Figure B



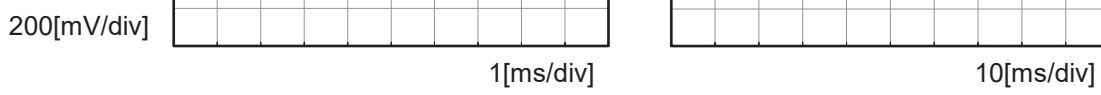
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Model	TECS10F-5	Temperature Testing Circuitry Figure A	25°C
Item	Dynamic Load Response		
Object	+5V2A		

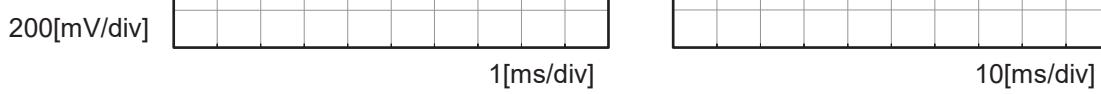
Input Volt. 230 V Response. $t_1=t_2=50\mu\text{s}$. Typ
 Cycle 1000 ms



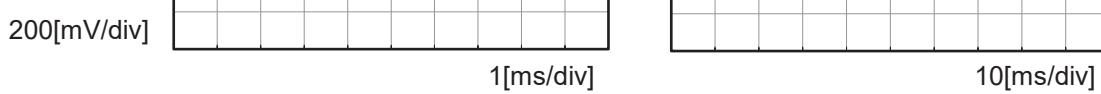
Load 0%(0A) \longleftrightarrow
 Load 100%(2A)



Load 50%(1A) \longleftrightarrow
 Load 100%(2A)



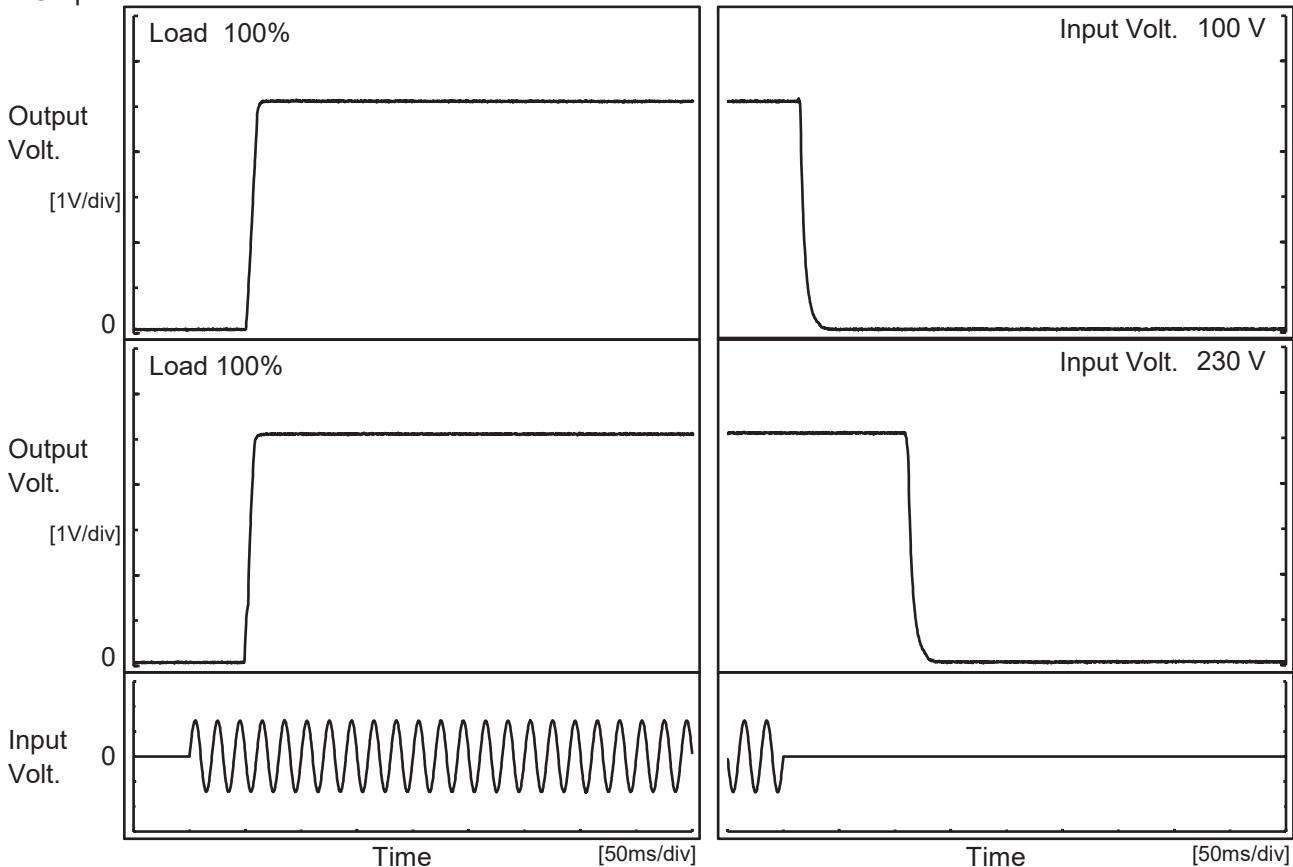
Load 0%(0A) \longleftrightarrow
 Load 50%(1A)



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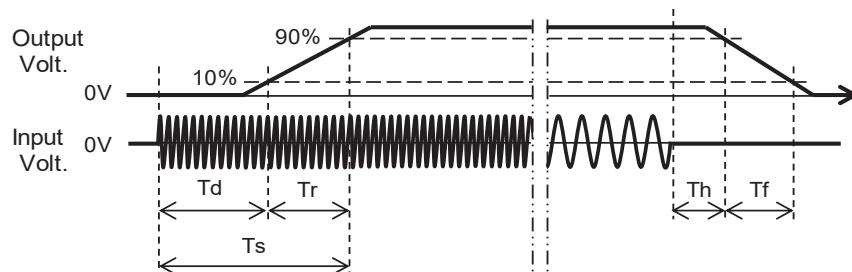
Model	TECS10F-5	Temperature	25°C
Item	Rise and Fall Time	Testing Circuitry	Figure A
Object	+5V2A		

1. Graph



2. Values

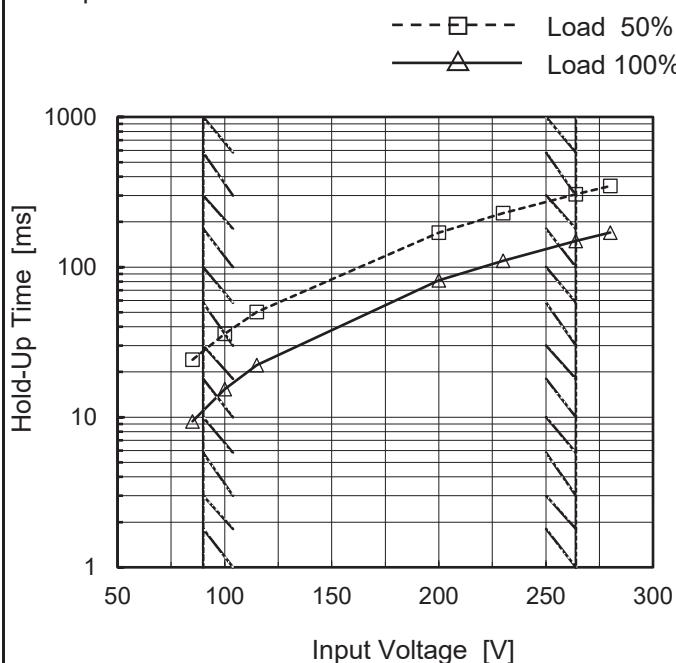
Input Volt.	Time	Td	Tr	Ts	Th	Tf	[ms]
100V		51.5	8.5	60.0	15.8	9.5	
230V		49.5	7.8	57.3	112.0	9.8	



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Model	TECS10F-5	Temperature	25°C
Item	Hold-Up Time	Testing Circuitry	Figure A
Object	+5V2A		

1.Graph



2.Values

Input Voltage [V]	Hold-Up Time [ms]	
	Load 50%	Load 100%
85	24	9
100	36	15
115	50	22
200	170	82
230	228	110
264	306	149
280	347	170
--	-	-
--	-	-

This duration covers from Shut-off of input voltage to the moment when output voltage descends to the rated range of voltage accuracy.
Note: Slanted line shows the range of the rated input voltage.

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Model	TECS10F-5	Temperature Testing Circuitry	25°C Figure A																																																							
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<p>Output Voltage [V]</p> <p>Load Current [A]</p> <p>Note: Slanted line shows the range of the rated load current.</p>		<table border="1"> <thead> <tr> <th rowspan="2">Output Voltage [V]</th> <th colspan="3">Load Current [A]</th> </tr> <tr> <th>Input Volt. 100[V]</th> <th>Input Volt. 200[V]</th> <th>Input Volt. 230[V]</th> </tr> </thead> <tbody> <tr><td>5.00</td><td>3.36</td><td>3.13</td><td>3.31</td></tr> <tr><td>4.75</td><td>-</td><td>-</td><td>-</td></tr> <tr><td>4.50</td><td>-</td><td>-</td><td>-</td></tr> <tr><td>4.00</td><td>-</td><td>-</td><td>-</td></tr> <tr><td>3.50</td><td>-</td><td>-</td><td>-</td></tr> <tr><td>3.00</td><td>-</td><td>-</td><td>-</td></tr> <tr><td>2.50</td><td>-</td><td>-</td><td>-</td></tr> <tr><td>2.00</td><td>-</td><td>-</td><td>-</td></tr> <tr><td>1.50</td><td>-</td><td>-</td><td>-</td></tr> <tr><td>1.00</td><td>-</td><td>-</td><td>-</td></tr> <tr><td>0.50</td><td>-</td><td>-</td><td>-</td></tr> <tr><td>0.00</td><td>-</td><td>-</td><td>-</td></tr> </tbody> </table>		Output Voltage [V]	Load Current [A]			Input Volt. 100[V]	Input Volt. 200[V]	Input Volt. 230[V]	5.00	3.36	3.13	3.31	4.75	-	-	-	4.50	-	-	-	4.00	-	-	-	3.50	-	-	-	3.00	-	-	-	2.50	-	-	-	2.00	-	-	-	1.50	-	-	-	1.00	-	-	-	0.50	-	-	-	0.00	-	-	-
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Model	TECS10F-5	Testing Circuitry Figure A
Item	Ambient Temperature Drift	
Object	+5V2A	

1.Values

Load 100%

Ambient Temperature[°C]	Output Voltage [V]		
	Input Volt. 100V	Input Volt. 200V	Input Volt. 230V
-20	5.057	5.057	5.057
25	5.054	5.054	5.054
60	5.045	5.046	5.046

Item	Minimum Input Voltage for Regulated Output Voltage	Testing Circuitry Figure A
Object	+5V2A	

1.Values

Ambient Temperature[°C]	Input Voltage [V]	
	Load 50%	Load 100%
-20	39	55
25	36	52
60	36	52

Item	Overvoltage Protection	Testing Circuitry Figure A
Object	+5V2A	

1.Values

Load 0%

Ambient Temperature[°C]	Operating Point [V]	
	Input Volt. 100V	Input Volt. 230V
-20	6.16	6.16
25	6.10	6.10
60	6.10	6.10

coSEL

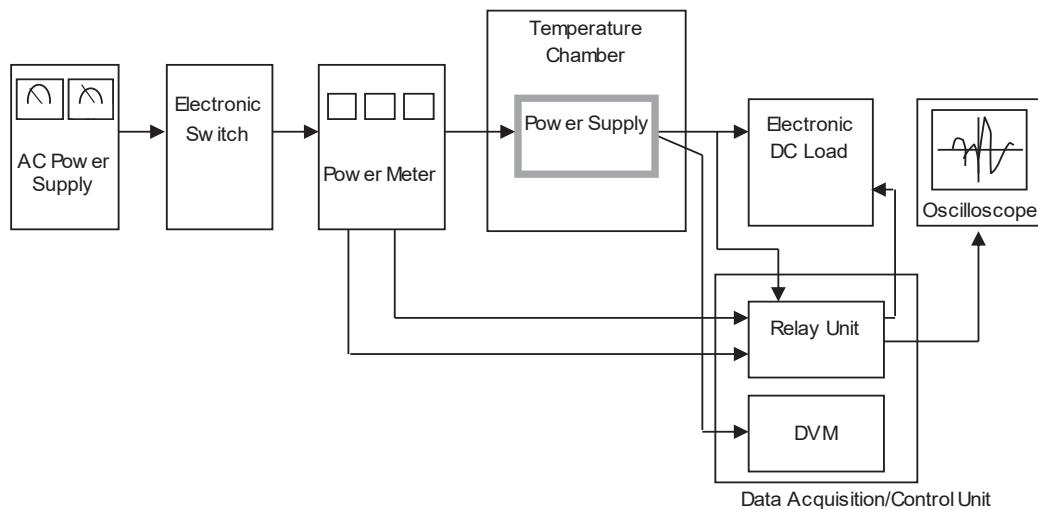


Figure A

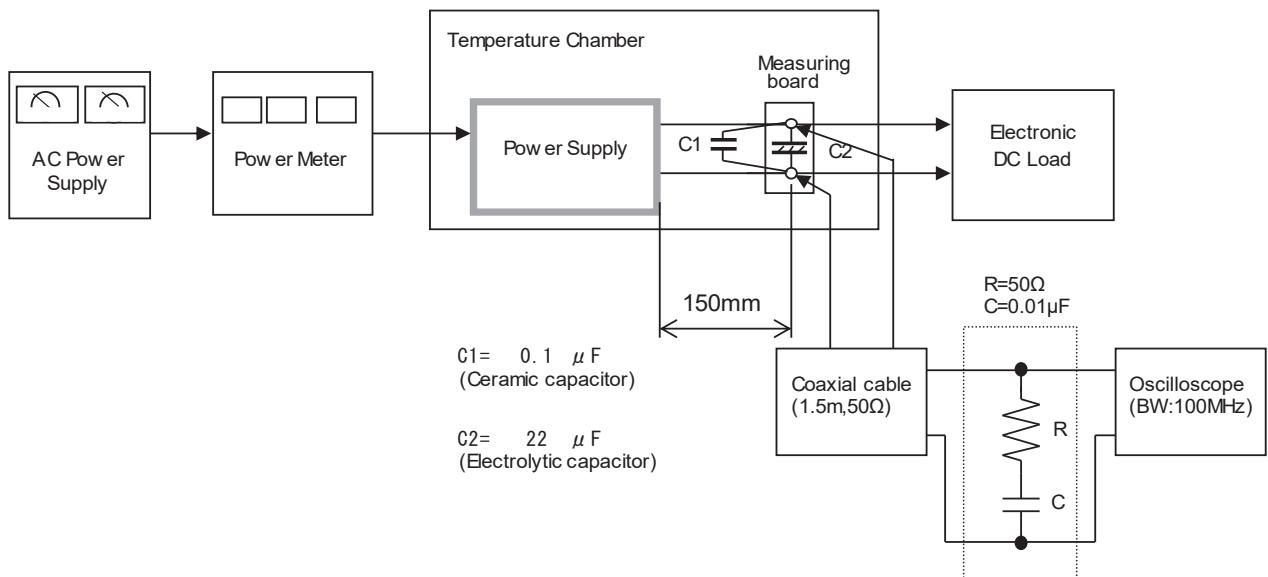


Figure B

COSEL

