

TEST DATA OF TECS10F-24

Regulated DC Power Supply
February 28, 2025

Approved by : _____ Tetsuro Hirata

Design Manager

Prepared by : _____ Junichi Otsubo

Design Engineer

COSEL CO.,LTD.



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Model	TECS10F-24																																																									
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<p>The graph plots Efficiency [%] on the y-axis (50 to 100) against Load Current [A] on the x-axis (0.0 to 0.5). Three data series are shown for different input voltages: 100V (solid line with open triangle markers), 200V (dashed line with open square markers), and 230V (dash-dot line with open circle markers). All curves show efficiency increasing with load current. A slanted line on the right side of the graph indicates the rated load current range.</p>		<table border="1"> <thead> <tr> <th rowspan="2">Load Current [A]</th> <th colspan="3">Efficiency [%]</th> </tr> <tr> <th>Input Volt. 100[V]</th> <th>Input Volt. 200[V]</th> <th>Input Volt. 230[V]</th> </tr> </thead> <tbody> <tr><td>0.000</td><td>-</td><td>-</td><td>-</td></tr> <tr><td>0.045</td><td>84.1</td><td>80.9</td><td>79.8</td></tr> <tr><td>0.090</td><td>89.5</td><td>84.8</td><td>84.8</td></tr> <tr><td>0.135</td><td>89.1</td><td>86.7</td><td>87.0</td></tr> <tr><td>0.180</td><td>90.5</td><td>86.8</td><td>87.1</td></tr> <tr><td>0.225</td><td>90.5</td><td>88.3</td><td>87.3</td></tr> <tr><td>0.270</td><td>90.3</td><td>88.7</td><td>87.6</td></tr> <tr><td>0.315</td><td>90.2</td><td>88.3</td><td>88.4</td></tr> <tr><td>0.360</td><td>91.1</td><td>88.9</td><td>88.7</td></tr> <tr><td>0.405</td><td>90.8</td><td>89.7</td><td>89.8</td></tr> <tr><td>0.450</td><td>90.2</td><td>90.0</td><td>90.3</td></tr> <tr><td>0.495</td><td>89.4</td><td>89.9</td><td>89.7</td></tr> </tbody> </table>		Load Current [A]	Efficiency [%]			Input Volt. 100[V]	Input Volt. 200[V]	Input Volt. 230[V]	0.000	-	-	-	0.045	84.1	80.9	79.8	0.090	89.5	84.8	84.8	0.135	89.1	86.7	87.0	0.180	90.5	86.8	87.1	0.225	90.5	88.3	87.3	0.270	90.3	88.7	87.6	0.315	90.2	88.3	88.4	0.360	91.1	88.9	88.7	0.405	90.8	89.7	89.8	0.450	90.2	90.0	90.3	0.495	89.4	89.9	89.7
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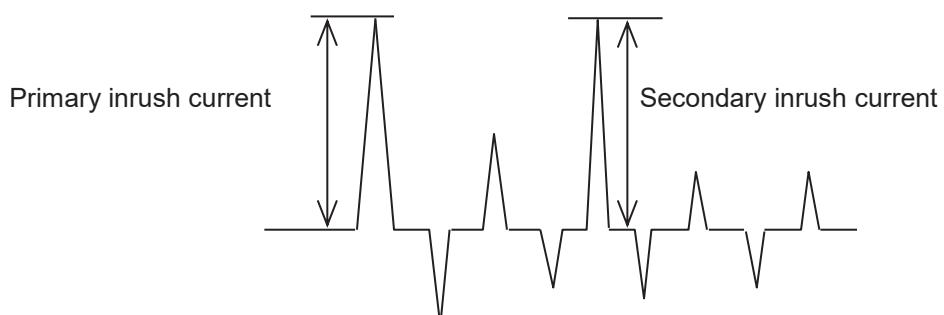
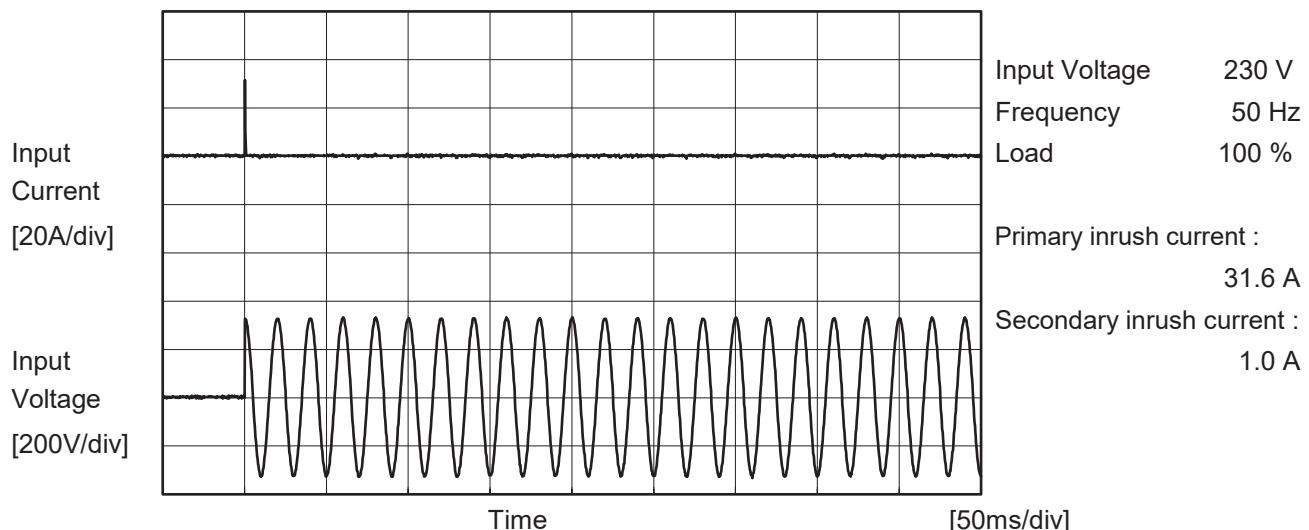
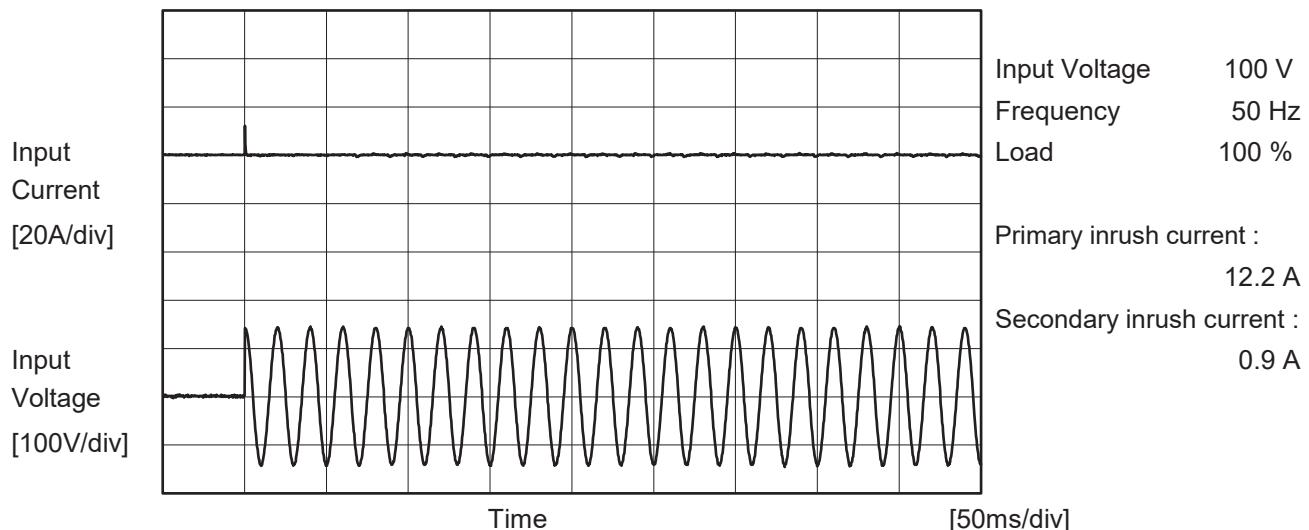
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Model	TECS10F-24	Temperature	25°C
Item	Inrush Current	Testing Circuitry	Figure A
Object	<hr/>		





Model	TECS10F-24	Temperature	25°C
Item	Leakage Current	Testing Circuitry	Figure C
Object	_____		

1. Results

[μ A]

Standards	Testing Circuitry	Measuring Method	Input Volt.			Note
			100 [V]	230 [V]	264 [V]	
DEN-AN	Figure C-1	Both phases	24	47	53	Operation
		One of phases	28	69	81	Stand by
IEC62368-1	Figure C-2	Both phases	19	44	52	Operation
		One of phases	28	69	81	Stand by
	Figure C-3	Both phases	19	45	52	Operation
		One of phases	28	69	81	Stand by

The value for "One of phases" is the reference value only.

2. Condition

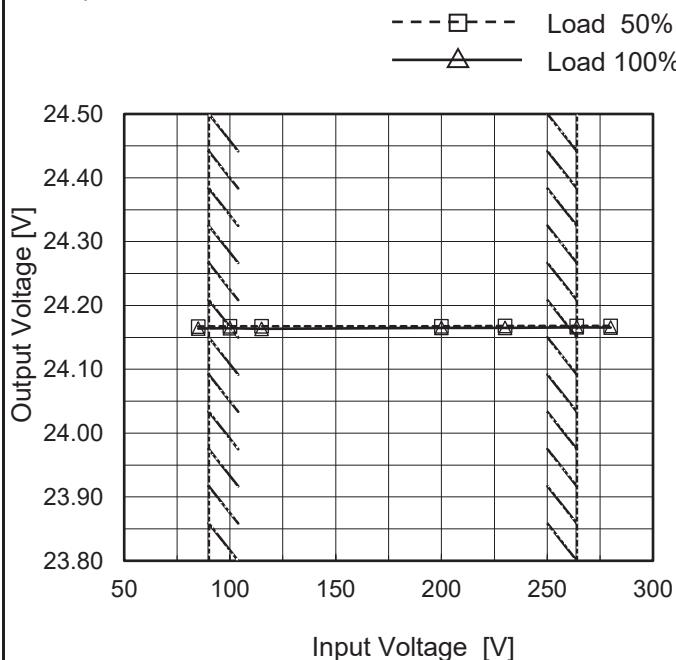
Leakage current value is concluded after measuring both phases of AC input and by choosing the larger one.

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Model	TECS10F-24
Item	Line Regulation
Object	+24V0.45A

 Temperature 25°C
 Testing Circuitry Figure A

1.Graph



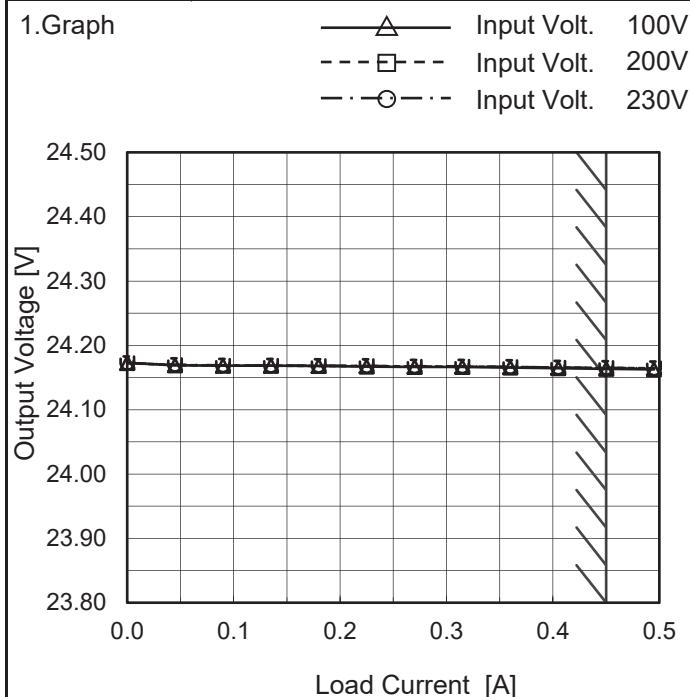
2.Values

Input Voltage [V]	Output Voltage [V]	
	Load 50%	Load 100%
85	24.167	24.164
100	24.168	24.164
115	24.168	24.163
200	24.168	24.165
230	24.168	24.165
264	24.168	24.166
280	24.168	24.165
--	-	-
--	-	-

Note: Slanted line shows the range of the rated input voltage.

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Model	TECS10F-24
Item	Load Regulation
Object	+24V0.45A

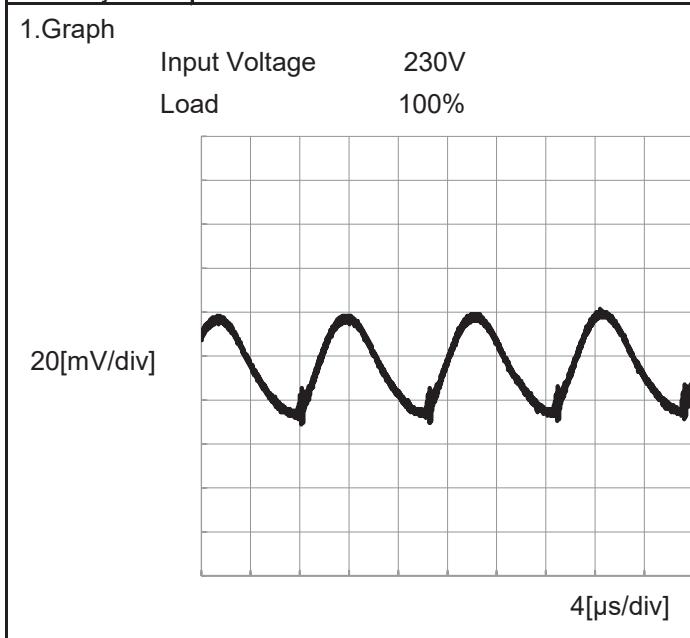

 Temperature 25°C
 Testing Circuitry Figure A

2.Values

Load Current [A]	Output Voltage [V]		
	Input Volt. 100[V]	Input Volt. 200[V]	Input Volt. 230[V]
0.000	24.173	24.172	24.173
0.045	24.169	24.169	24.170
0.090	24.169	24.169	24.169
0.135	24.169	24.169	24.169
0.180	24.168	24.168	24.168
0.225	24.167	24.168	24.168
0.270	24.166	24.167	24.168
0.315	24.166	24.167	24.167
0.360	24.165	24.166	24.166
0.405	24.164	24.165	24.165
0.450	24.164	24.165	24.165
0.495	24.163	24.164	24.164

Note: Slanted line shows the range of the rated load current.

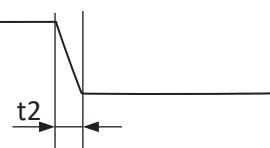
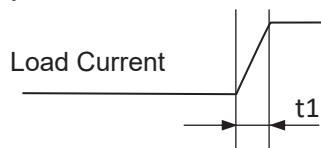
Item	Ripple-Noise
Object	+24V0.45A

 Temperature 25°C
 Testing Circuitry Figure B


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Model	TECS10F-24	Temperature Testing Circuitry	25°C Figure A
Item	Dynamic Load Response		
Object	+24V0.45A		

Input Volt. 230 V
 Cycle 1000 ms

Response. $t_1=t_2=50\mu\text{s}$. Typ

Load 0%(0A) \longleftrightarrow
 Load 100%(0.45A)

200[mV/div]

1[ms/div]

10[ms/div]

Load 50%(0.225A) \longleftrightarrow
 Load 100%(0.45A)

200[mV/div]

1[ms/div]

10[ms/div]

Load 0%(0A) \longleftrightarrow
 Load 50%(0.225A)

200[mV/div]

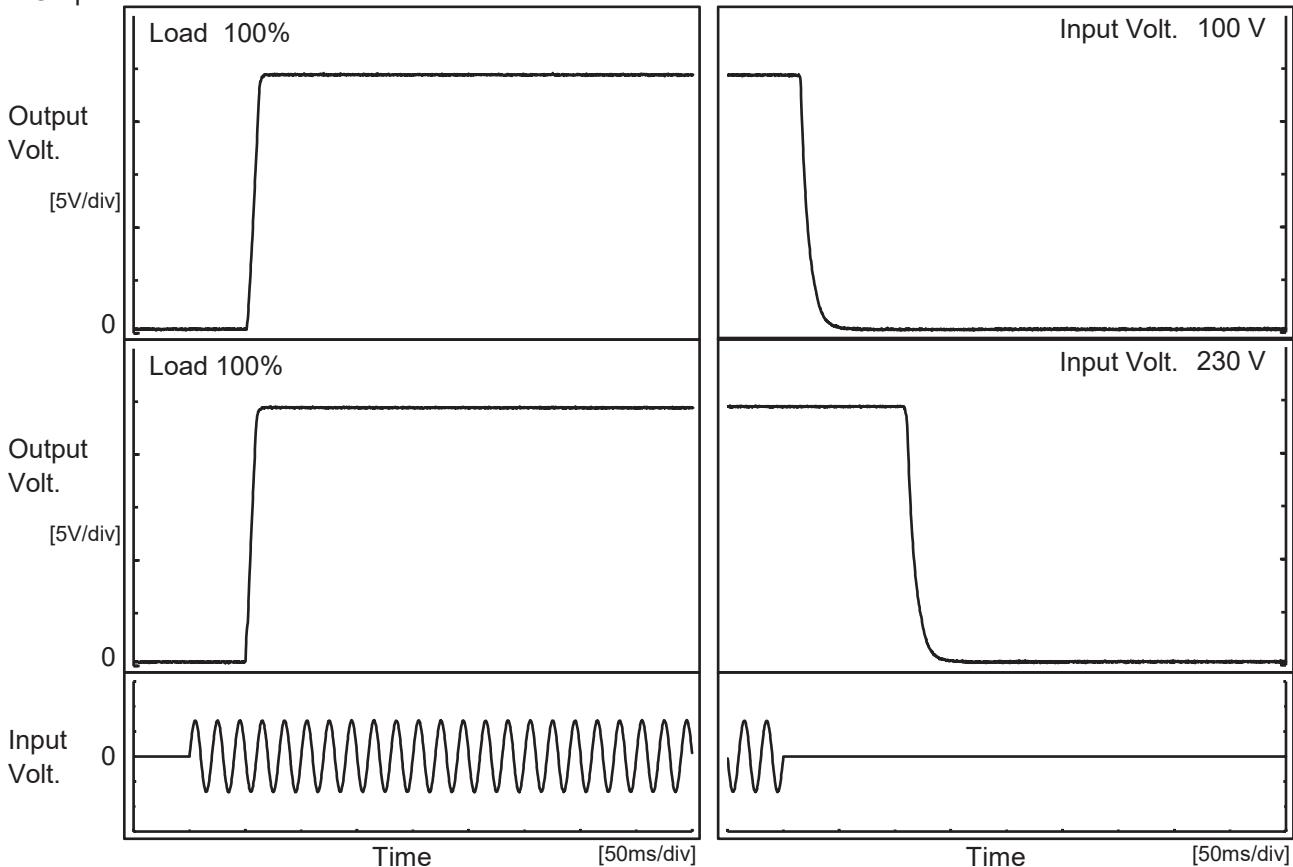
1[ms/div]

10[ms/div]

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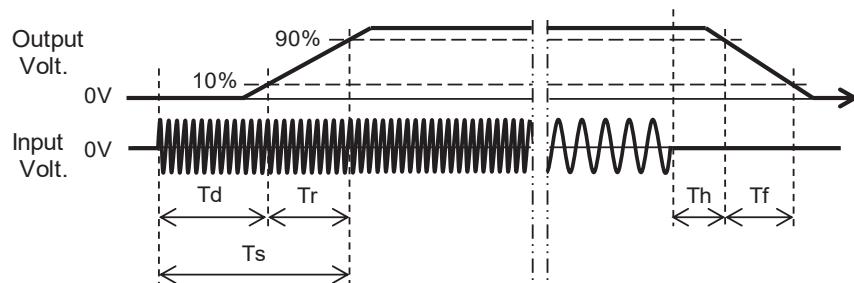
Model	TECS10F-24	Temperature	25°C
Item	Rise and Fall Time	Testing Circuitry	Figure A
Object	+24V0.45A		

1. Graph



2. Values

Input Volt.	Time	Td	Tr	Ts	Th	Tf	[ms]
100V		52.5	9.3	61.8	16.3	16.0	
230V		50.5	8.8	59.3	111.3	16.0	

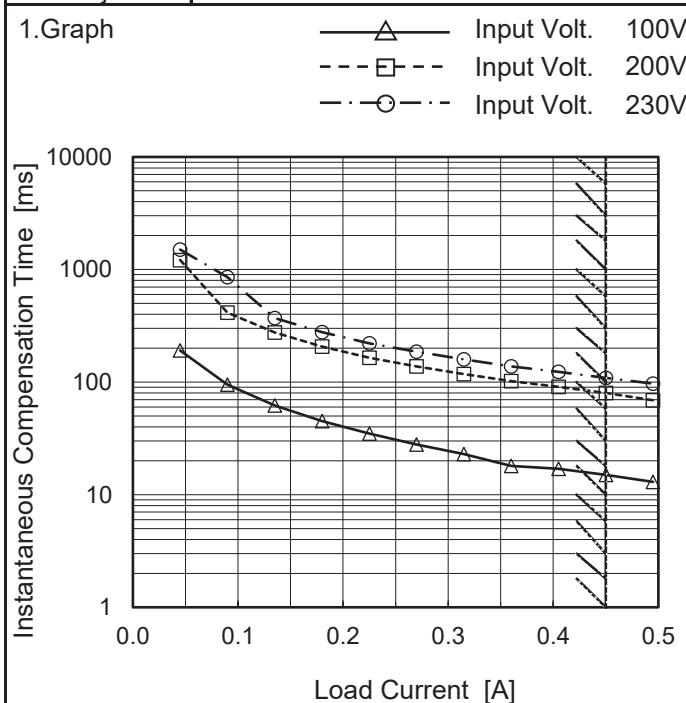


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Model	TECS10F-24	Temperature	25°C																																
Item	Hold-Up Time	Testing Circuitry	Figure A																																
Object	+24V0.45A																																		
1. Graph			2. Values																																
<p>The graph illustrates the relationship between input voltage and hold-up time for two load conditions. The Y-axis represents hold-up time in milliseconds on a logarithmic scale, ranging from 1 to 1000 ms. The X-axis represents input voltage in Volts, ranging from 50 to 300 V. A slanted line indicates the rated input voltage range. At higher voltages, the hold-up time is relatively constant or slightly increasing. As the input voltage drops below the rated range, the hold-up time increases significantly, particularly for the 50% load condition.</p>			<table border="1"> <thead> <tr> <th rowspan="2">Input Voltage [V]</th> <th colspan="2">Hold-Up Time [ms]</th> </tr> <tr> <th>Load 50%</th> <th>Load 100%</th> </tr> </thead> <tbody> <tr> <td>85</td> <td>23</td> <td>9</td> </tr> <tr> <td>100</td> <td>35</td> <td>15</td> </tr> <tr> <td>115</td> <td>49</td> <td>22</td> </tr> <tr> <td>200</td> <td>165</td> <td>81</td> </tr> <tr> <td>230</td> <td>222</td> <td>110</td> </tr> <tr> <td>264</td> <td>297</td> <td>149</td> </tr> <tr> <td>280</td> <td>337</td> <td>168</td> </tr> <tr> <td>--</td> <td>-</td> <td>-</td> </tr> <tr> <td>--</td> <td>-</td> <td>-</td> </tr> </tbody> </table>	Input Voltage [V]	Hold-Up Time [ms]		Load 50%	Load 100%	85	23	9	100	35	15	115	49	22	200	165	81	230	222	110	264	297	149	280	337	168	--	-	-	--	-	-
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<p>This duration covers from Shut-off of input voltage to the moment when output voltage descends to the rated range of voltage accuracy.</p> <p>Note: Slanted line shows the range of the rated input voltage.</p>																																			

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Model	TECS10F-24
Item	Instantaneous Interruption Compensation
Object	+24V0.45A


 Temperature 25°C
 Testing Circuitry Figure A

2.Values

Load Current [A]	Time [ms]		
	Input Volt. 100[V]	Input Volt. 200[V]	Input Volt. 230[V]
0.000	-	-	-
0.045	191	1215	1504
0.090	95	414	856
0.135	62	276	371
0.180	45	207	278
0.225	35	165	221
0.270	28	138	186
0.315	23	118	159
0.360	18	102	138
0.405	17	91	123
0.450	15	80	109
0.495	13	69	97

Note: Slanted line shows the range of the rated load current.

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Model	TECS10F-24	Temperature	25°C																																																							
Item	Overcurrent Protection	Testing Circuitry	Figure A																																																							
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<p>Output Voltage [V]</p> <p>Load Current [A]</p> <p>Note: Slanted line shows the range of the rated load current.</p> <p>Overcurrent protection is Hiccup mode.</p>		<table border="1"> <thead> <tr> <th rowspan="2">Output Voltage [V]</th> <th colspan="3">Load Current [A]</th> </tr> <tr> <th>Input Volt. 100[V]</th> <th>Input Volt. 200[V]</th> <th>Input Volt. 230[V]</th> </tr> </thead> <tbody> <tr><td>24.0</td><td>0.75</td><td>0.69</td><td>0.72</td></tr> <tr><td>22.8</td><td>-</td><td>-</td><td>-</td></tr> <tr><td>21.6</td><td>-</td><td>-</td><td>-</td></tr> <tr><td>19.2</td><td>-</td><td>-</td><td>-</td></tr> <tr><td>16.8</td><td>-</td><td>-</td><td>-</td></tr> <tr><td>14.4</td><td>-</td><td>-</td><td>-</td></tr> <tr><td>12.0</td><td>-</td><td>-</td><td>-</td></tr> <tr><td>9.6</td><td>-</td><td>-</td><td>-</td></tr> <tr><td>7.2</td><td>-</td><td>-</td><td>-</td></tr> <tr><td>4.8</td><td>-</td><td>-</td><td>-</td></tr> <tr><td>2.4</td><td>-</td><td>-</td><td>-</td></tr> <tr><td>0.0</td><td>-</td><td>-</td><td>-</td></tr> </tbody> </table>		Output Voltage [V]	Load Current [A]			Input Volt. 100[V]	Input Volt. 200[V]	Input Volt. 230[V]	24.0	0.75	0.69	0.72	22.8	-	-	-	21.6	-	-	-	19.2	-	-	-	16.8	-	-	-	14.4	-	-	-	12.0	-	-	-	9.6	-	-	-	7.2	-	-	-	4.8	-	-	-	2.4	-	-	-	0.0	-	-	-
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21.6	-	-	-																																																							
19.2	-	-	-																																																							
16.8	-	-	-																																																							
14.4	-	-	-																																																							
12.0	-	-	-																																																							
9.6	-	-	-																																																							
7.2	-	-	-																																																							
4.8	-	-	-																																																							
2.4	-	-	-																																																							
0.0	-	-	-																																																							



Model	TECS10F-24	Testing Circuitry Figure A
Item	Ambient Temperature Drift	
Object	+24V0.45A	

1.Values

Load 100%

Ambient Temperature[°C]	Output Voltage [V]		
	Input Volt. 100V	Input Volt. 200V	Input Volt. 230V
-20	24.115	24.116	24.117
25	24.158	24.159	24.159
60	24.159	24.161	24.161

Item	Minimum Input Voltage for Regulated Output Voltage	Testing Circuitry Figure A
Object	+24V0.45A	

1.Values

Ambient Temperature[°C]	Input Voltage [V]	
	Load 50%	Load 100%
-20	35	52
25	33	51
60	33	50

Item	Overvoltage Protection	Testing Circuitry Figure A
Object	+24V0.45A	

1.Values

Load 0%

Ambient Temperature[°C]	Operating Point [V]	
	Input Volt. 100V	Input Volt. 230V
-20	30.53	30.50
25	31.32	31.33
60	31.98	32.01

coSEL

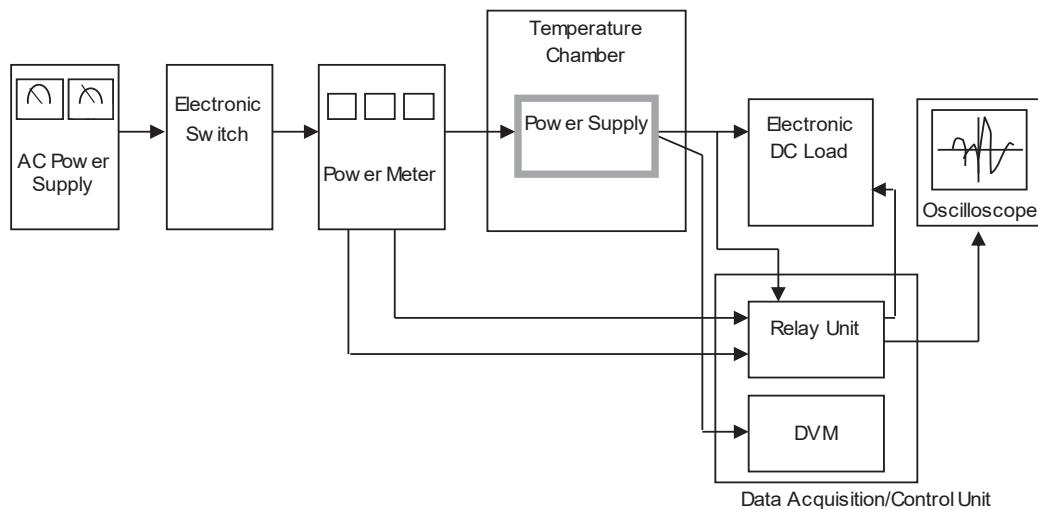


Figure A

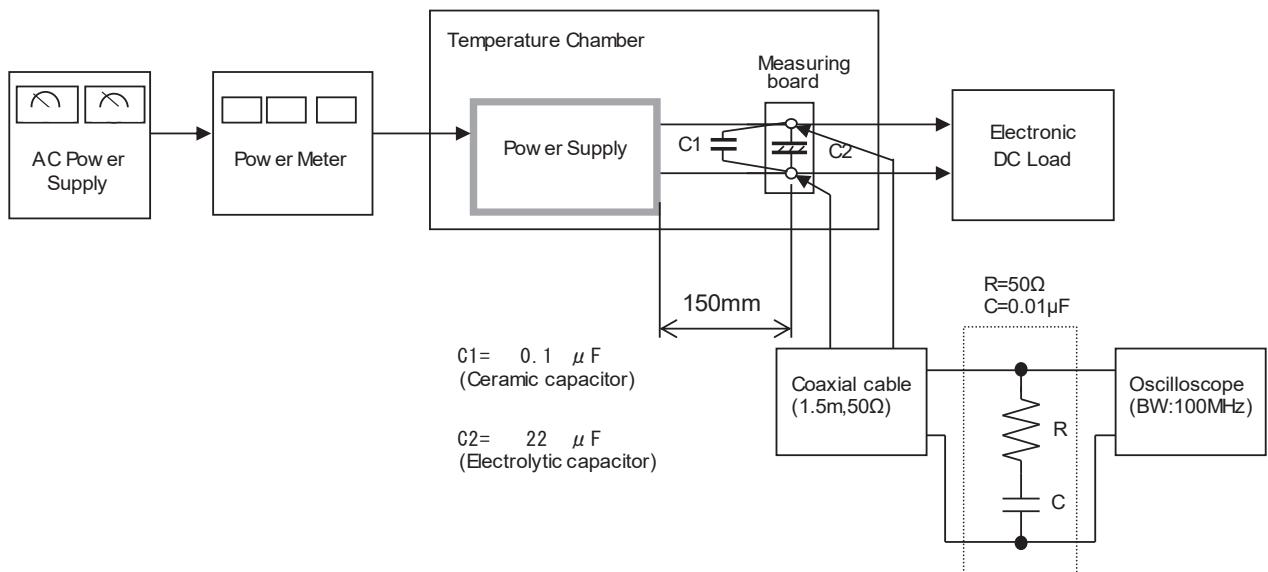


Figure B

COSEL

