

TEST DATA OF TECS10F-15

Regulated DC Power Supply
February 28, 2025

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Design Manager

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Design Engineer

COSEL CO.,LTD.



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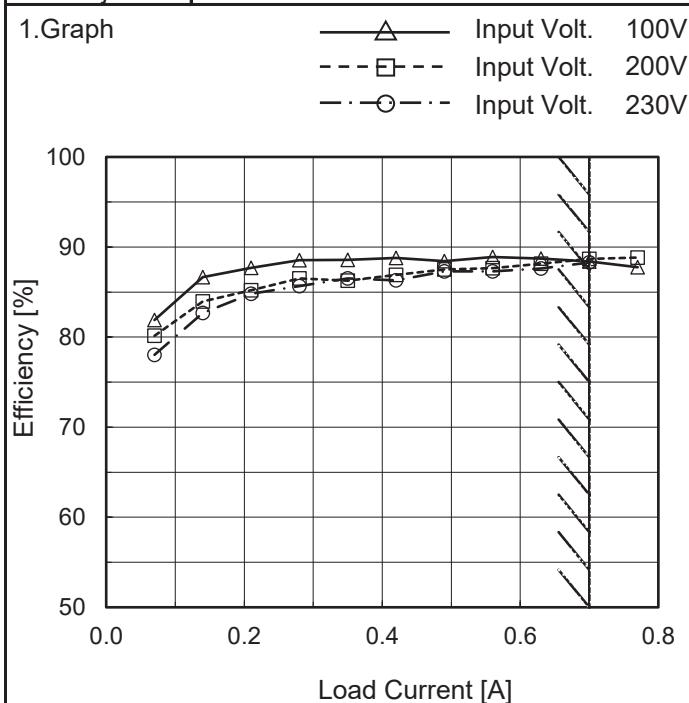
COSEL

Model	TECS10F-15	Temperature Testing Circuitry	25°C Figure A																																																							
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Note: Slanted line shows the range of the rated load current.

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Model	TECS10F-15
Item	Efficiency (by Load Current)
Object	_____


 Temperature 25°C
 Testing Circuitry Figure A

2.Values

Load Current [A]	Efficiency [%]		
	Input Volt. 100[V]	Input Volt. 200[V]	Input Volt. 230[V]
0.00	-	-	-
0.07	81.9	80.1	78.0
0.14	86.7	84.0	82.7
0.21	87.7	85.2	84.8
0.28	88.6	86.5	85.7
0.35	88.6	86.3	86.5
0.42	88.8	86.9	86.3
0.49	88.4	87.5	87.3
0.56	88.9	87.7	87.3
0.63	88.7	88.1	87.6
0.70	88.4	88.7	88.3
0.77	87.8	88.8	88.5

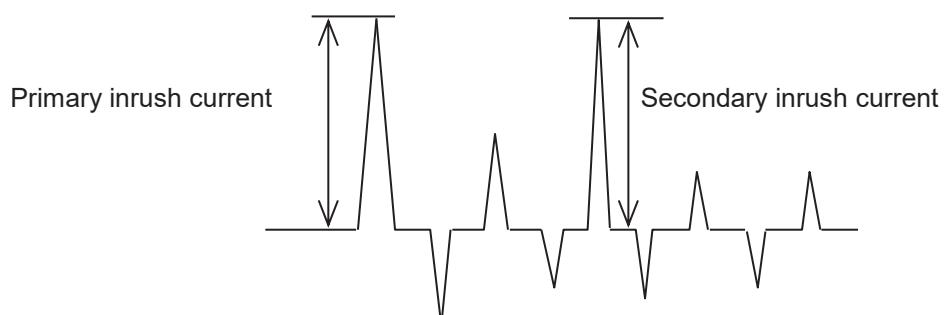
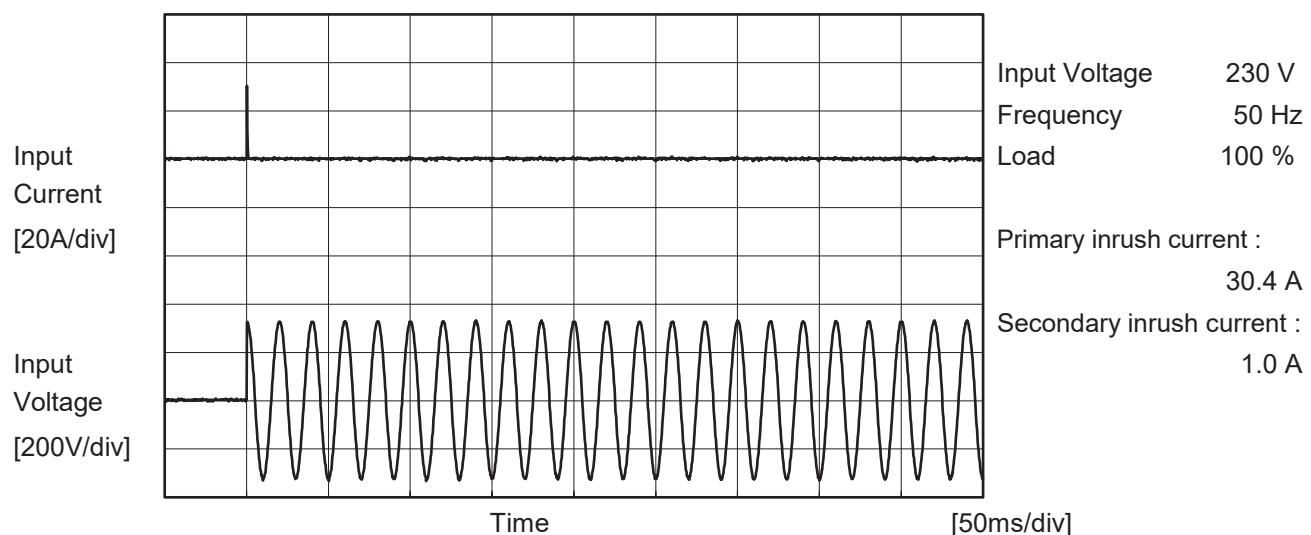
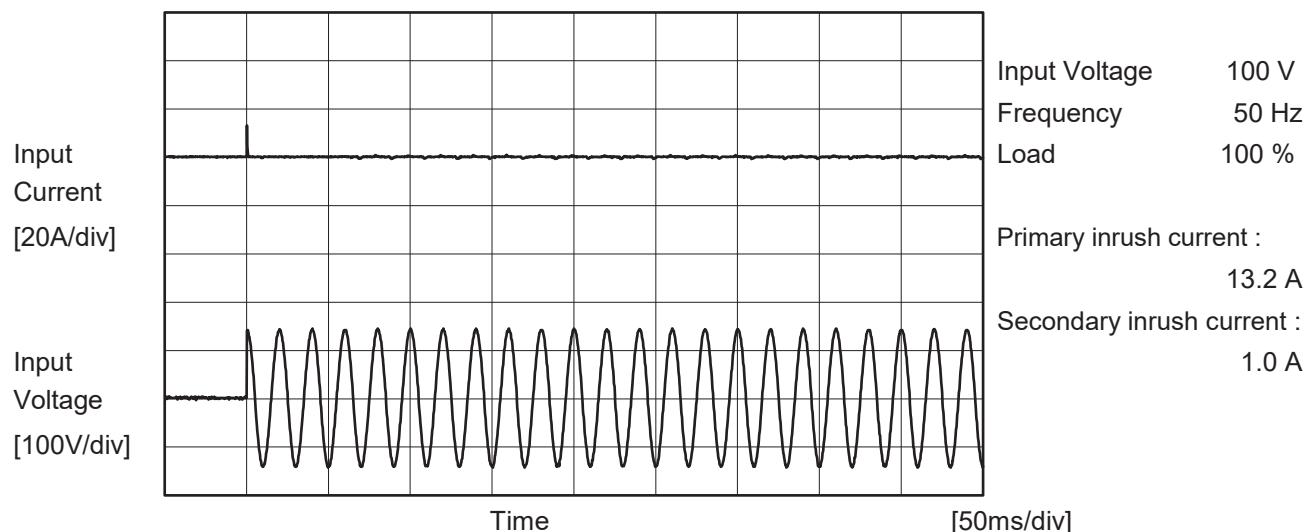
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<p>Note: Slanted line shows the range of the rated load current.</p>																																																										

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Model	TECS10F-15	Temperature	25°C
Item	Inrush Current	Testing Circuitry	Figure A
Object	<hr/>		





Model	TECS10F-15	Temperature Testing Circuitry	25°C Figure C
Item	Leakage Current		
Object	<hr/>		

1. Results

[μ A]

Standards	Testing Circuitry	Measuring Method	Input Volt.			Note
			100 [V]	230 [V]	264 [V]	
DEN-AN	Figure C-1	Both phases	24	47	53	Operation
		One of phases	28	69	81	Stand by
IEC62368-1	Figure C-2	Both phases	19	44	52	Operation
		One of phases	28	69	81	Stand by
	Figure C-3	Both phases	19	45	52	Operation
		One of phases	28	69	81	Stand by

The value for "One of phases" is the reference value only.

2. Condition

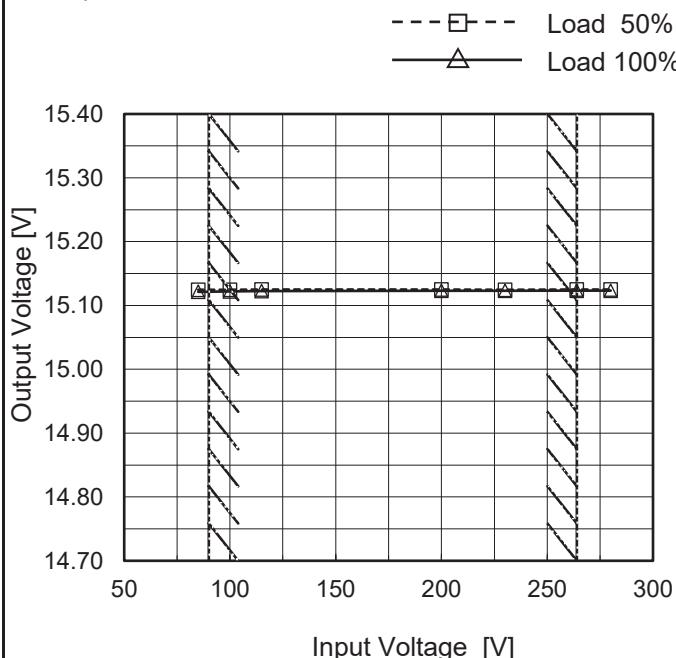
Leakage current value is concluded after measuring both phases of AC input and by choosing the larger one.

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Model	TECS10F-15
Item	Line Regulation
Object	+15V0.7A

 Temperature 25°C
 Testing Circuitry Figure A

1.Graph



2.Values

Input Voltage [V]	Output Voltage [V]	
	Load 50%	Load 100%
85	15.125	15.121
100	15.125	15.122
115	15.125	15.123
200	15.125	15.123
230	15.125	15.123
264	15.125	15.123
280	15.125	15.123
--	-	-
--	-	-

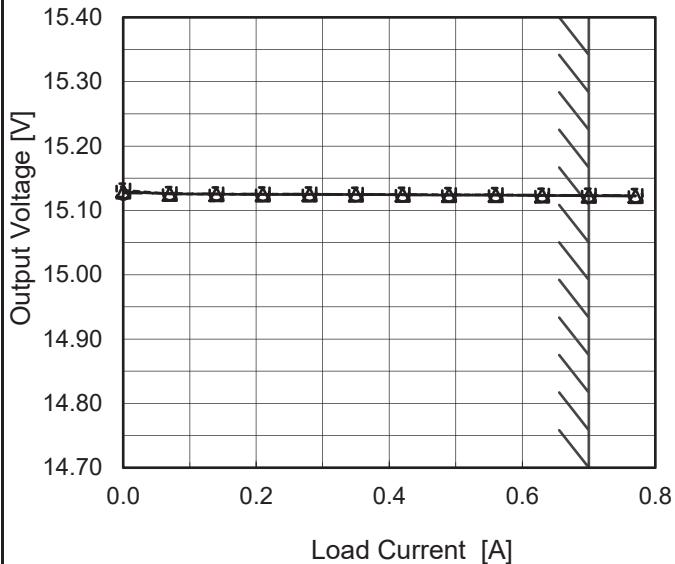
Note: Slanted line shows the range of the rated input voltage.

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Model	TECS10F-15
Item	Load Regulation
Object	+15V0.7A

 Temperature 25°C
 Testing Circuitry Figure A

- 1.Graph
- △— Input Volt. 100V
 - -□--- Input Volt. 200V
 - ·○--- Input Volt. 230V



2.Values

Load Current [A]	Output Voltage [V]		
	Input Volt. 100[V]	Input Volt. 200[V]	Input Volt. 230[V]
0.00	15.129	15.131	15.127
0.07	15.126	15.126	15.126
0.14	15.125	15.125	15.126
0.21	15.125	15.125	15.125
0.28	15.125	15.125	15.125
0.35	15.125	15.125	15.125
0.42	15.124	15.125	15.124
0.49	15.124	15.124	15.124
0.56	15.124	15.124	15.124
0.63	15.123	15.123	15.124
0.70	15.123	15.123	15.123
0.77	15.122	15.123	15.123

Note: Slanted line shows the range of the rated load current.

Item	Ripple-Noise
Object	+15V0.7A

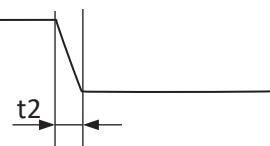
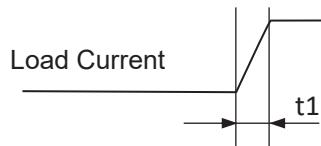
 Temperature 25°C
 Testing Circuitry Figure B

- 1.Graph
- Input Voltage 230V
 Load 100%
-

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Model	TECS10F-15	Temperature	25°C
Item	Dynamic Load Response	Testing Circuitry	Figure A
Object	+15V0.7A		

Input Volt. 230 V
 Cycle 1000 ms

Response. $t_1=t_2=50\mu\text{s}$. Typ

Load 0%(0A) \longleftrightarrow
 Load 100%(0.7A)

200[mV/div]

1[ms/div]

10[ms/div]

Load 50%(0.35A) \longleftrightarrow
 Load 100%(0.7A)

200[mV/div]

1[ms/div]

10[ms/div]

Load 0%(0A) \longleftrightarrow
 Load 50%(0.35A)

200[mV/div]

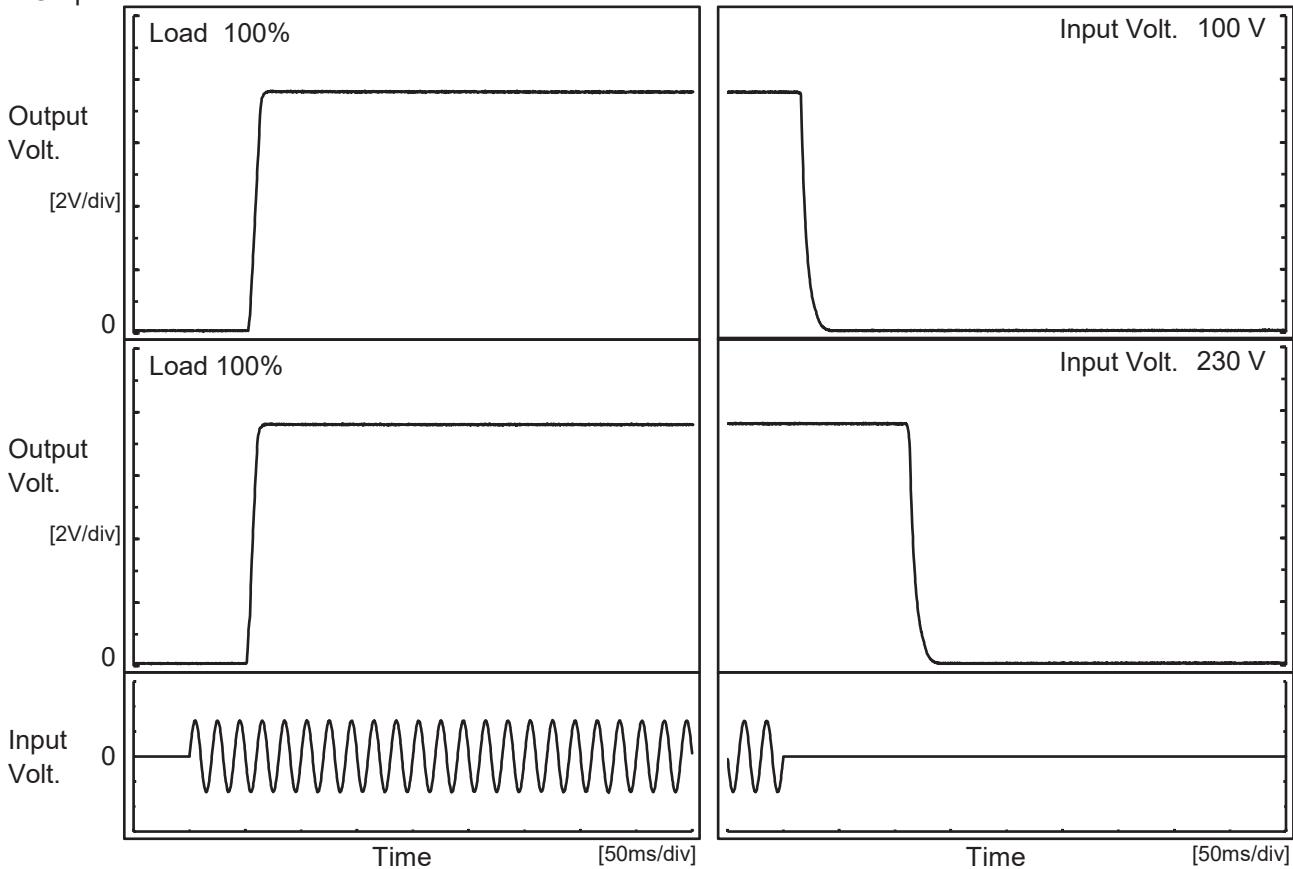
1[ms/div]

10[ms/div]

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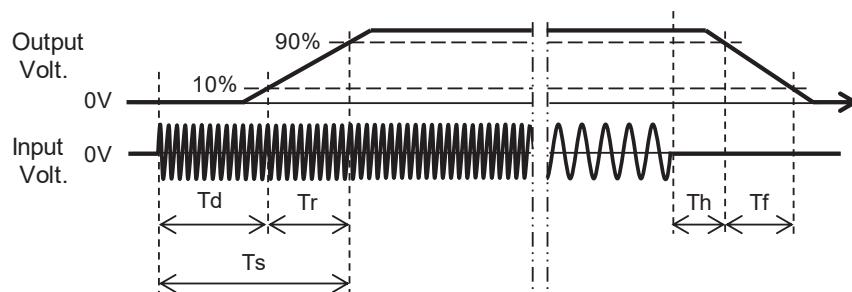
Model	TECS10F-15	Temperature	25°C
Item	Rise and Fall Time	Testing Circuitry	Figure A
Object	+15V0.7A		

1. Graph



2. Values

Input Volt.	Time	Td	Tr	Ts	Th	Tf	[ms]
100V		54.3	8.8	63.1	16.5	11.8	
230V		52.0	8.5	60.5	113.5	12.0	

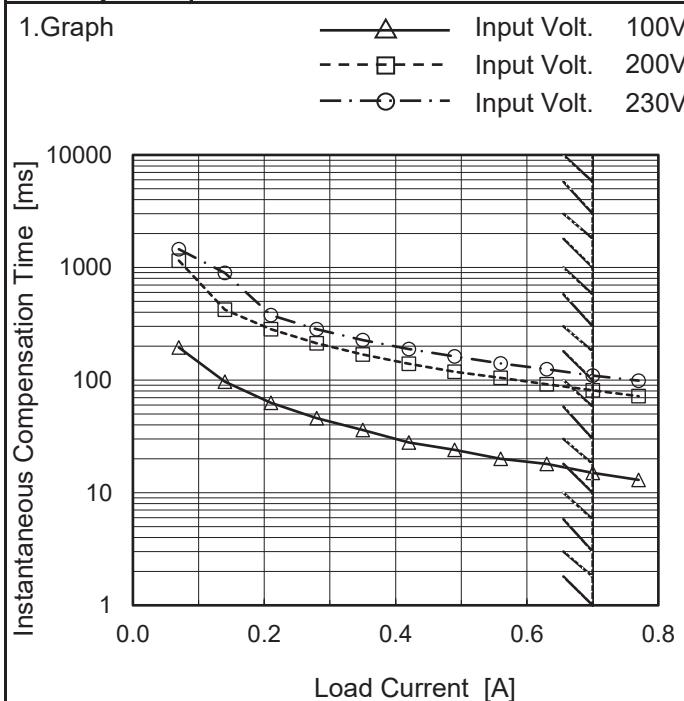


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Model	TECS10F-15	Temperature	25°C																																
Item	Hold-Up Time	Testing Circuitry	Figure A																																
Object	+15V0.7A																																		
1. Graph		2. Values																																	
<p>Graph showing Hold-Up Time [ms] vs Input Voltage [V]. The Y-axis is logarithmic, ranging from 1 to 1000 ms. The X-axis is linear, ranging from 50 to 300 V. Two curves are plotted: Load 50% (dashed line with square markers) and Load 100% (solid line with triangle markers). Both curves show an increase in hold-up time as input voltage decreases below the rated range (indicated by a slanted line between approximately 115V and 264V).</p>		<table border="1"> <thead> <tr> <th rowspan="2">Input Voltage [V]</th> <th colspan="2">Hold-Up Time [ms]</th> </tr> <tr> <th>Load 50%</th> <th>Load 100%</th> </tr> </thead> <tbody> <tr> <td>85</td> <td>24</td> <td>10</td> </tr> <tr> <td>100</td> <td>36</td> <td>16</td> </tr> <tr> <td>115</td> <td>50</td> <td>23</td> </tr> <tr> <td>200</td> <td>168</td> <td>82</td> </tr> <tr> <td>230</td> <td>226</td> <td>112</td> </tr> <tr> <td>264</td> <td>303</td> <td>150</td> </tr> <tr> <td>280</td> <td>343</td> <td>171</td> </tr> <tr> <td>--</td> <td>-</td> <td>-</td> </tr> <tr> <td>--</td> <td>-</td> <td>-</td> </tr> </tbody> </table>		Input Voltage [V]	Hold-Up Time [ms]		Load 50%	Load 100%	85	24	10	100	36	16	115	50	23	200	168	82	230	226	112	264	303	150	280	343	171	--	-	-	--	-	-
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<p>This duration covers from Shut-off of input voltage to the moment when output voltage descends to the rated range of voltage accuracy.</p> <p>Note: Slanted line shows the range of the rated input voltage.</p>																																			

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Model	TECS10F-15
Item	Instantaneous Interruption Compensation
Object	+15V0.7A


 Temperature 25°C
 Testing Circuitry Figure A

2.Values

Load Current [A]	Time [ms]		
	Input Volt. 100[V]	Input Volt. 200[V]	Input Volt. 230[V]
0.00	-	-	-
0.07	195	1153	1455
0.14	97	423	899
0.21	63	283	379
0.28	46	212	284
0.35	36	169	227
0.42	28	140	189
0.49	24	119	162
0.56	20	105	141
0.63	18	92	125
0.70	15	81	110
0.77	13	72	99

Note: Slanted line shows the range of the rated load current.

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Model	TECS10F-15	Temperature Testing Circuitry	25°C Figure A																																																							
Item	Overcurrent Protection																																																									
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<p>Output Voltage [V]</p> <p>Load Current [A]</p> <p>Note: Slanted line shows the range of the rated load current.</p> <p>Overcurrent protection is Hiccup mode.</p>		<table border="1"> <thead> <tr> <th rowspan="2">Output Voltage [V]</th> <th colspan="3">Load Current [A]</th> </tr> <tr> <th>Input Volt. 100[V]</th> <th>Input Volt. 200[V]</th> <th>Input Volt. 230[V]</th> </tr> </thead> <tbody> <tr><td>15.0</td><td>1.17</td><td>1.09</td><td>1.12</td></tr> <tr><td>14.5</td><td>-</td><td>-</td><td>-</td></tr> <tr><td>14.0</td><td>-</td><td>-</td><td>-</td></tr> <tr><td>13.0</td><td>-</td><td>-</td><td>-</td></tr> <tr><td>12.0</td><td>-</td><td>-</td><td>-</td></tr> <tr><td>11.0</td><td>-</td><td>-</td><td>-</td></tr> <tr><td>10.0</td><td>-</td><td>-</td><td>-</td></tr> <tr><td>8.0</td><td>-</td><td>-</td><td>-</td></tr> <tr><td>6.0</td><td>-</td><td>-</td><td>-</td></tr> <tr><td>4.0</td><td>-</td><td>-</td><td>-</td></tr> <tr><td>2.0</td><td>-</td><td>-</td><td>-</td></tr> <tr><td>1.0</td><td>-</td><td>-</td><td>-</td></tr> </tbody> </table>		Output Voltage [V]	Load Current [A]			Input Volt. 100[V]	Input Volt. 200[V]	Input Volt. 230[V]	15.0	1.17	1.09	1.12	14.5	-	-	-	14.0	-	-	-	13.0	-	-	-	12.0	-	-	-	11.0	-	-	-	10.0	-	-	-	8.0	-	-	-	6.0	-	-	-	4.0	-	-	-	2.0	-	-	-	1.0	-	-	-
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2.0	-	-	-																																																							
1.0	-	-	-																																																							



Model	TECS10F-15	Testing Circuitry Figure A
Item	Ambient Temperature Drift	
Object	+15V0.7A	

1.Values

Load 100%

Ambient Temperature[°C]	Output Voltage [V]		
	Input Volt. 100V	Input Volt. 200V	Input Volt. 230V
-20	15.108	15.109	15.109
25	15.124	15.125	15.125
60	15.112	15.114	15.114

Item	Minimum Input Voltage for Regulated Output Voltage	Testing Circuitry Figure A	
Object	+15V0.7A		

1.Values

Ambient Temperature[°C]	Input Voltage [V]	
	Load 50%	Load 100%
-20	38	52
25	35	49
60	34	50

Item	Overvoltage Protection	Testing Circuitry Figure A	
Object	+15V0.7A		

1.Values

Load 0%

Ambient Temperature[°C]	Operating Point [V]	
	Input Volt. 100V	Input Volt. 230V
-20	18.43	18.40
25	18.94	18.95
60	19.37	19.40

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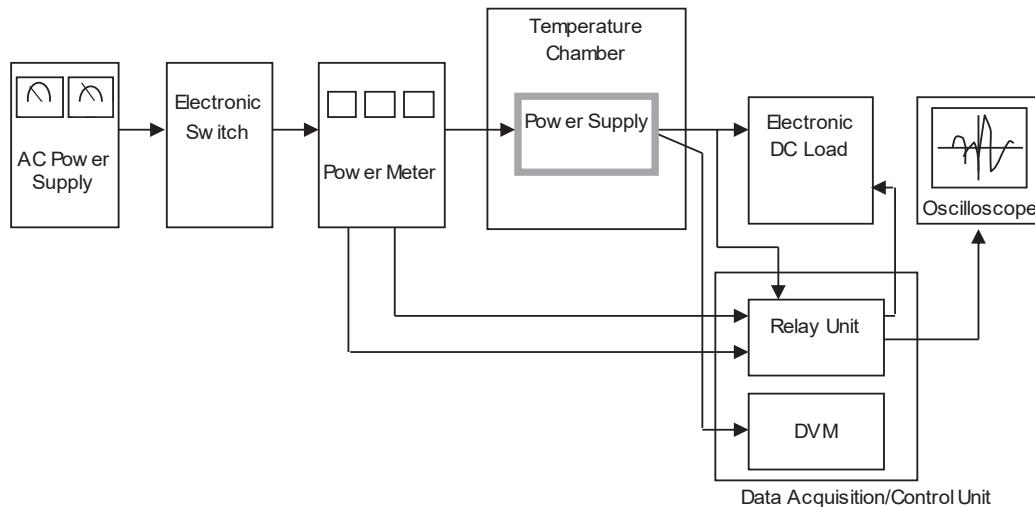


Figure A

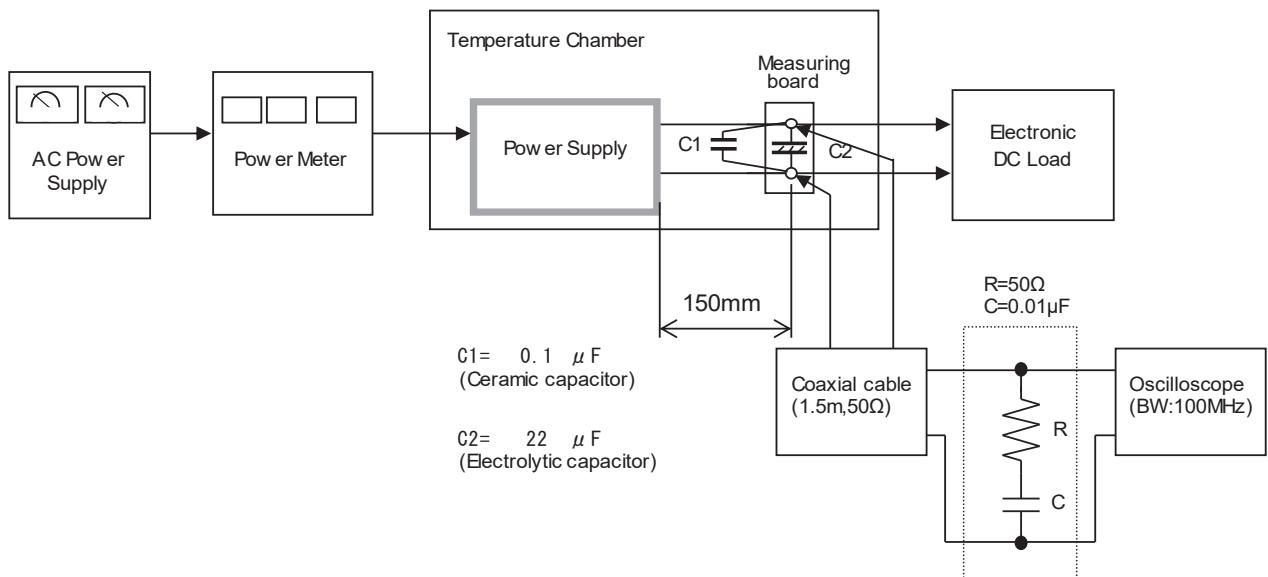


Figure B

COSEL

