

TEST DATA OF STMGFS30483R3

Regulated DC Power Supply
February 2, 2013

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Takahiro Yoneda Design Manager

Prepared by : Satoshi Kinoshita
Satoshi Kinoshita Design Engineer

COSEL CO.,LTD.

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Model		STMGFS30483R3		Temperature	25°C																																																																															
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COSEL		
Model	STMGFS30483R3	
Item	Output Voltage Accuracy	Testing Circuitry Figure A
Object	+3.3V7.5A	

1. Output Voltage Accuracy

This is defined as the value of the output voltage, regulation load, ambient temperature and input voltage varied at random in the range as specified below.

- Temperature : -20 - 60°C
- Input Voltage : 18 - 76V
- Load Current : 0 - 7.5A

* Output Voltage Accuracy = $\pm(\text{Maximum of Output Voltage} - \text{Minimum of Output Voltage}) / 2$

* Output Voltage Accuracy (Ration) = $\frac{\text{Output Voltage Accuracy}}{\text{Rated Output Voltage}} \times 100$

2. Values

Item	Temperature [°C]	Input Voltage[V]	Output		Output Voltage Accuracy	
			Current[A]	Voltage[V]	Value [mV]	Ration [%]
Maximum Voltage	-20	18	0	3.384	±14	±0.4
Minimum Voltage	60	76	7.5	3.357		



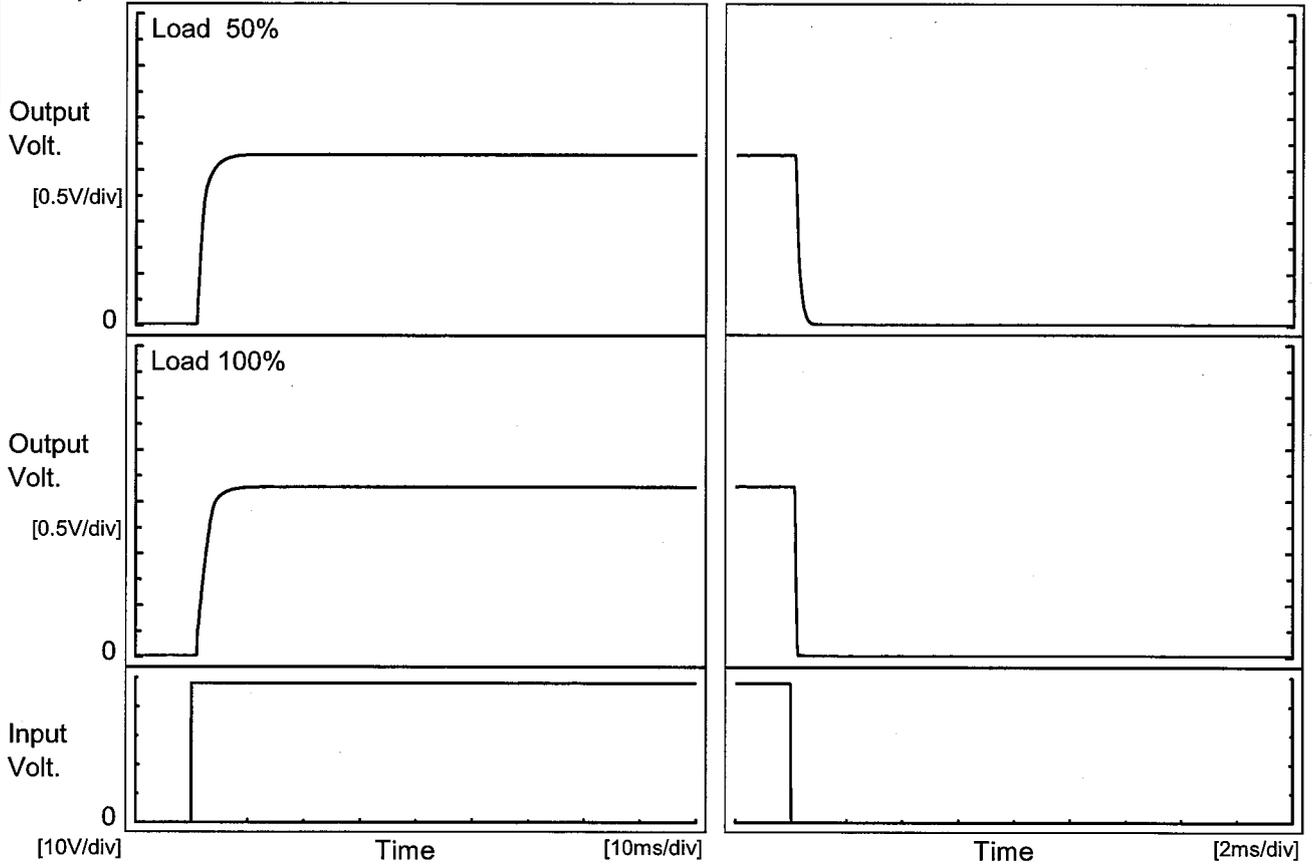
COSEL																								
Model	STMGFS30483R3	Temperature 25°C Testing Circuitry Figure A																						
Item	Time Lapse Drift																							
Object	+3.3V7.5A	2.Values																						
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<p style="text-align: center;">Time [H]</p> <p>Input Volt. 48V Load 100%</p>		<table border="1"> <thead> <tr> <th>Time since start [H]</th> <th>Output Voltage [V]</th> </tr> </thead> <tbody> <tr><td>0.0</td><td>3.363</td></tr> <tr><td>0.5</td><td>3.362</td></tr> <tr><td>1.0</td><td>3.362</td></tr> <tr><td>2.0</td><td>3.362</td></tr> <tr><td>3.0</td><td>3.362</td></tr> <tr><td>4.0</td><td>3.362</td></tr> <tr><td>5.0</td><td>3.362</td></tr> <tr><td>6.0</td><td>3.362</td></tr> <tr><td>7.0</td><td>3.362</td></tr> <tr><td>8.0</td><td>3.362</td></tr> </tbody> </table>	Time since start [H]	Output Voltage [V]	0.0	3.363	0.5	3.362	1.0	3.362	2.0	3.362	3.0	3.362	4.0	3.362	5.0	3.362	6.0	3.362	7.0	3.362	8.0	3.362
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Model	STMGFS30483R3	Temperature	25°C
Item	Rise and Fall Time	Testing Circuitry	Figure A
Object	+3.3V/7.5A		

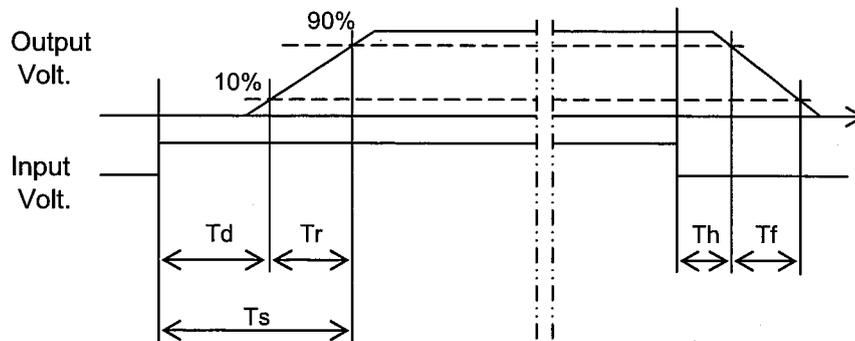
1. Graph

Input Volt. 48 V



2. Values

		[ms]				
Load \ Time	Time	Td	Tr	Ts	Th	Tf
50 %		1.1	2.9	4.0	0.2	0.3
100 %		1.1	3.2	4.3	0.1	0.1





<p>Model STMGFS30483R3</p> <p>Item Minimum Input Voltage for Regulated Output Voltage</p> <p>Object +3.3V/7.5A</p>		<p>Testing Circuitry Figure A</p>																																						
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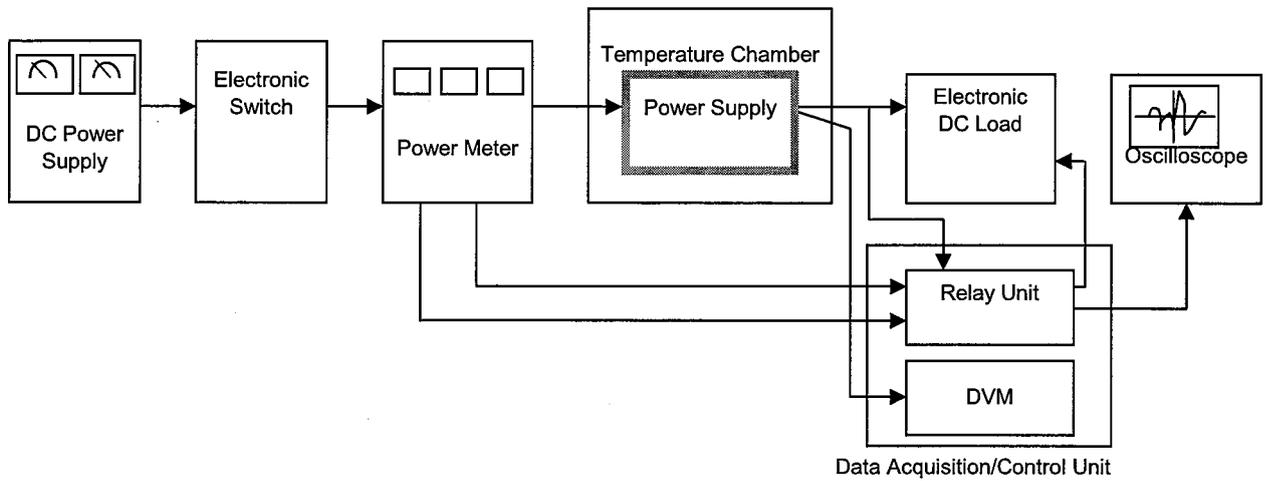


Figure A

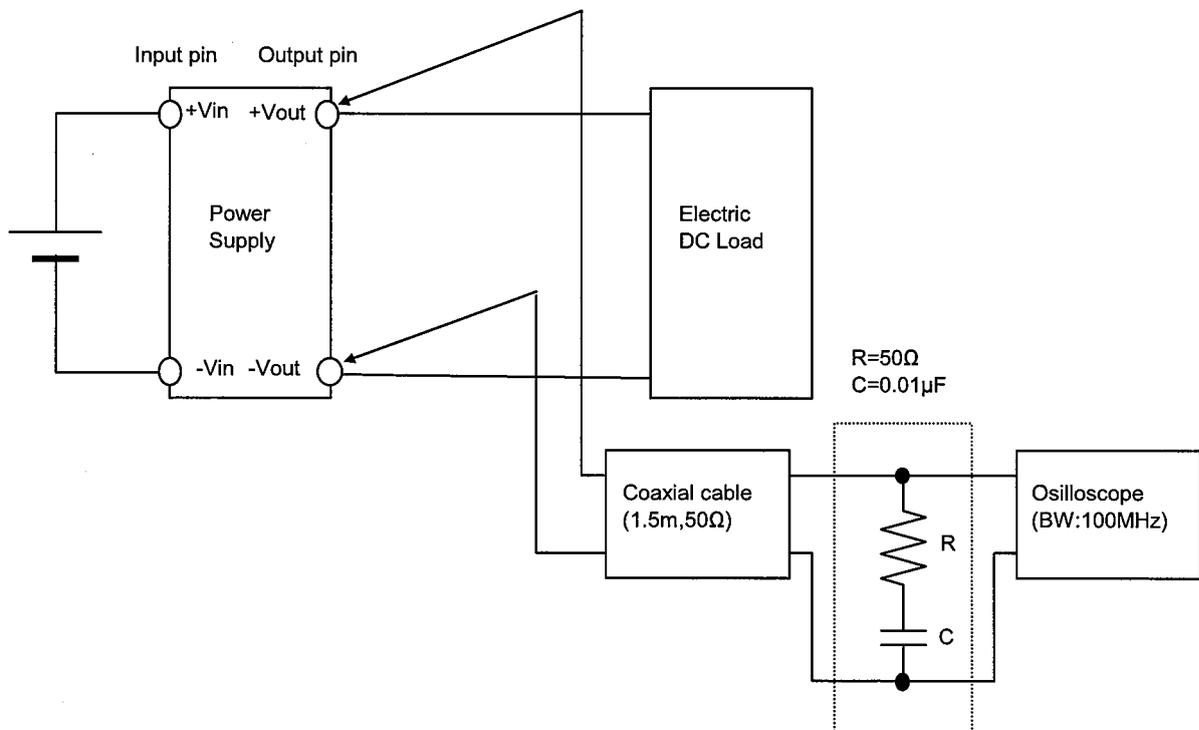


Figure B (Ripple and Ripple noise Characteristic)