

TEST DATA OF STMGFS302415

Regulated DC Power Supply
February 1, 2013

Approved by : Takahiro Yoneda
Takahiro Yoneda Design Manager

Prepared by : Satoshi Kinoshita
Satoshi Kinoshita Design Engineer

COSEL CO.,LTD.

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Model		STMGFS302415		Temperature	25°C																																																																															
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COSEL		
Model	STMGFS302415	
Item	Output Voltage Accuracy	Testing Circuitry Figure A
Object	+15V2A	

1. Output Voltage Accuracy

This is defined as the value of the output voltage, regulation load, ambient temperature and input voltage varied at random in the range as specified below.

Temperature : -20 - 60°C

Input Voltage : 9 - 36V

Load Current : 0 - 2A

* Output Voltage Accuracy = $\pm(\text{Maximum of Output Voltage} - \text{Minimum of Output Voltage}) / 2$

* Output Voltage Accuracy (Ration) = $\frac{\text{Output Voltage Accuracy}}{\text{Rated Output Voltage}} \times 100$

2. Values

Item	Temperature [°C]	Input Voltage[V]	Output		Output Voltage Accuracy	
			Current[A]	Voltage[V]	Value [mV]	Ration [%]
Maximum Voltage	60	18	0	15.126	±36	±0.2
Minimum Voltage	-20	9	2	15.055		



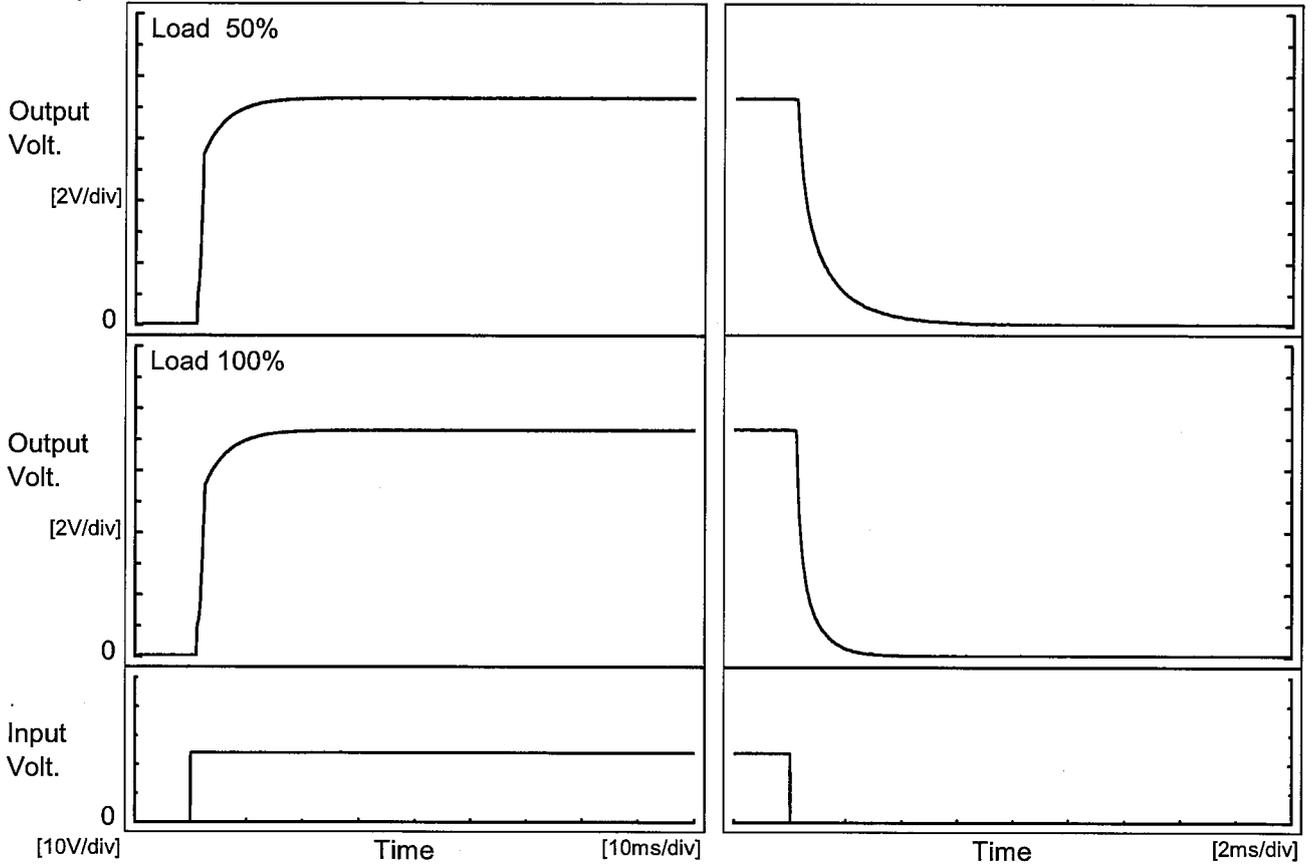
COSEL																								
Model	STMGFS302415																							
Item	Time Lapse Drift	Temperature 25°C Testing Circuitry Figure A																						
Object	+15V2A																							
<p>1.Graph</p> <p style="text-align: center;">Time [H]</p> <p>Input Volt. 24V Load 100%</p>		<p>2.Values</p> <table border="1"> <thead> <tr> <th>Time since start [H]</th> <th>Output Voltage [V]</th> </tr> </thead> <tbody> <tr><td>0.0</td><td>15.093</td></tr> <tr><td>0.5</td><td>15.113</td></tr> <tr><td>1.0</td><td>15.114</td></tr> <tr><td>2.0</td><td>15.114</td></tr> <tr><td>3.0</td><td>15.113</td></tr> <tr><td>4.0</td><td>15.113</td></tr> <tr><td>5.0</td><td>15.113</td></tr> <tr><td>6.0</td><td>15.113</td></tr> <tr><td>7.0</td><td>15.113</td></tr> <tr><td>8.0</td><td>15.113</td></tr> </tbody> </table>	Time since start [H]	Output Voltage [V]	0.0	15.093	0.5	15.113	1.0	15.114	2.0	15.114	3.0	15.113	4.0	15.113	5.0	15.113	6.0	15.113	7.0	15.113	8.0	15.113
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Model	STMGFS302415	Temperature	25°C
Item	Rise and Fall Time	Testing Circuitry	Figure A
Object	+15V2A		

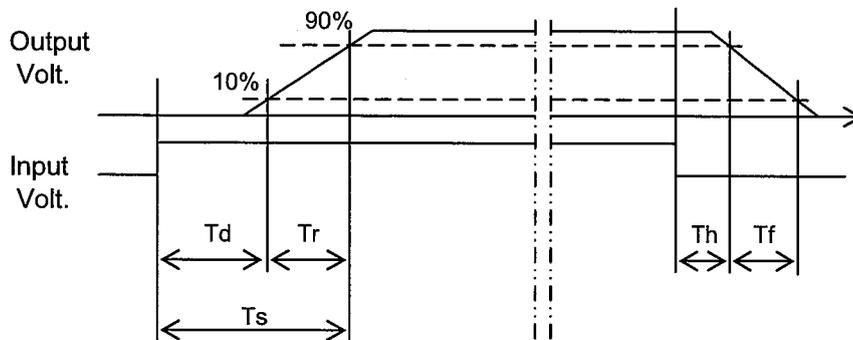
1. Graph

Input Volt. 24 V



2. Values

Load \ Time	Td	Tr	Ts	Th	Tf
50 %	1.2	6.5	7.7	0.2	2.1
100 %	1.2	6.8	8.0	0.2	1.0





<p>Model STMGFS302415</p> <p>Item Minimum Input Voltage for Regulated Output Voltage</p> <p>Object +15V2A</p>		<p>Testing Circuitry Figure A</p>																																						
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<p>Model STMGFS302415</p>		<p>Temperature 25°C Testing Circuitry Figure A</p>																																																																																			
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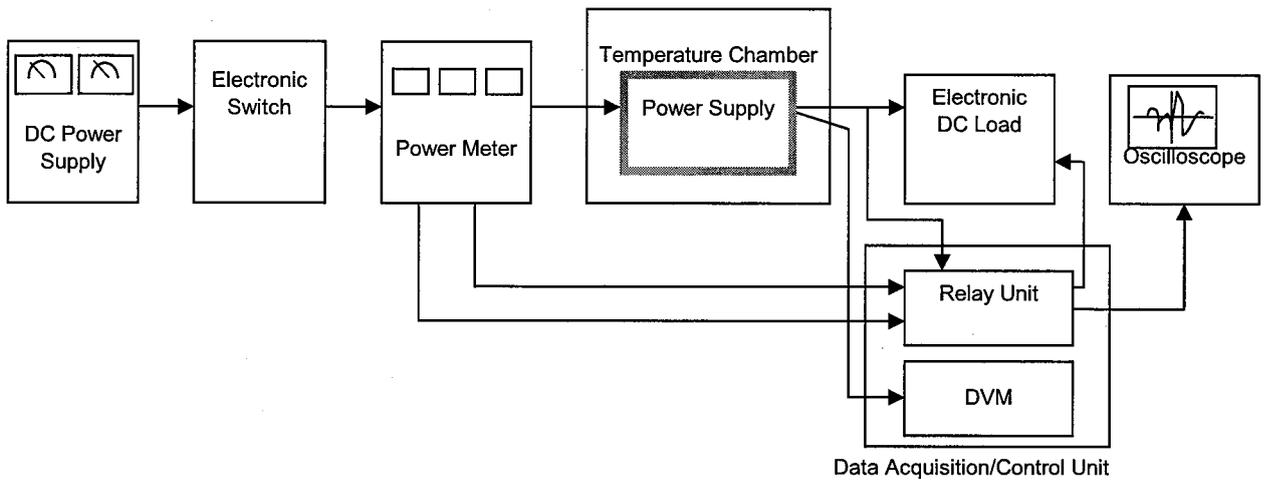


Figure A

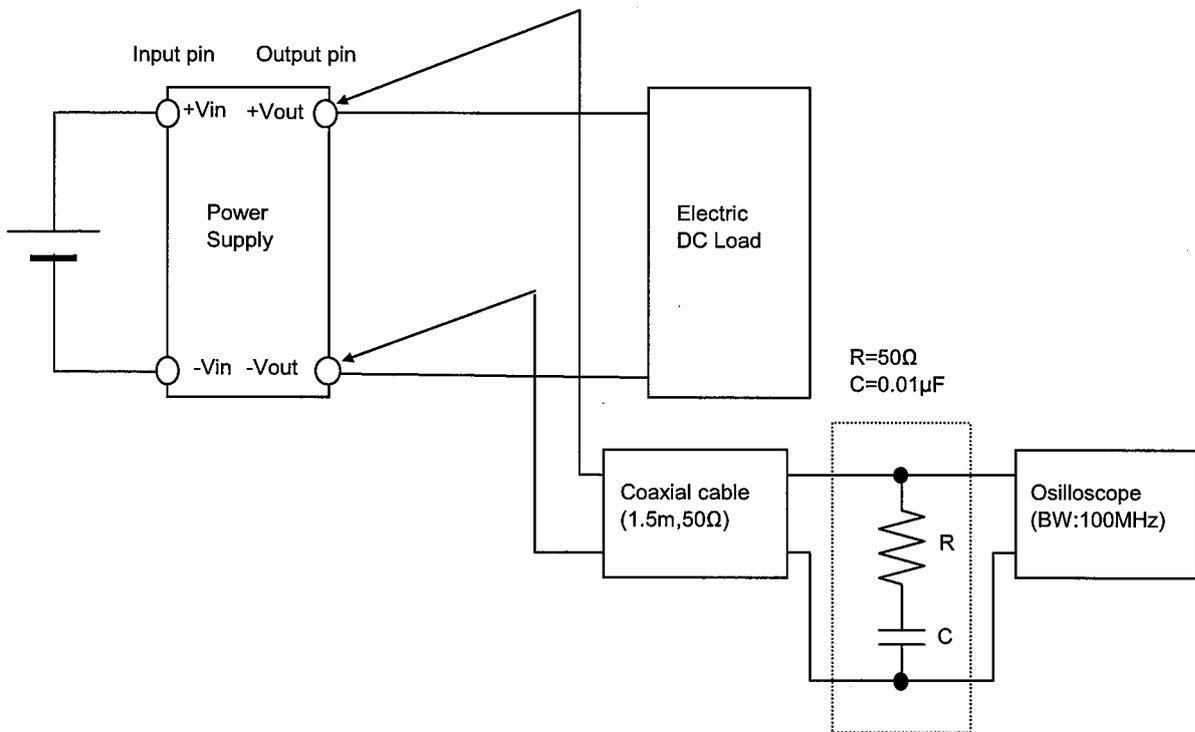


Figure B (Ripple and Ripple noise Characteristic)