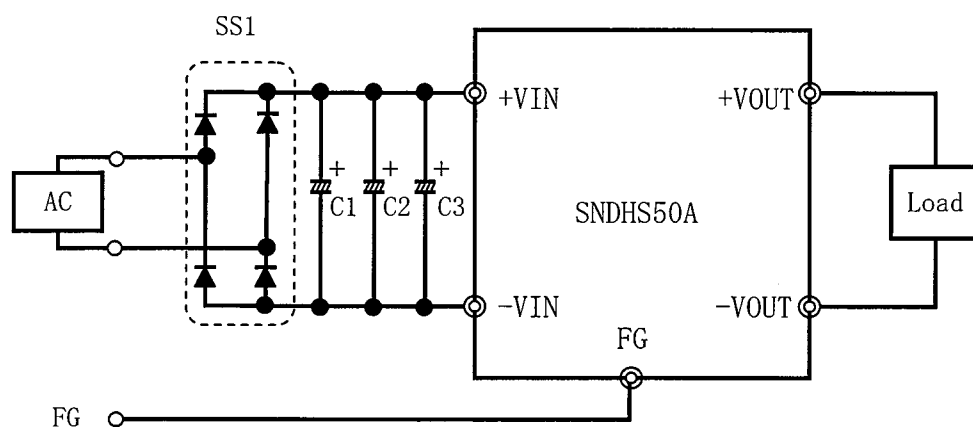


Approved: Takahiro Yoneda
Takahiro YonedaPrepared: Tadashi Arai
Tadashi Arai

No.	Test item	Test conditions	Conditions of acceptability	Result
1	High temp./overload test	(1) Input Max.voltage, Min.voltage (2) Overload (3) Base plate end face temp. : 70°C (4) Test period : 48 hours	(1) Power supply is not failed.	OK
2	Capacitance reduction test	(1) Rated input (DC110V) (2) Rated output (3) Ambient temp. : 25±10°C	(1) Power supply is no fire. (2) No rise of the output voltage.	OK
3	High voltage input test	(1) Input (DC220V) (2) Rated output (3) Ambient temp. : 25±10°C	(1) No smoke, no fire.	OK
4	Low voltage input test	(1) Input Min. regulation voltage (2) Rated output (3) Base plate end face temp. : 95°C (4) Test period 48 hours	(1) Power supply is not failed.	OK
5	Input ON/OFF test	(1) Input : Max.voltage T= 2sec Duty= 50% (2) Output : Rated output (3) Ambient temp. : 25±10°C (4) On/Off period : 1,000	(1)Power supply is not failed. (2)The surge current of each components should not exceed the rated value.	OK
6	Output ON/OFF test	(1) Rated input (DC110V) (2) Output 0%←→100% T= 2sec Duty= 50% (3) Ambient temp. : 25±10°C (4) On/Off period : 1,000	(1) Power supply is not failed.	OK
7	Output-short start test	(1) Rated input (DC110V) (2) Output Short start (3) Ambient temp. : 25±10°C	(1) Power supply is not failed.	OK
8	Output short test	(1) Rated input (DC110V) (2) Output Short (3) Ambient temp. : 25±10°C (4) Test period : 48 hours	(1) Power supply is not failed.	OK
9	Withstand voltage test (High-pot test)	(1) Input : Not applied. (2) Ambient temp. : 25±10°C (3) The applied voltage is 1.4 times of specifications.	(1) Insulation breakdown ,flashover or electric arc is not occurred.	OK
10	Isolation resistance test	(1) Input : Not applied. (2) Ambient temp. : 25±10°C	(1) When a regulation voltage is applied, isolation resistance is 1.4 times of specifications.	OK
11	Vibration/impact test	Vibration (1)f=10~150Hz : 29.4m/s ² (2)3 minutes period (3)60 minutes along X, Y and Z axis Impact (1)294.2m/s ² 11ms (2)Once each X, Y and Z axis	(1) No degradation of electric characteristics after test. (2) No crack at solder joint. (3) No marked damage of appearance.	OK
12	Line Noise Tolerance test	(1) Input AC90V (2) Rated Output (3) Ambient temp. : 25±10°C (4) Test voltage : ±2 kV (5) Pulse width : 50~1000ns (6) Mode : Normal and Common (7) Testing circuitry Fig.1	(1) No protection circuit failure. (2) No output voltage drop with control circuit failure. (3) No any other function failure.	OK

COSEL

SS1 : D10XB60 (SHINDENGEN)
C1, C2, C3 : 100uF

Fig.1 Line Noise Tolerance Testing circuitry