

# TEST DATA OF PDA300F-24

Regulated DC Power Supply  
May 29, 2025

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Design Manager

Prepared by : Terumasa Araki  
Design Engineer

**COSEL CO.,LTD.**



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Model	PDA300F-24																																																					
Item	Input Current (by Load Current)	Temperature 25°C	Testing Circuitry Figure A																																																			
Object	_____	_____	_____																																																			
1. Graph			2. Values																																																			
<p>Graph showing Input Current [A] vs Load Current [A] for PDA300F-24 at 25°C. The graph plots Input Current [A] on the y-axis (0.0 to 5.0) against Load Current [A] on the x-axis (0 to 16). Three curves are shown for Input Voltages: 100V (solid line with triangles), 200V (dashed line with squares), and 230V (dash-dot line with circles). A slanted line indicates the rated load current range.</p>			<table border="1"> <thead> <tr> <th rowspan="2">Load Current [A]</th> <th colspan="3">Input Current [A]</th> </tr> <tr> <th>Input Volt. 100[V]</th> <th>Input Volt. 200[V]</th> <th>Input Volt. 230[V]</th> </tr> </thead> <tbody> <tr> <td>0.0</td><td>0.189</td><td>0.144</td><td>0.145</td></tr> <tr> <td>2.0</td><td>0.771</td><td>0.478</td><td>0.435</td></tr> <tr> <td>4.0</td><td>1.318</td><td>0.738</td><td>0.677</td></tr> <tr> <td>6.0</td><td>1.879</td><td>0.995</td><td>0.905</td></tr> <tr> <td>8.0</td><td>2.445</td><td>1.262</td><td>1.134</td></tr> <tr> <td>10.0</td><td>3.017</td><td>1.534</td><td>1.365</td></tr> <tr> <td>12.0</td><td>3.595</td><td>1.811</td><td>1.601</td></tr> <tr> <td>14.0</td><td>4.180</td><td>2.099</td><td>1.848</td></tr> <tr> <td>15.4</td><td>4.621</td><td>2.301</td><td>2.022</td></tr> <tr> <td>--</td><td>-</td><td>-</td><td>-</td></tr> <tr> <td>--</td><td>-</td><td>-</td><td>-</td></tr> </tbody> </table>	Load Current [A]	Input Current [A]			Input Volt. 100[V]	Input Volt. 200[V]	Input Volt. 230[V]	0.0	0.189	0.144	0.145	2.0	0.771	0.478	0.435	4.0	1.318	0.738	0.677	6.0	1.879	0.995	0.905	8.0	2.445	1.262	1.134	10.0	3.017	1.534	1.365	12.0	3.595	1.811	1.601	14.0	4.180	2.099	1.848	15.4	4.621	2.301	2.022	--	-	-	-	--	-	-	-
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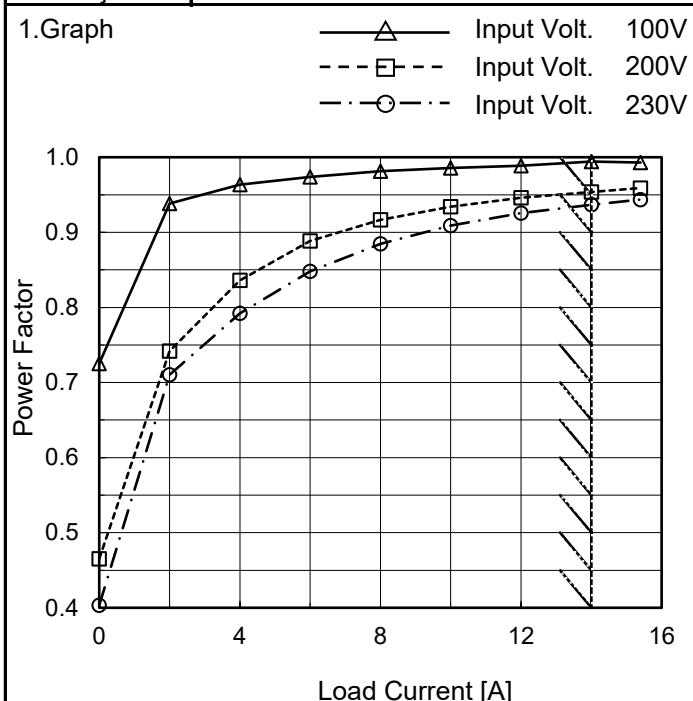
Note: Slanted line shows the range of the rated load current.

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Model	PDA300F-24
Item	Power Factor (by Load Current)
Object	_____


 Temperature 25°C  
 Testing Circuitry Figure A

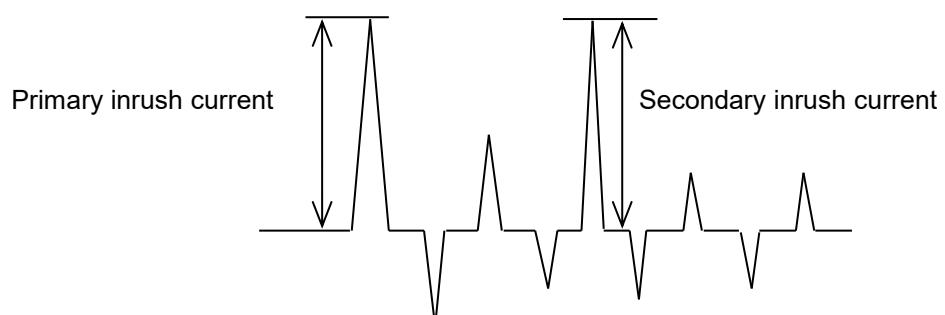
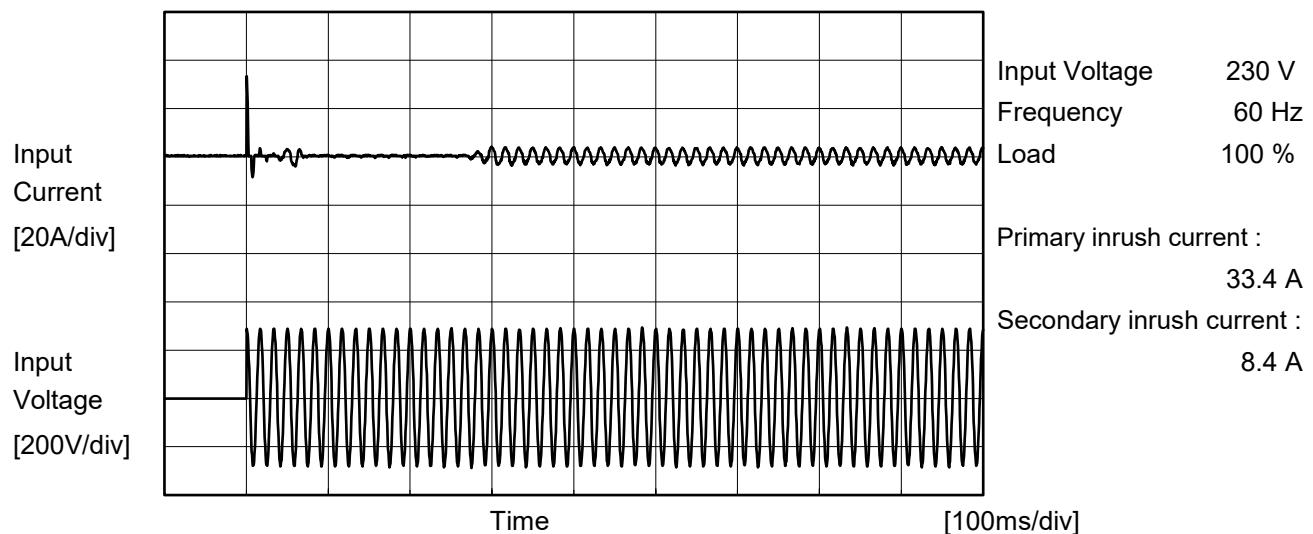
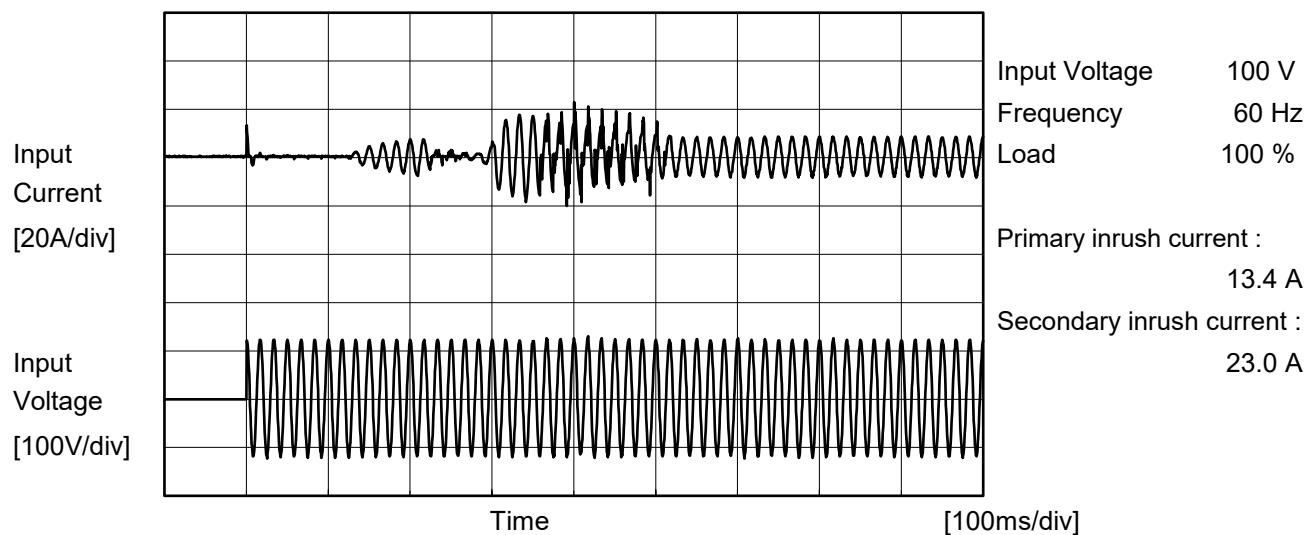
## 2.Values

Load Current [A]	Power Factor		
	Input Volt. 100[V]	Input Volt. 200[V]	Input Volt. 230[V]
0.0	0.725	0.465	0.403
2.0	0.939	0.742	0.710
4.0	0.963	0.836	0.792
6.0	0.974	0.888	0.848
8.0	0.981	0.917	0.885
10.0	0.986	0.934	0.909
12.0	0.989	0.946	0.926
14.0	0.994	0.954	0.937
15.4	0.993	0.959	0.944
--	-	-	-
--	-	-	-

Note: Slanted line shows the range of the rated load current.

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Model	PDA300F-24	Temperature	25°C
Item	Inrush Current	Testing Circuitry	Figure A
Object	_____		





Model	PDA300F-24	Temperature	25°C
Item	Leakage Current	Testing Circuitry	Figure C
Object	_____		

## 1. Results

[mA]

Standards	Testing Circuitry	Measuring Method	Input Volt.			Note
			100 [V]	230 [V]	240 [V]	
DEN-AN	Figure C-1	Both phases	0.19	0.38	0.37	Operation
		One of phases	0.29	0.68	0.71	Stand by
IEC62368-1	Figure C-2	Both phases	0.14	0.35	0.37	Operation
		One of phases	0.26	0.67	0.70	Stand by
	Figure C-3	Both phases	0.14	0.35	0.37	Operation
		One of phases	0.26	0.66	0.69	Stand by

The value for "One of phases" is the reference value only.

## 2. Condition

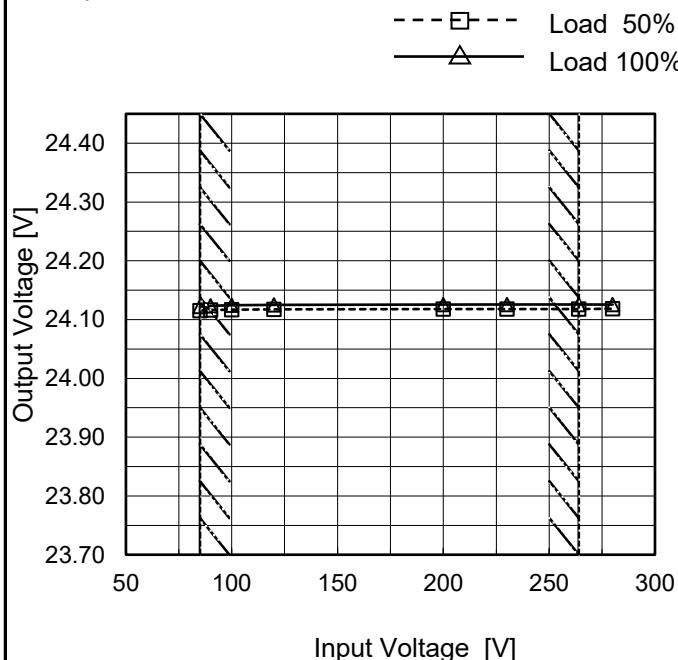
Leakage current value is concluded after measuring both phases of AC input and by choosing the larger one.

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Model	PDA300F-24
Item	Line Regulation
Object	+24V14A

 Temperature 25°C  
 Testing Circuitry Figure A

## 1.Graph

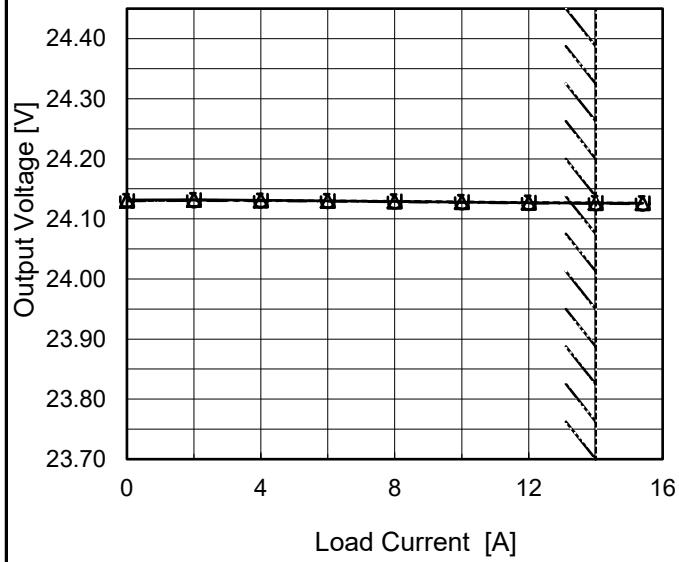
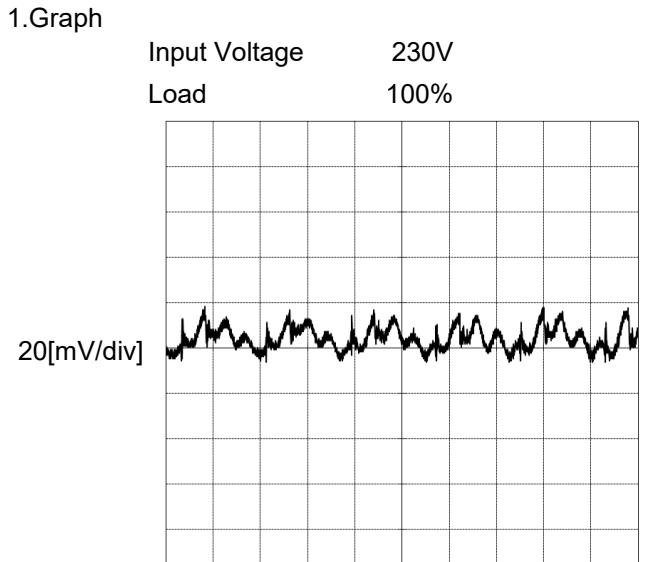


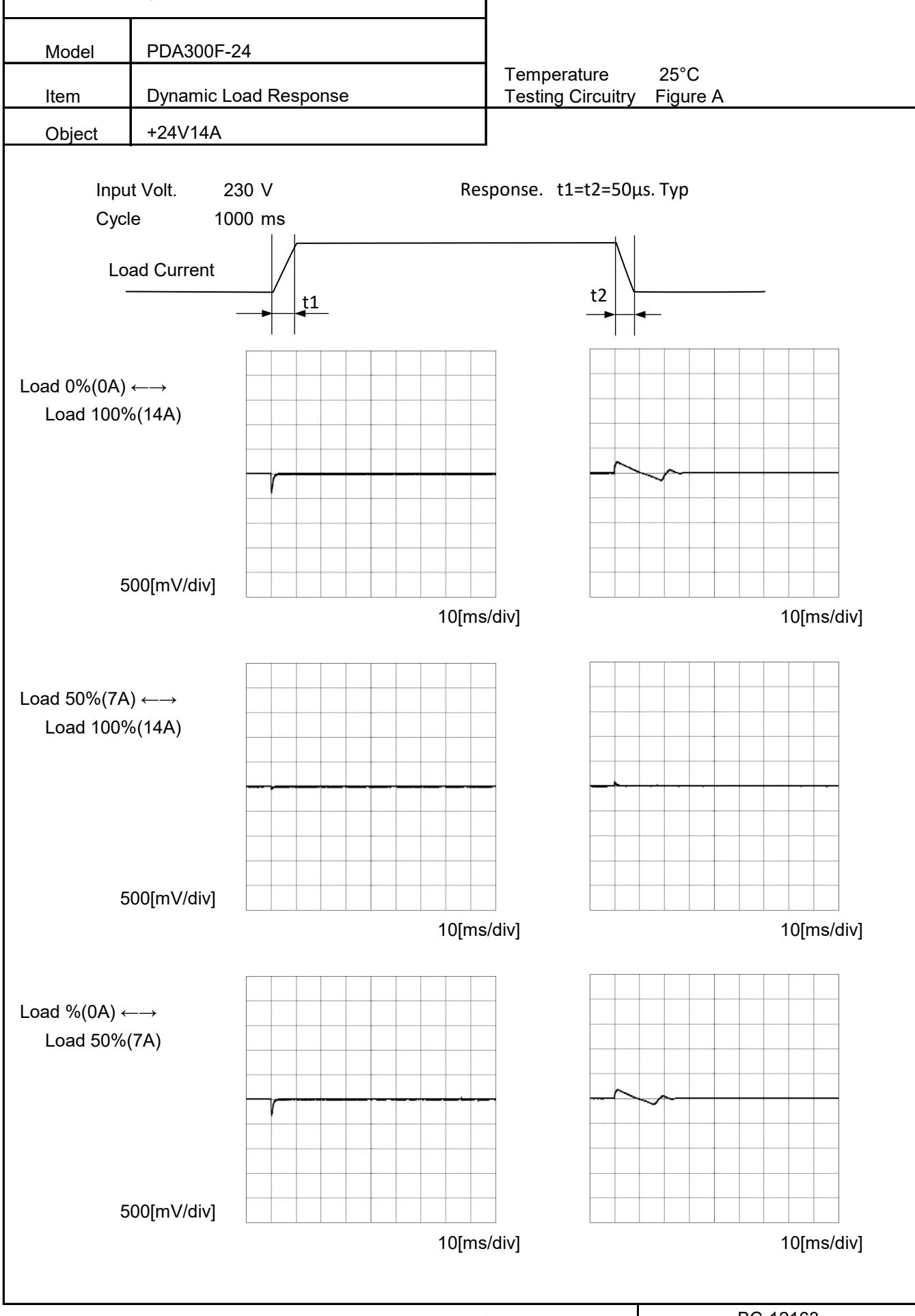
## 2.Values

Input Voltage [V]	Output Voltage [V]	
	Load 50%	Load 100%
85	24.115	24.122
90	24.116	24.123
100	24.117	24.124
120	24.117	24.125
200	24.118	24.125
230	24.118	24.126
264	24.118	24.126
280	24.119	24.126
--	-	-

Note: Slanted line shows the range of the rated input voltage.

**COSEL**

Model	PDA300F-24	Temperature	25°C																																																			
Item	Load Regulation	Testing Circuitry	Figure A																																																			
Object	+24V14A																																																					
1.Graph	<p>—▲— Input Volt. 100V        - - □ - - Input Volt. 200V        - - ○ - - Input Volt. 230V</p>  <p>Output Voltage [V]</p> <p>Load Current [A]</p>																																																					
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Object	+24V14A	Testing Circuitry	Figure B																																																			
1.Graph	<p>Input Voltage 230V        Load 100%</p>  <p>20[mV/div]</p> <p>4[μs/div]</p>																																																					

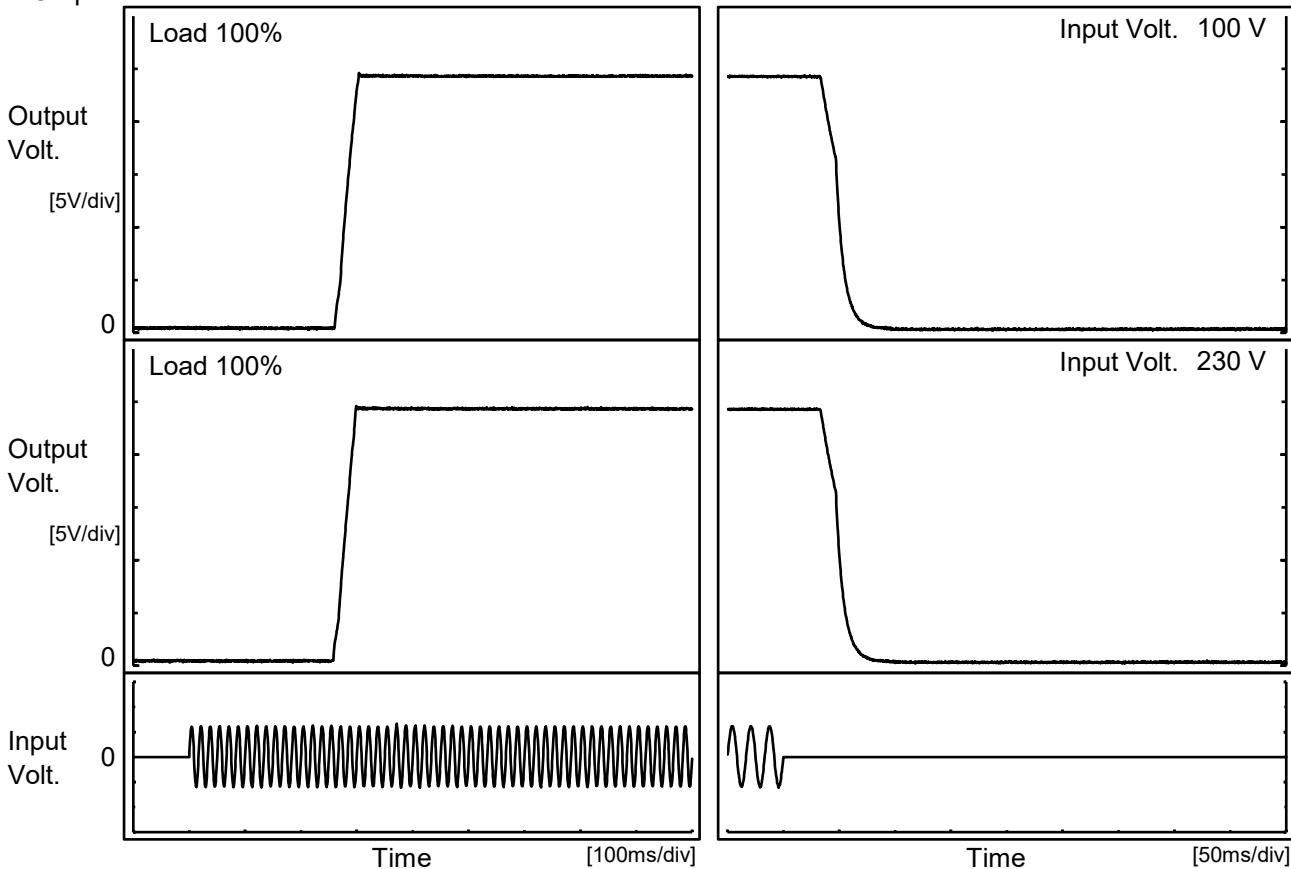
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Model	PDA300F-24
Item	Rise and Fall Time
Object	+24V14A

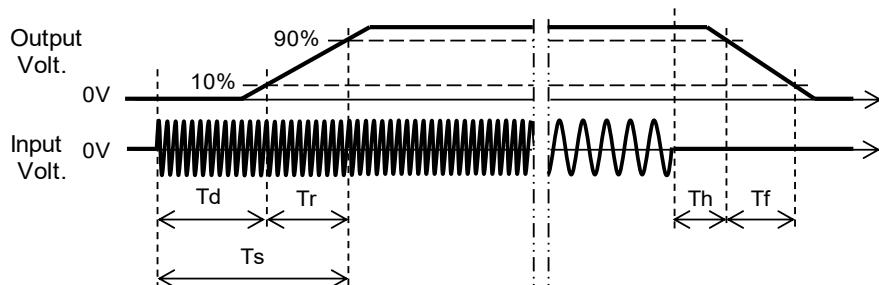
 Temperature 25°C  
 Testing Circuitry Figure A

## 1. Graph



## 2. Values

Input Volt.	Time	Td	Tr	Ts	Th	Tf	[ms]
100 V		264.5	33.0	297.5	37.0	23.8	
230 V		262.5	32.5	295.0	37.0	23.8	



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Item	Hold-Up Time	Testing Circuitry	Figure A																																
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<p>This duration covers from Shut-off of input voltage to the moment when output voltage descends to the rated range of voltage accuracy.</p> <p>Note: Slanted line shows the range of the rated input voltage.</p>																																			

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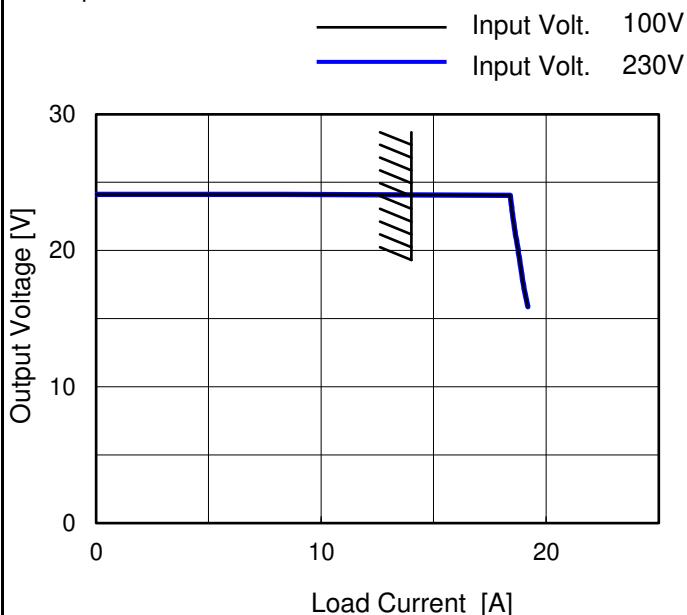
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Note:	Slanted line shows the range of the rated load current.																																																					

**COSEL**

Model	PDA300F-24
Item	Overcurrent Protection
Object	+24V14A

 Temperature 25°C  
 Testing Circuitry Figure A

## 1. Graph



Note: Slanted line shows the range of the rated load current.

Intermittent operation occurs when the output voltage is from 16.8V to 0V.

## 2. Values

Output Voltage [V]	Load Current [A]	
	Input Volt. 100[V]	Input Volt. 230[V]
22.8	18.50	18.51
21.6	18.40	18.41
19.2	18.81	18.82
16.8	19.04	19.05
--	-	-
--	-	-
--	-	-
--	-	-
--	-	-
--	-	-
--	-	-
--	-	-
--	-	-

**COSEL**

Model	PDA300F-24	
Item	Ambient Temperature Drift	Testing Circuitry Figure A
Object	+24V14A	

## 1.Values

Load 100%

Ambient Temperature[°C]	Output Voltage [V]		
	Input Volt. 100V	Input Volt. 200V	Input Volt. 230V
-20	24.036	24.037	24.039
25	24.120	24.121	24.122
50	24.158	24.158	24.159

Item	Minimum Input Voltage for Regulated Output Voltage	Testing Circuitry Figure A
Object	+24V14A	

## 1.Values

Ambient Temperature[°C]	Input Voltage [V]	
	Load 50%	Load 100%
-20	67	69
25	67	69
50	67	69

Item	Overvoltage Protection	Testing Circuitry Figure A
Object	+24V14A	

## 1.Values

Load 0%

Ambient Temperature[°C]	Operating Point [V]	
	Input Volt. 100V	Input Volt. 230V
-20	33.89	33.89
25	34.07	34.07
50	34.06	34.06

**COSEL**

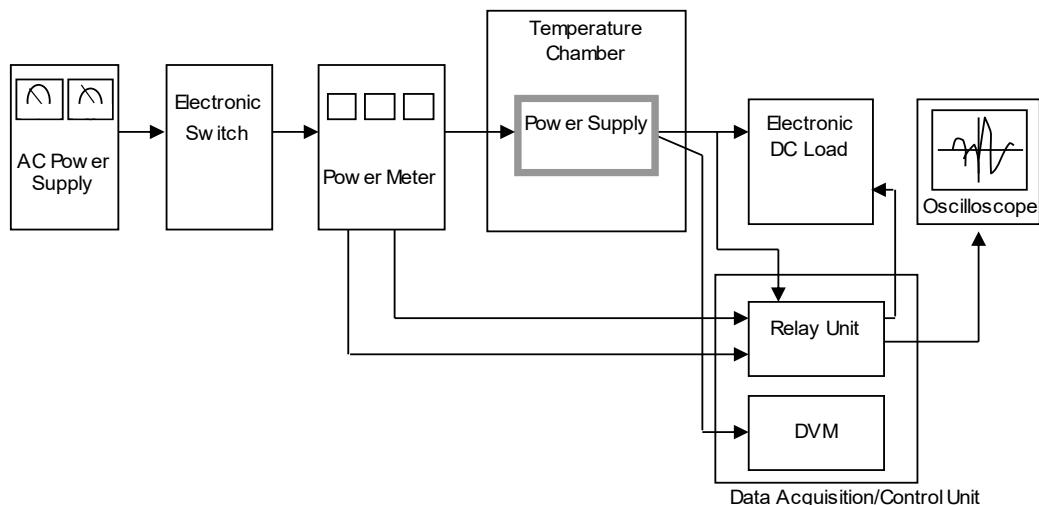
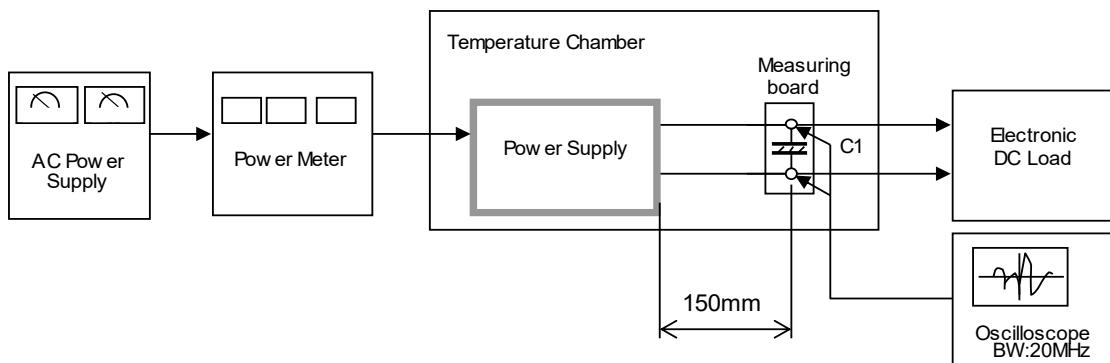


Figure A



$$C1 = 22 \mu F \\ (\text{Electrolytic capacitor})$$

Figure B

**COSEL**

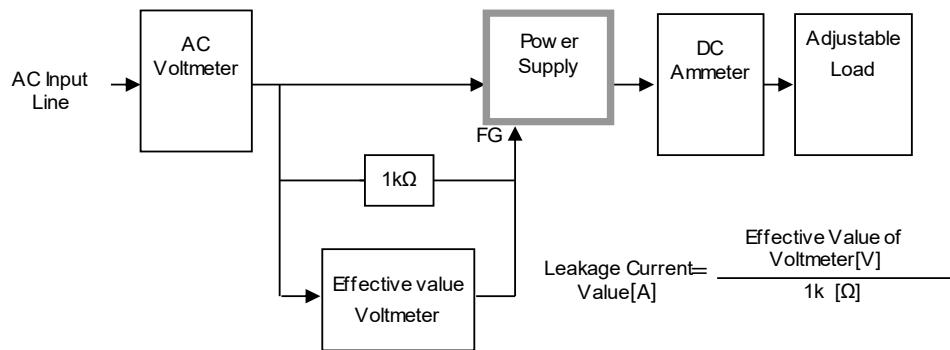


Figure C-1 ( DEN-AN )

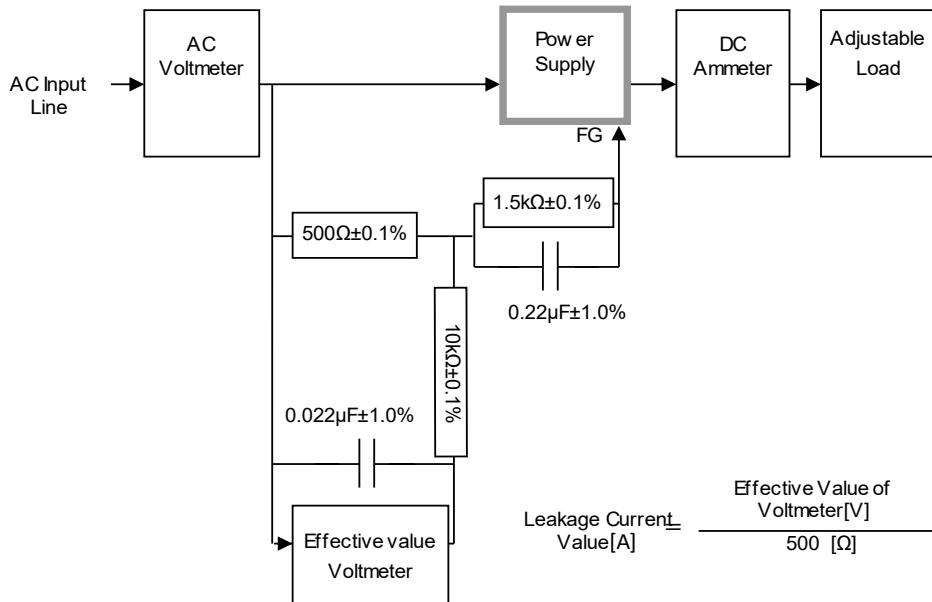


Figure C-2 ( IEC62368-1 refer to IEC60990 Fig.4 )

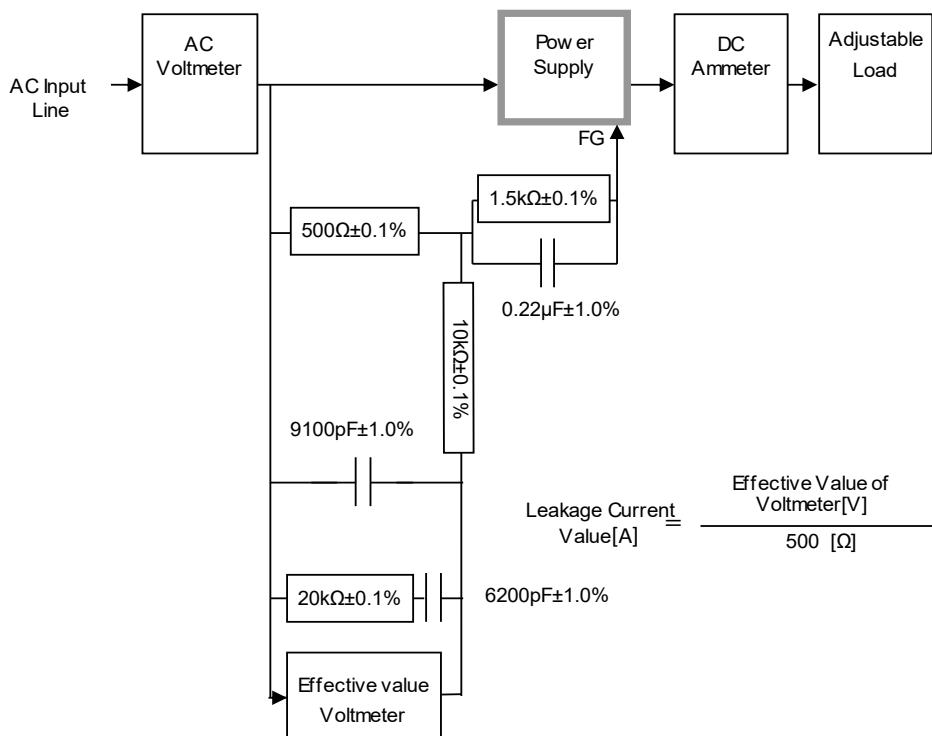


Figure C-3 ( IEC62368-1 refer to IEC60990 Fig.5 )