

TEST DATA OF PDA100F-12

Regulated DC Power Supply
December 12, 2024

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Design Manager

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Design Engineer

COSEL CO.,LTD.

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Model		PDA100F-12		Temperature Testing Circuitry	25°C Figure A																																																			
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Object																																																								
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Model

PDA100F-12

Item

Efficiency (by Load Current)

Object

1.Graph

—△—

Input Volt. 100V

---□---

Input Volt. 200V

-·-○-·-

Input Volt. 230V

Efficiency [%]

100

92

84

76

68

60

52

44

0

2

4

6

8

10

Load Current [A]

Note: Slanted line shows the range of the rated load current.

2.Values

Load Current [A]	Efficiency [%]		
	Input Volt. 100[V]	Input Volt. 200[V]	Input Volt. 230[V]
0.00	-	-	-
1.50	83.1	82.1	80.4
3.00	87.2	88.0	87.3
4.50	88.2	89.7	89.5
6.00	88.5	90.4	90.4
7.50	88.9	90.9	91.1
8.50	88.9	91.0	91.1
9.35	88.7	91.0	91.1
--	-	-	-
--	-	-	-
--	-	-	-

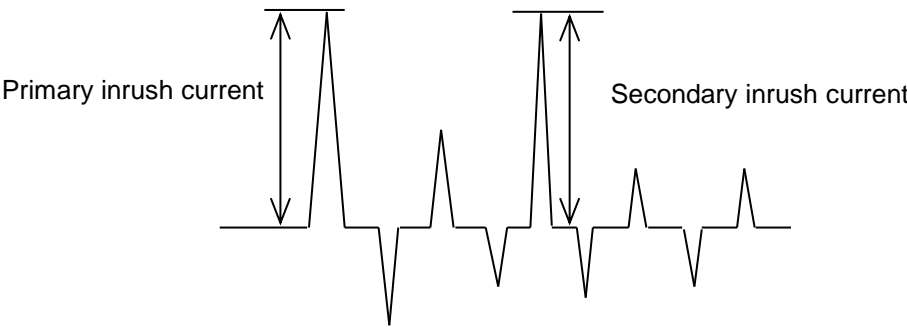
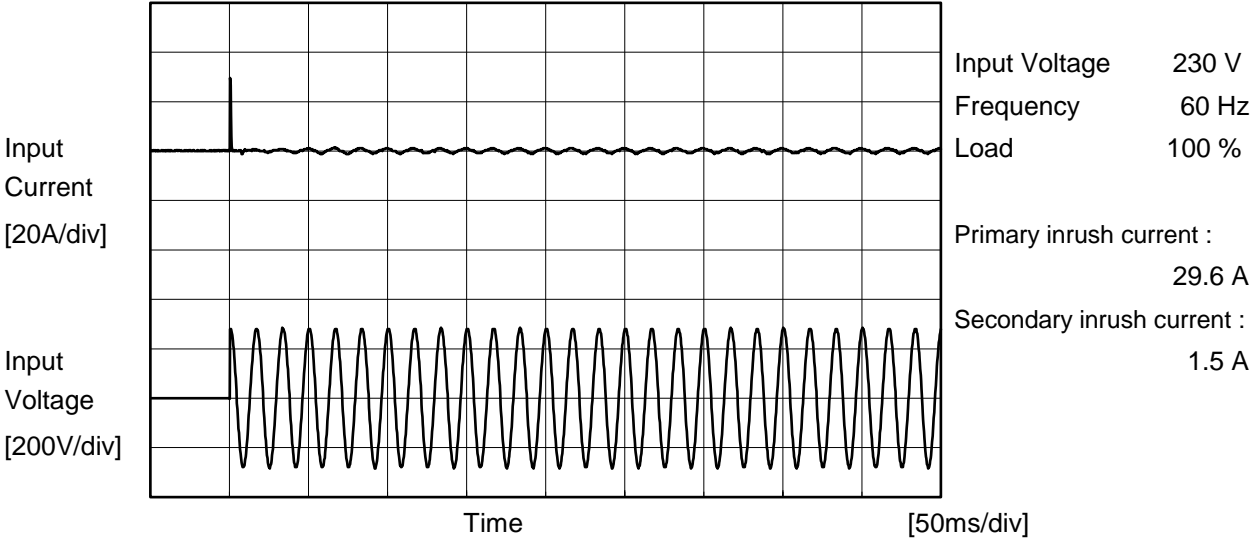
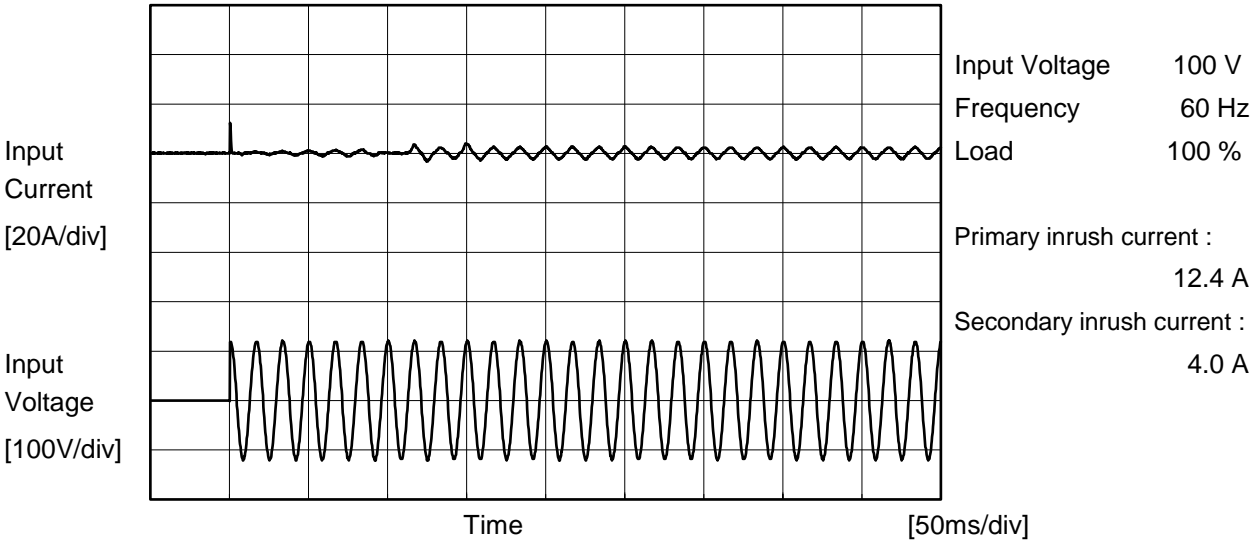
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[illegible]



Model		PDA100F-12	
Item		Inrush Current	
Object		_____	
Temperature		25°C	
Testing Circuitry		Figure A	





Model		PDA100F-12	Temperature 25°C Testing Circuitry Figure C
Item		Leakage Current	
Object		_____	

1.Results

[mA]

Standards	Testing Circuitry	Measuring Method	Input Volt.			Note
			100 [V]	230 [V]	240 [V]	
DEN-AN	Figure C-1	Both phases	0.14	0.37	0.38	Operation
		One of phases	0.28	0.70	0.73	Stand by
IEC62368-1	Figure C-2	Both phases	0.14	0.36	0.37	Operation
		One of phases	0.27	0.69	0.72	Stand by
	Figure C-3	Both phases	0.14	0.35	0.37	Operation
		One of phases	0.27	0.67	0.71	Stand by

The value for "One of phases" is the reference value only.

2.Condition

Leakage current value is concluded after measuring both phases of AC input and by choosing the larger one.



Model		PDA100F-12	Temperature25°C Testing CircuitryFigure A
Item		Line Regulation	
Object		+12V8.5A	
1.Graph			2.Values
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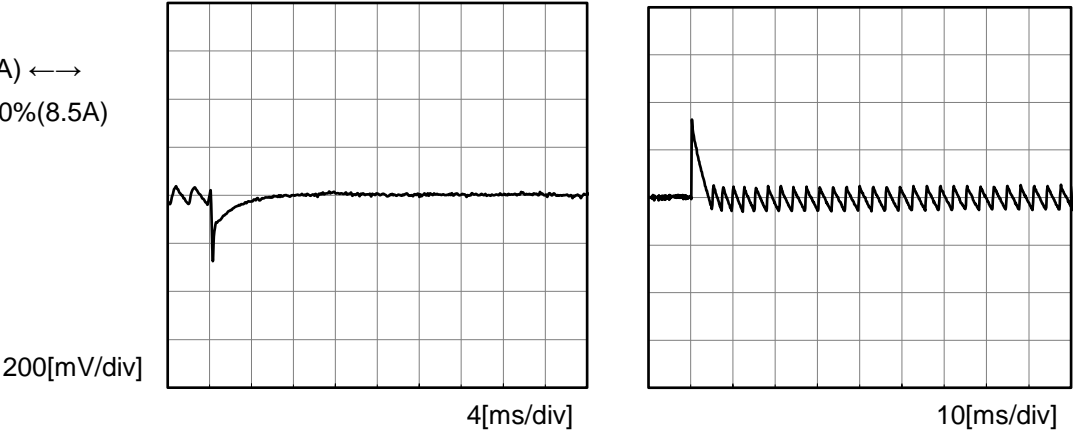


Model		PDA100F-12	Temperature 25°C Testing Circuitry Figure A
Item		Dynamic Load Response	
Object		+12V8.5A	

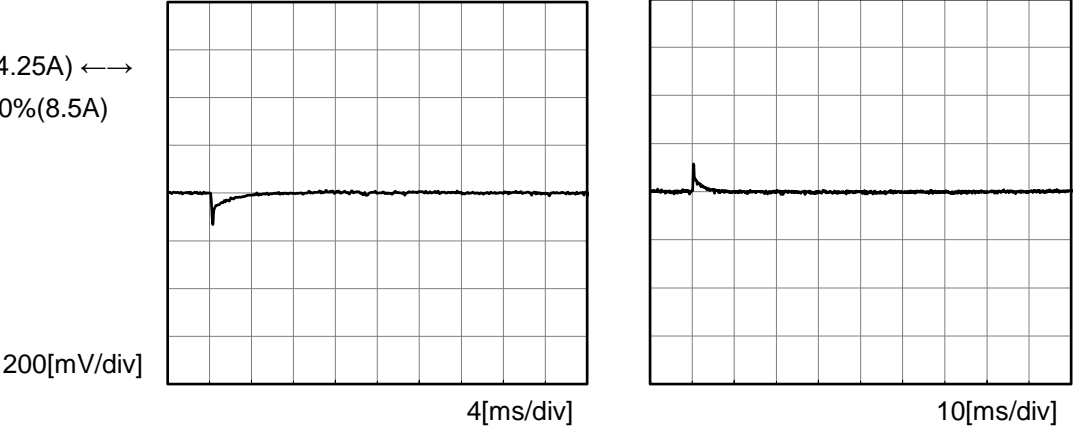
Input Volt. 230 V Response. t1=t2=50μs. Typ
Cycle 1000 ms



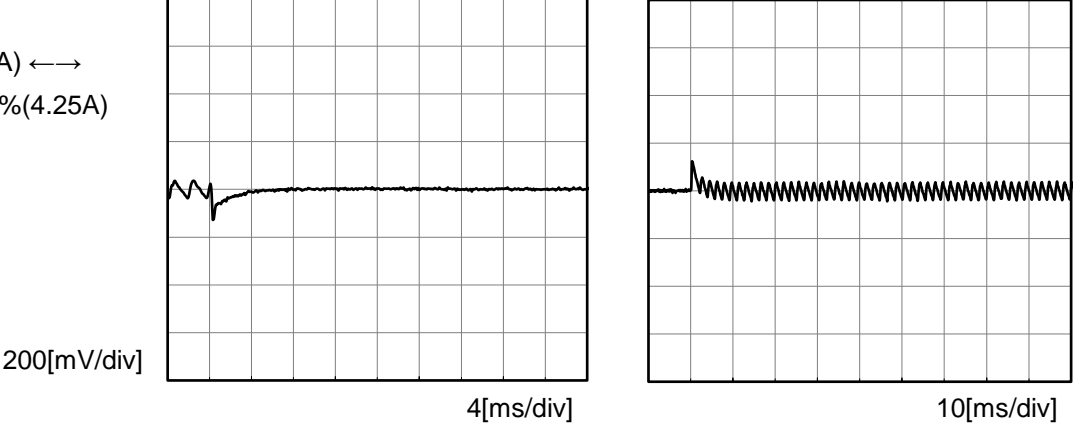
Load 0%(0A) ←→
Load 100%(8.5A)



Load 50%(4.25A) ←→
Load 100%(8.5A)

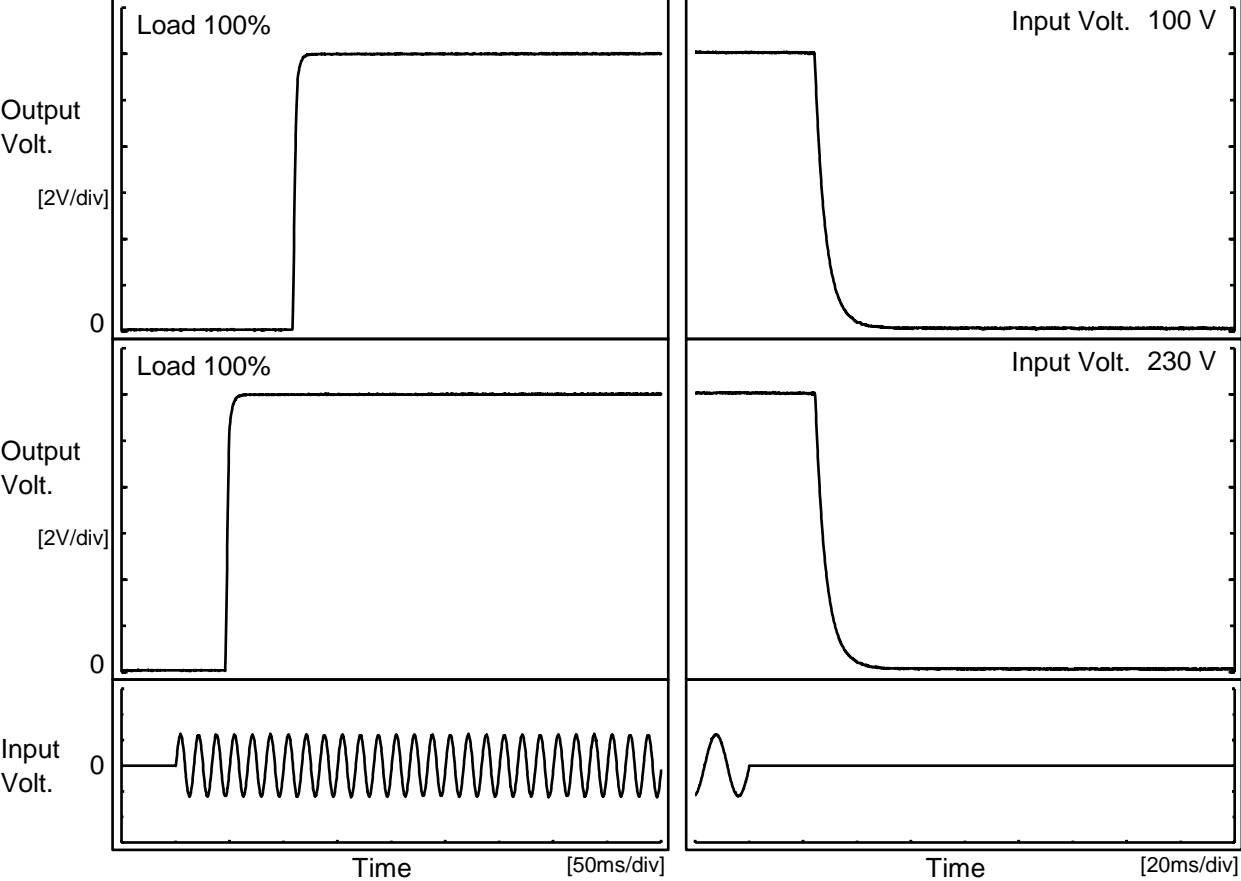


Load 0%(0A) ←→
Load 50%(4.25A)



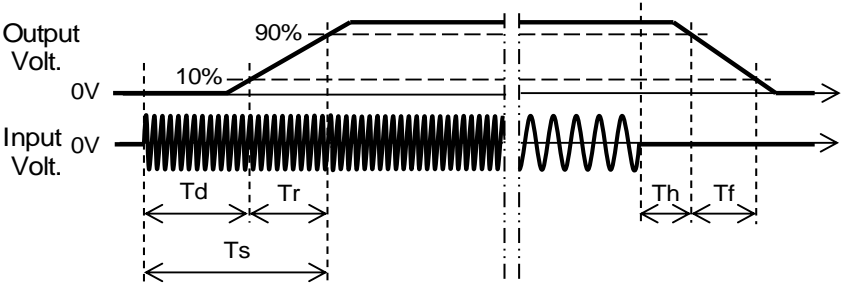
Model		PDA100F-12	Temperature 25°C Testing Circuitry Figure A
Item		Rise and Fall Time	
Object		+12V8.5A	

1.Graph



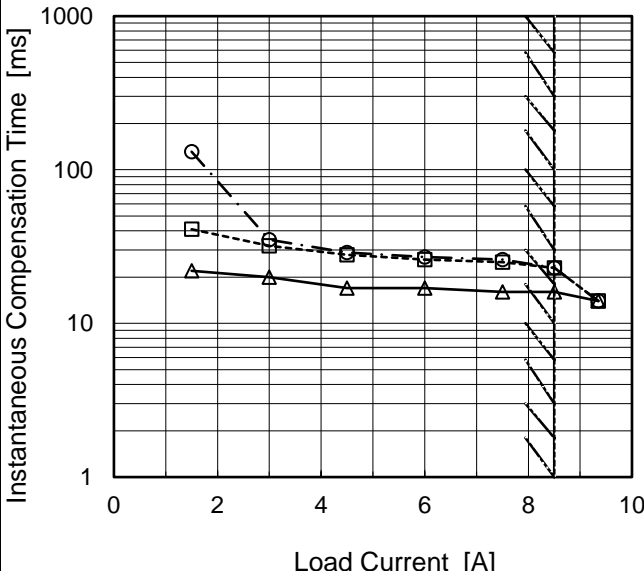
2.Values

		[ms]				
Input Volt.	Time	Td	Tr	Ts	Th	Tf
100 V		109.3	4.0	113.3	24.6	9.2
230 V		46.8	4.3	51.1	24.8	9.0





Model		PDA100F-12	Temperature 25°C Testing Circuitry Figure A
Item		Hold-Up Time	
Object		+12V8.5A	
1.Graph			2.Values
<div><div><div><div></div><div></div></div><div><div></div><div></div></div></div><div><div>Load 50%</div><div>Load 100%</div></div></div> <p>Hold-Up Time [ms]</p> <p>Input Voltage [V]</p>			
<p>This duration covers from Shut-off of input voltage to the moment when output voltage descends to the rated range of voltage accuracy.</p> <p>Note: Slanted line shows the range of the rated input voltage.</p>			

Model		PDA100F-12		Temperature 25°C																																																				
Item		Instantaneous Interruption Compensation		Testing Circuitry Figure A																																																				
Object		+12V8.5A																																																						
1.Graph		<div><div><div>—△—</div><div>Input Volt. 100V</div></div><div><div>---□---</div><div>Input Volt. 200V</div></div><div><div>-·-○-·-</div><div>Input Volt. 230V</div></div></div>  <p>Instantaneous Compensation Time [ms]</p> <p>Load Current [A]</p> <p>Note: Slanted line shows the range of the rated load current.</p>		2.Values																																																				
		<table><tr><th rowspan="2">Load Current [A]</th><th colspan="3">Time [ms]</th></tr><tr><th>Input Volt. 100[V]</th><th>Input Volt. 200[V]</th><th>Input Volt. 230[V]</th></tr><tr><td>0.00</td><td>-</td><td>-</td><td>-</td></tr><tr><td>1.50</td><td>22</td><td>41</td><td>131</td></tr><tr><td>3.00</td><td>20</td><td>32</td><td>35</td></tr><tr><td>4.50</td><td>17</td><td>28</td><td>29</td></tr><tr><td>6.00</td><td>17</td><td>26</td><td>27</td></tr><tr><td>7.50</td><td>16</td><td>25</td><td>26</td></tr><tr><td>8.50</td><td>16</td><td>23</td><td>23</td></tr><tr><td>9.35</td><td>14</td><td>14</td><td>14</td></tr><tr><td>--</td><td>-</td><td>-</td><td>-</td></tr><tr><td>--</td><td>-</td><td>-</td><td>-</td></tr><tr><td>--</td><td>-</td><td>-</td><td>-</td></tr></table>				Load Current [A]	Time [ms]			Input Volt. 100[V]	Input Volt. 200[V]	Input Volt. 230[V]	0.00	-	-	-	1.50	22	41	131	3.00	20	32	35	4.50	17	28	29	6.00	17	26	27	7.50	16	25	26	8.50	16	23	23	9.35	14	14	14	--	-	-	-	--	-	-	-	--	-	-	-
Load Current [A]	Time [ms]																																																							
	Input Volt. 100[V]	Input Volt. 200[V]	Input Volt. 230[V]																																																					
0.00	-	-	-																																																					
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8.50	16	23	23																																																					
9.35	14	14	14																																																					
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<div>LUCEL</div>																																												
Model	PDA100F-12																																											
Item	Overcurrent Protection	Temperature	25°C																																									
		Testing Circuitry	Figure A																																									
Object	+12V8.5A																																											
1.Graph		2.Values																																										
<div><div><div></div>Input Volt. 100V</div><div><div></div>Input Volt. 230V</div></div> <p>Note: Slanted line shows the range of the rated load current.</p>		<table><tr><th rowspan="2">Output Voltage [V]</th><th colspan="2">Load Current [A]</th></tr><tr><th>Input Volt. 100[V]</th><th>Input Volt. 230[V]</th></tr><tr><td>12.00</td><td>10.26</td><td>10.26</td></tr><tr><td>11.40</td><td>-</td><td>-</td></tr><tr><td>10.80</td><td>-</td><td>-</td></tr><tr><td>9.60</td><td>-</td><td>-</td></tr><tr><td>8.40</td><td>-</td><td>-</td></tr><tr><td>7.20</td><td>-</td><td>-</td></tr><tr><td>6.00</td><td>-</td><td>-</td></tr><tr><td>4.80</td><td>-</td><td>-</td></tr><tr><td>3.60</td><td>-</td><td>-</td></tr><tr><td>2.40</td><td>-</td><td>-</td></tr><tr><td>1.20</td><td>-</td><td>-</td></tr><tr><td>0.00</td><td>-</td><td>-</td></tr></table>		Output Voltage [V]	Load Current [A]		Input Volt. 100[V]	Input Volt. 230[V]	12.00	10.26	10.26	11.40	-	-	10.80	-	-	9.60	-	-	8.40	-	-	7.20	-	-	6.00	-	-	4.80	-	-	3.60	-	-	2.40	-	-	1.20	-	-	0.00	-	-
Output Voltage [V]	Load Current [A]																																											
	Input Volt. 100[V]	Input Volt. 230[V]																																										
12.00	10.26	10.26																																										
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3.60	-	-																																										
2.40	-	-																																										
1.20	-	-																																										
0.00	-	-																																										



Model	PDA100F-12		
Item	Ambient Temperature Drift	Testing Circuitry Figure A	
Object	+12V8.5A		
1.Values		Load 100%	
Ambient Temperature[°C]	Output Voltage [V]		
	Input Volt. 100V	Input Volt. 200V	Input Volt. 230V
-10	12.051	12.051	12.051
25	12.076	12.077	12.076
50	12.076	12.076	12.077
Item	Minimum Input Voltage for Regulated Output Voltage	Testing Circuitry Figure A	
Object	+12V8.5A		
1.Values			
Ambient Temperature[°C]	Input Voltage [V]		
	Load 50%	Load 100%	
-10	43	56	
25	42	57	
50	41	59	
Item	Overvoltage Protection	Testing Circuitry Figure A	
Object	+12V8.5A		
1.Values		Load 0%	
Ambient Temperature[°C]	Operating Point [V]		
	Input Volt. 100V	Input Volt. 230V	
-20	16.43	16.43	
25	16.81	16.81	
50	17.05	17.05	

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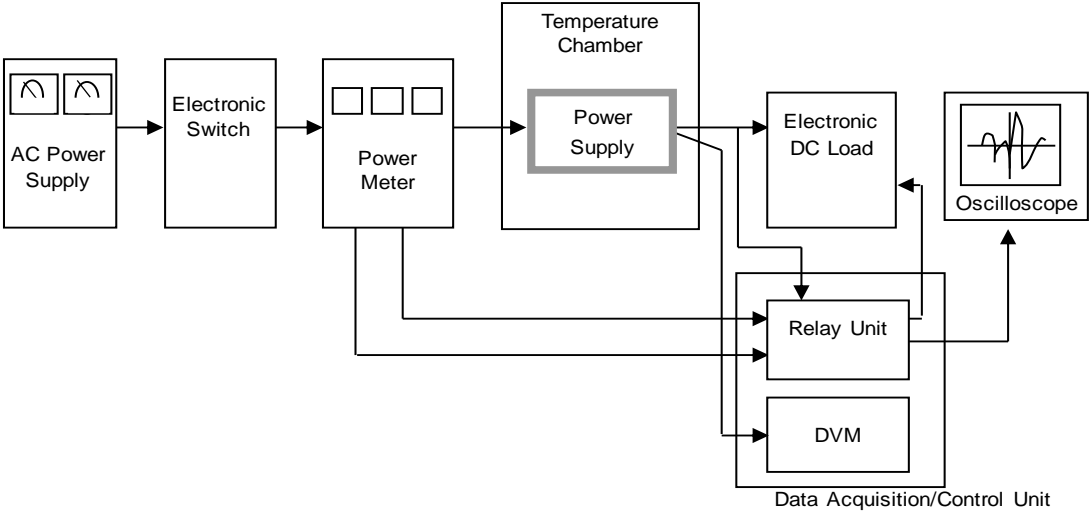


Figure A

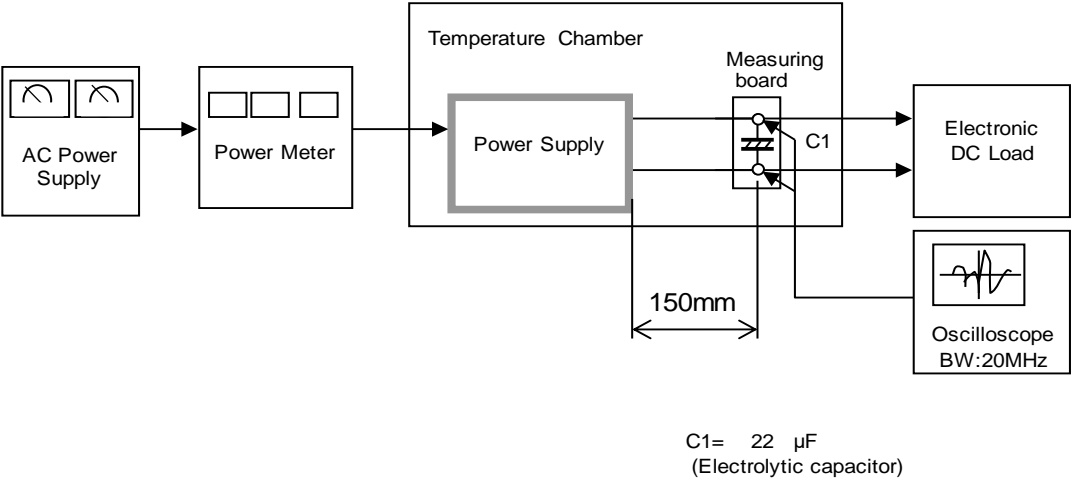


Figure B

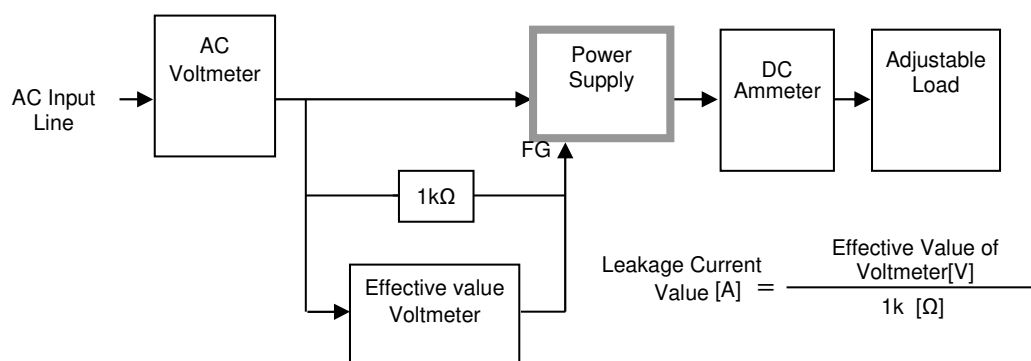


Figure C-1 (DEN-AN)

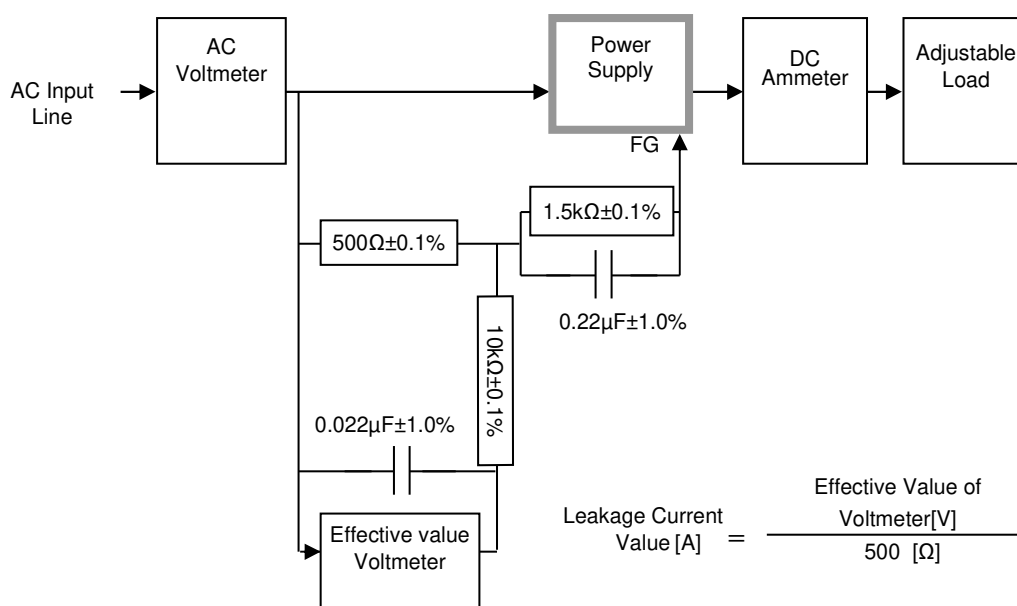


Figure C-2 (IEC62368-1 refer to IEC60990 Fig.4)

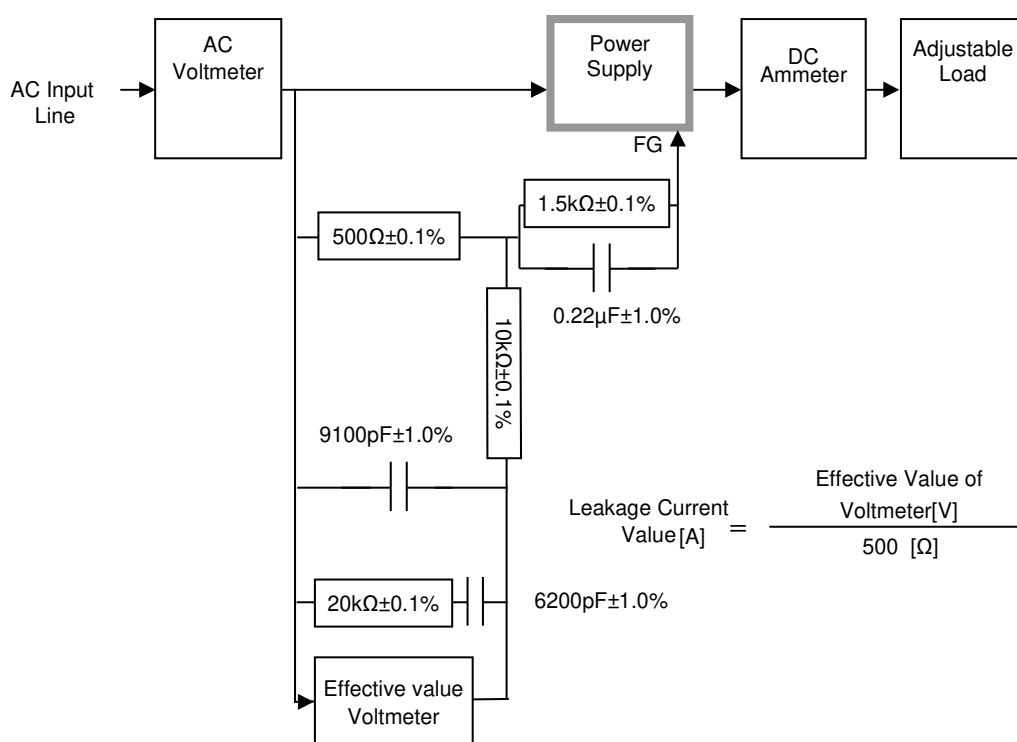


Figure C-3 (IEC62368-1 refer to IEC60990 Fig.5)