

TEST DATA OF MUS32415

Regulated DC Power Supply
February 3, 2025

Approved by : Kenichi Tsukada
Design Manager

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Design Engineer

COSEL CO.,LTD.



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Model	MUS32415	Temperature	25°C																																																			
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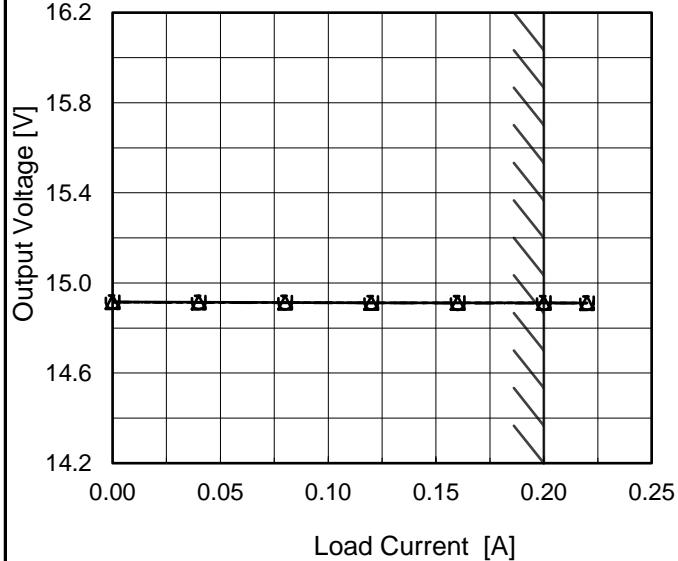
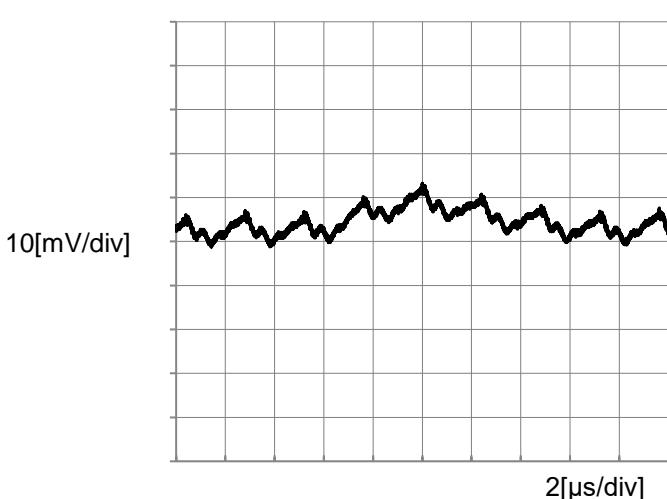
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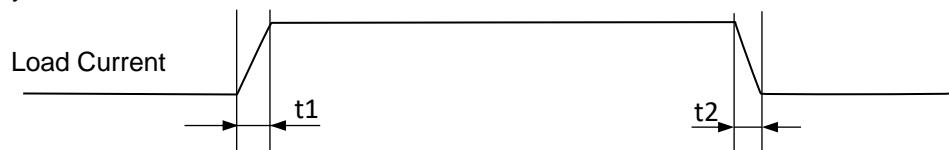
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Item	Ripple-Noise	Temperature	25°C																																																			
Object	+15V0.2A	Testing Circuitry	Figure B																																																			
1.Graph	<p>Input Voltage 24V Load 100%</p> 																																																					

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Model	MUS32415	Temperature Testing Circuitry Figure A
Item	Dynamic Load Response	
Object	+15V0.2A	

Input Volt. 24 V Response. $t_1=t_2=50\mu\text{s}$. Typ

Cycle 1000 ms

Load 0%(0A) \longleftrightarrow Load 100%(0.2A)

200[mV/div]

1[ms/div]

1[ms/div]

Load 50%(0.1A) \longleftrightarrow Load 100%(0.2A)

200[mV/div]

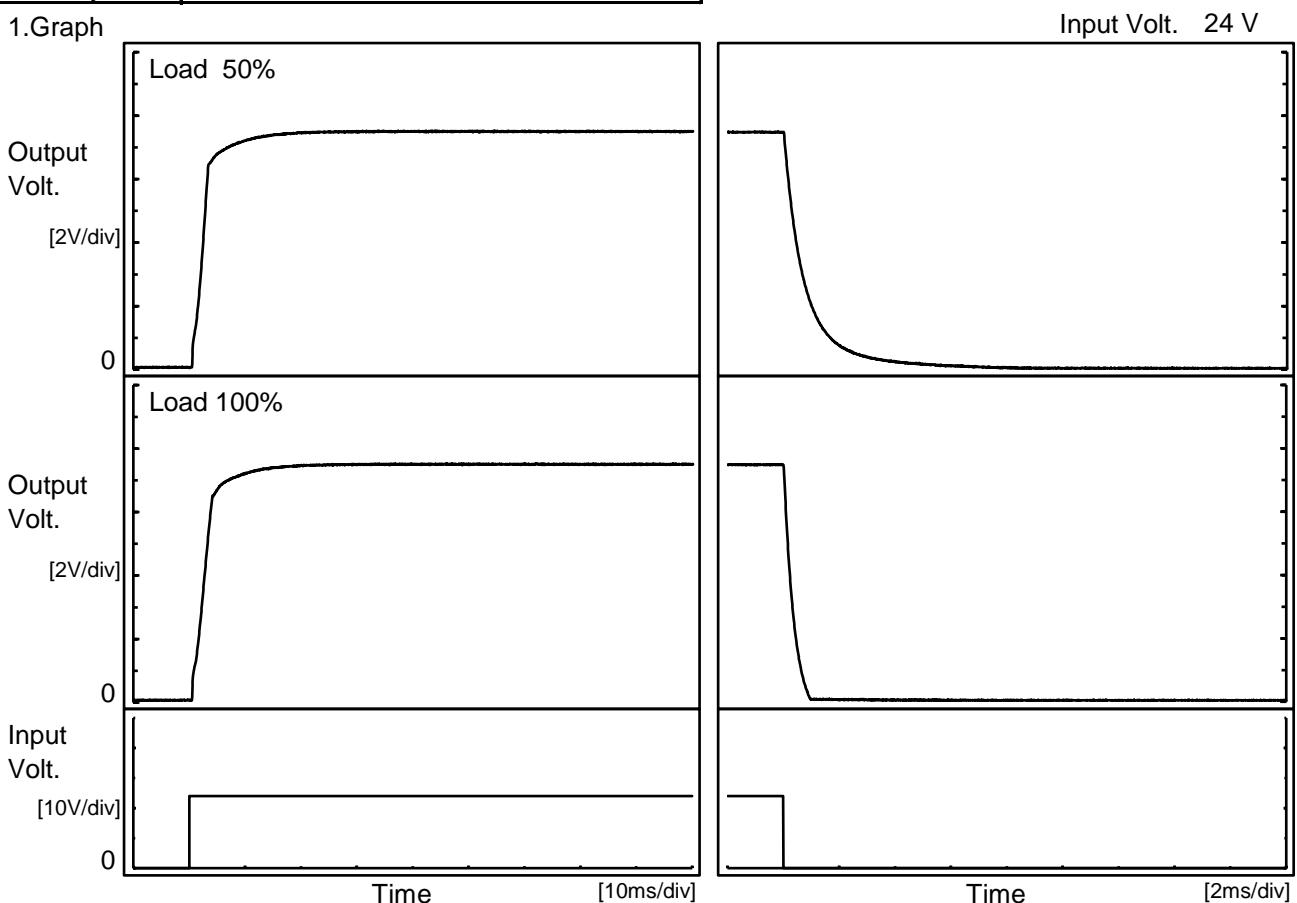
1[ms/div]

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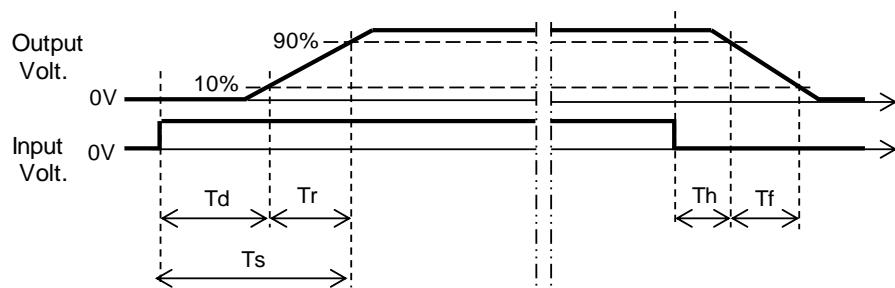
Model	MUS32415	Temperature	25°C
Item	Rise and Fall Time	Testing Circuitry	Figure A
Object	+15V0.2A		

1. Graph



2. Values

Load	Time	Td	Tr	Ts	Th	Tf	[ms]
50 %		0.7	4.2	4.9	0.1	1.9	
100 %		0.7	4.7	5.4	0.1	0.6	



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Item	Overcurrent Protection	Testing Circuitry	Figure A																																																							
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Model	MUS32415	
Item	Ambient Temperature Drift	Testing Circuitry Figure A
Object	+15V0.2A	

1.Values

Load 100%

Ambient Temperature[°C]	Output Voltage [V]		
	Input Volt. 18V	Input Volt. 24V	Input Volt. 36V
-40	14.810	14.813	14.814
25	14.910	14.910	14.911
85	14.943	14.943	14.943

Item	Minimum Input Voltage for Regulated Output Voltage	Testing Circuitry Figure A
Object	+15V0.2A	

1.Values

Ambient Temperature[°C]	Input Voltage [V]	
	Load 50%	Load 100%
-40	13.8	13.8
25	13.8	13.8
85	13.8	13.8

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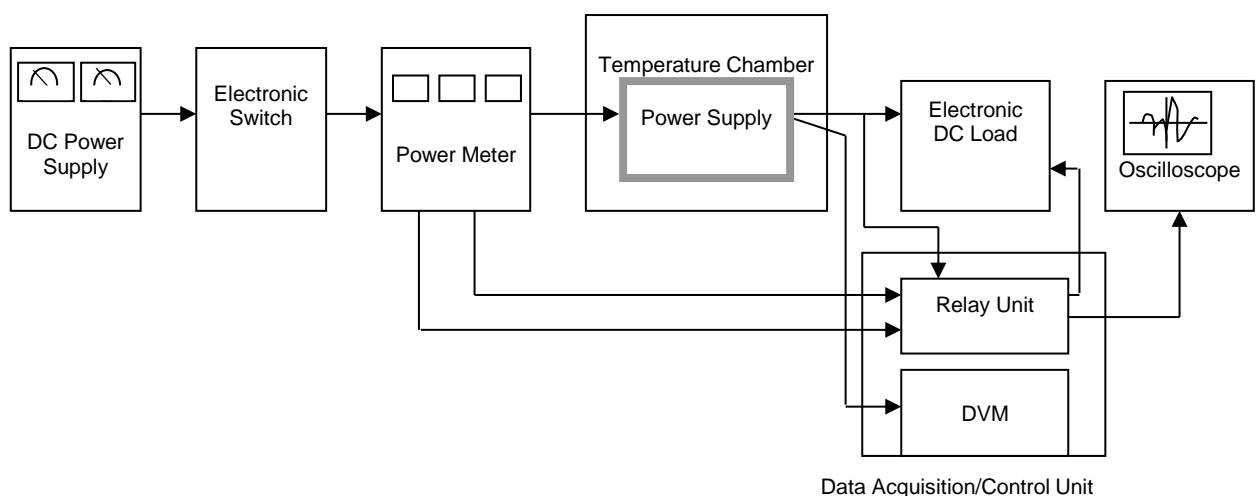


Figure A

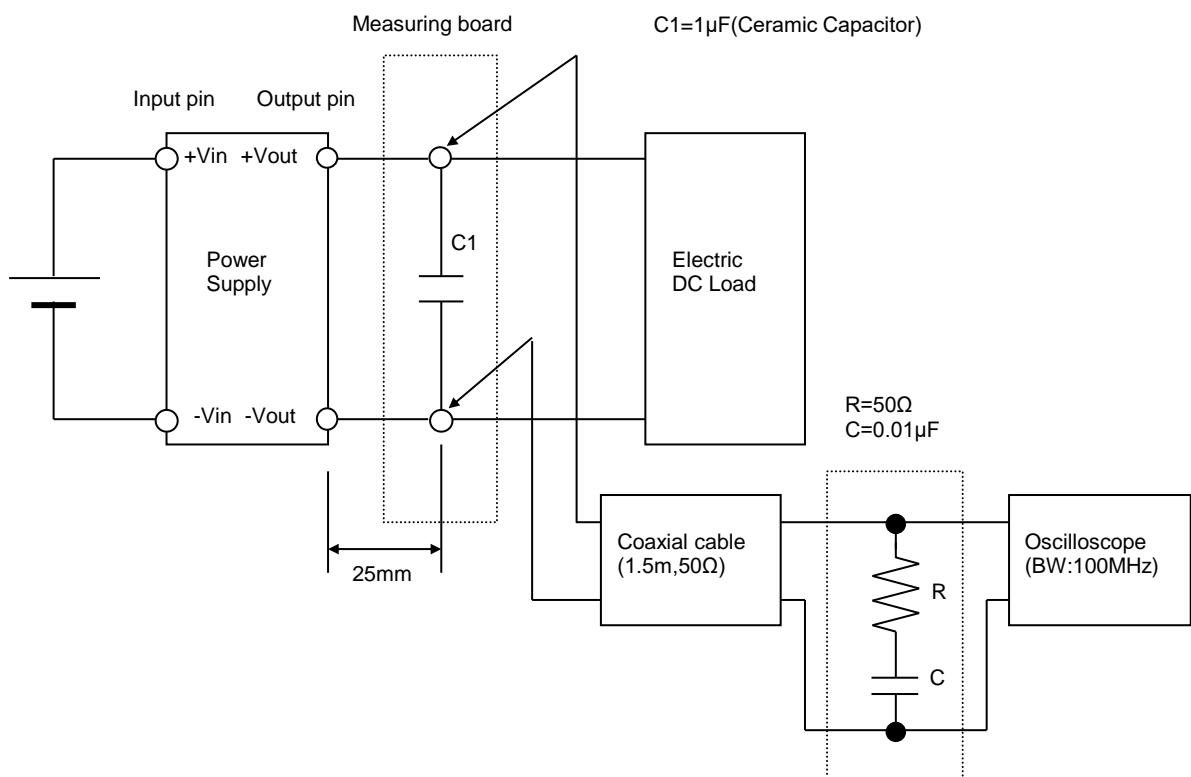


Figure B