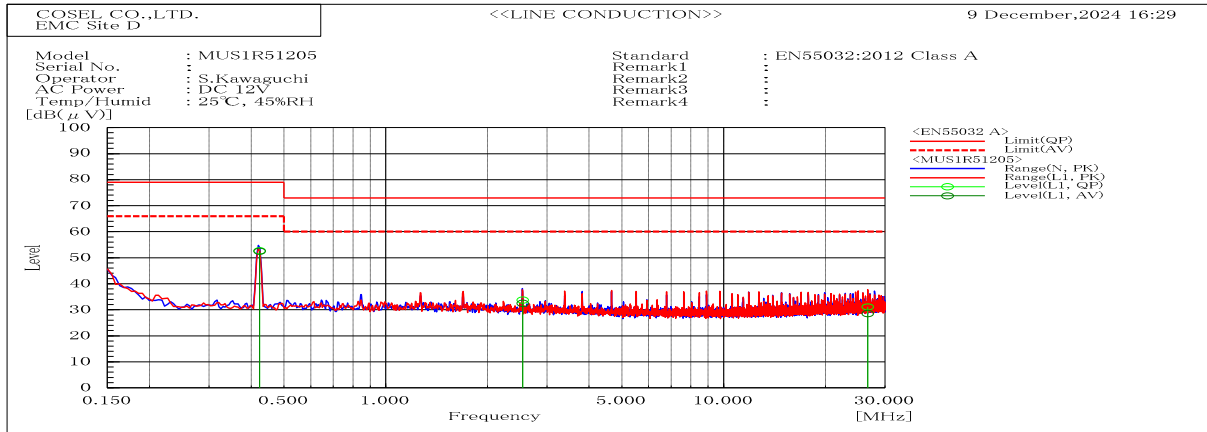
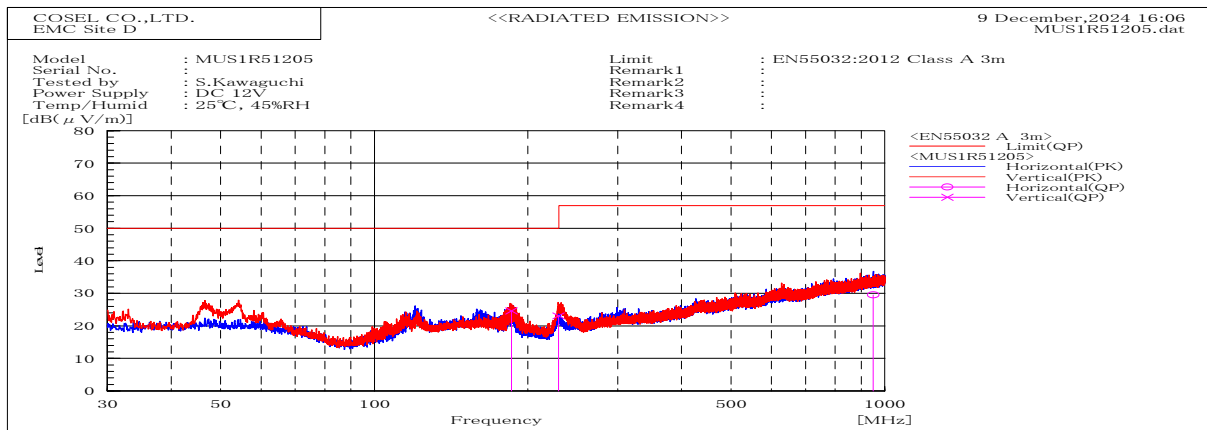


DATA SHEET		Date	05-Mar-25
Model	MUS1R51205	Temp.	25 degreeC
Test	EMI Line conduction & Radiated emission	Humid.	45 %RH
		Tested by	S.Kawaguchi



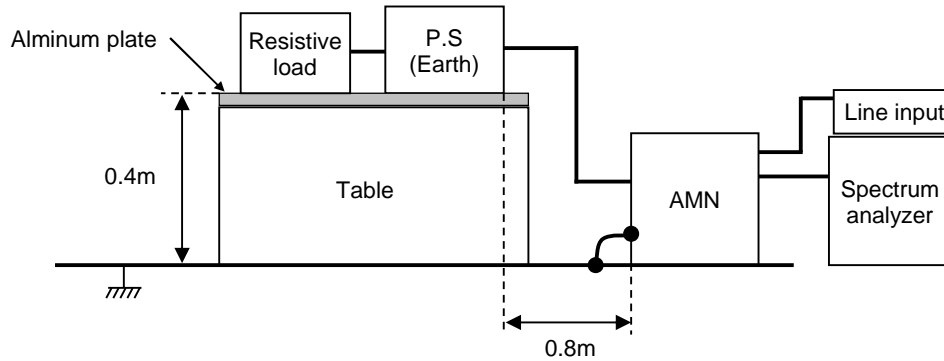
Frequency MHz	Line	Level dB(μV)		Limit dB(μV)		Margin dB		Pass/Fail	Remark
		QP	AV	QP	AV	QP	AV		
0.424	L1	52.7	52.5	79	66	26.3	13.5	Pass	
2.543	L1	33.7	32.5	73	60	39.3	27.5	Pass	
26.713	L1	31.1	28.5	73	60	41.9	31.5	Pass	



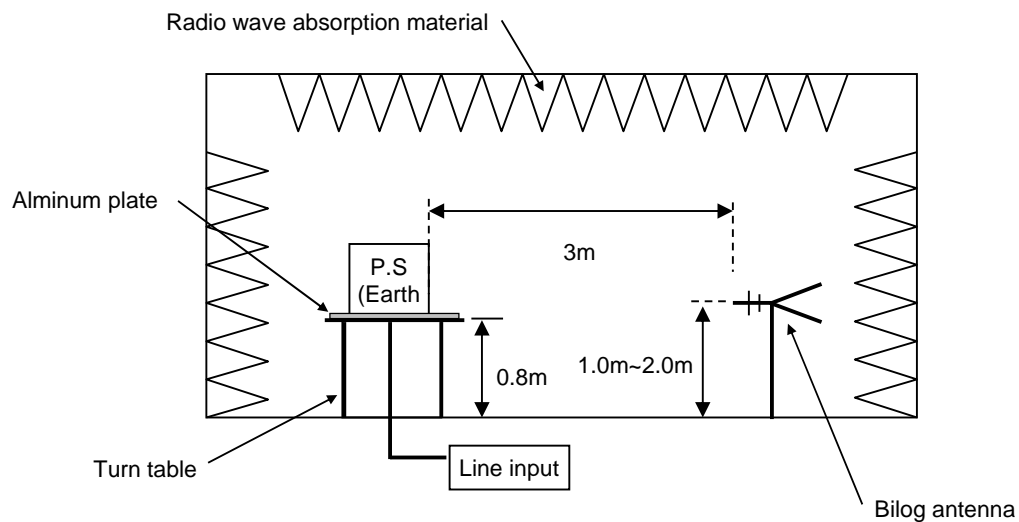
Frequency MHz	Polarization	Stability	Level dB(μV/m)		Margin dB	Pass/Fail	Height cm	Angle deg	Remark
			QP	QP					
949.855	H	Stable	29.6	57	27.4	Pass	110	275.5	
229.476	V	Stable	23.1	50	26.9	Pass	100.2	255.8	
185.66	V	Stable	24.6	50	25.4	Pass	101.8	351.7	

DATA SHEET		Date	05-Mar-25
Model	Circuit used for measurement	Temp.	25 degreeC
Test	EMI Line conduction & Radiated emission	Humid.	45 %RH
		Tested by	S.Kawaguchi

1. Line conduction



2. Radiated emission



Conditions

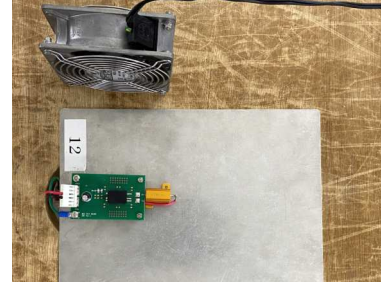
Test : EMI
Model Name: MUS1R5□□

○Photographs of Test Set-Up

LINE CONDUCTION



RADIATED EMISSION



○Testing circuitry

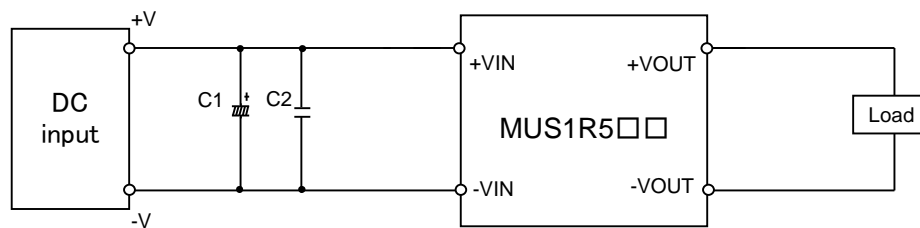


Fig.1 MUS1R505□, MUS1R512□, MUS1R524□ Testing circuitry

C1 :	MUS1R505□	16V 220 μ F	Electric capacitor (UPWseries NICHICON)
	MUS1R512□	50V 100 μ F	Electric capacitor (UPWseries NICHICON)
	MUS1R524□	-	
C2 :	MUS1R505□	16V 22 μ F	Ceramic capacitor (GRM31CC71C226M MURATA MANUFACTURING)
	MUS1R512□	25V 22 μ F	Ceramic capacitor (C3216JB1E226MT TDK)
	MUS1R524□	50V 10 μ F	Ceramic capacitor (C3216X7R1H106KT TDK)

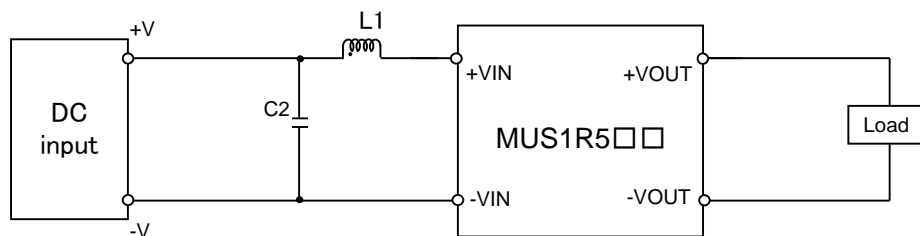


Fig.2 MUS1R548□ Testing circuitry

C2 :	MUS1R548□	100V 2.2 μ F	Ceramic capacitor (C3216X7S2A225KT TDK)
L1 :	MUS1R548□	520mA 15 μ H	Inductor(LQH32PN150MN0L MURATA MANUFACTURING)