

TEST DATA OF MHFS64812

Regulated DC Power Supply
October 26, 2021

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Design Manager

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Design Engineer

COSEL CO.,LTD.

CONTENTS

1.Input Current (by Load Current)	1
2.Efficiency (by Load Current)	2
3.Line Regulation	3
4.Load Regulation	4
5.Ripple-Noise	4
6.Dynamic Load Response	5
7.Rise and Fall Time	6
8.Overcurrent Protection	7
9.Ambient Temperature Drift	8
10.Minimum Input Voltage for Regulated Output Voltage	8
11.Switching frequency (by Load Current)	9
12.Figure of Testing Circuitry	10

(Final Page 10)



Model		MHFS64812		Temperature 25°C																																																																														
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Model		MHFS64812	Temperature 25°C Testing Circuitry Figure A
Item		Dynamic Load Response	
Object		+12V0.5A	

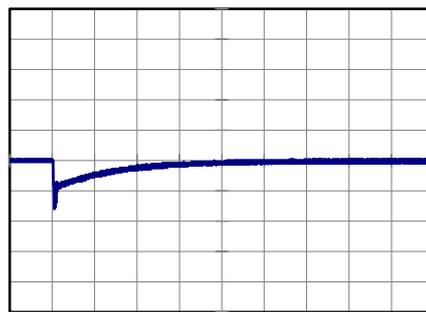
Input Volt. 48 V
Cycle 100 ms

Response. $t_1=t_2=50\mu\text{s}$. Typ

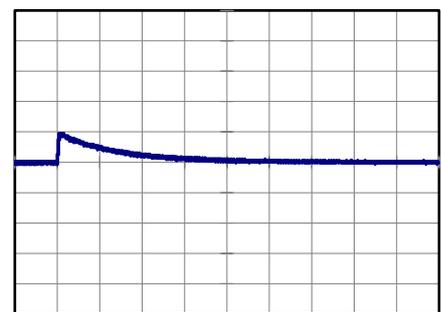


Min.Load (0A) ←→
Load 100% (0.5A)

500 mV/div



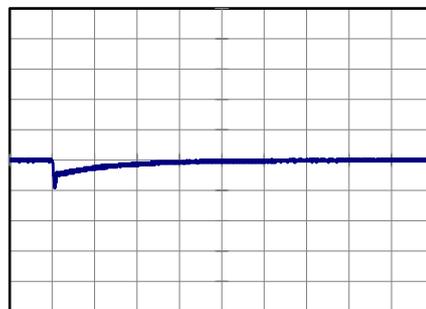
1 ms/div



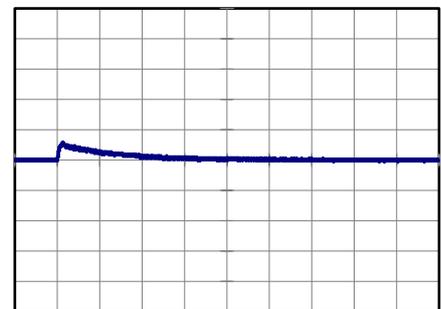
1 ms/div

Min.Load (0A) ←→
Load 50% (0.25A)

500 mV/div



1 ms/div



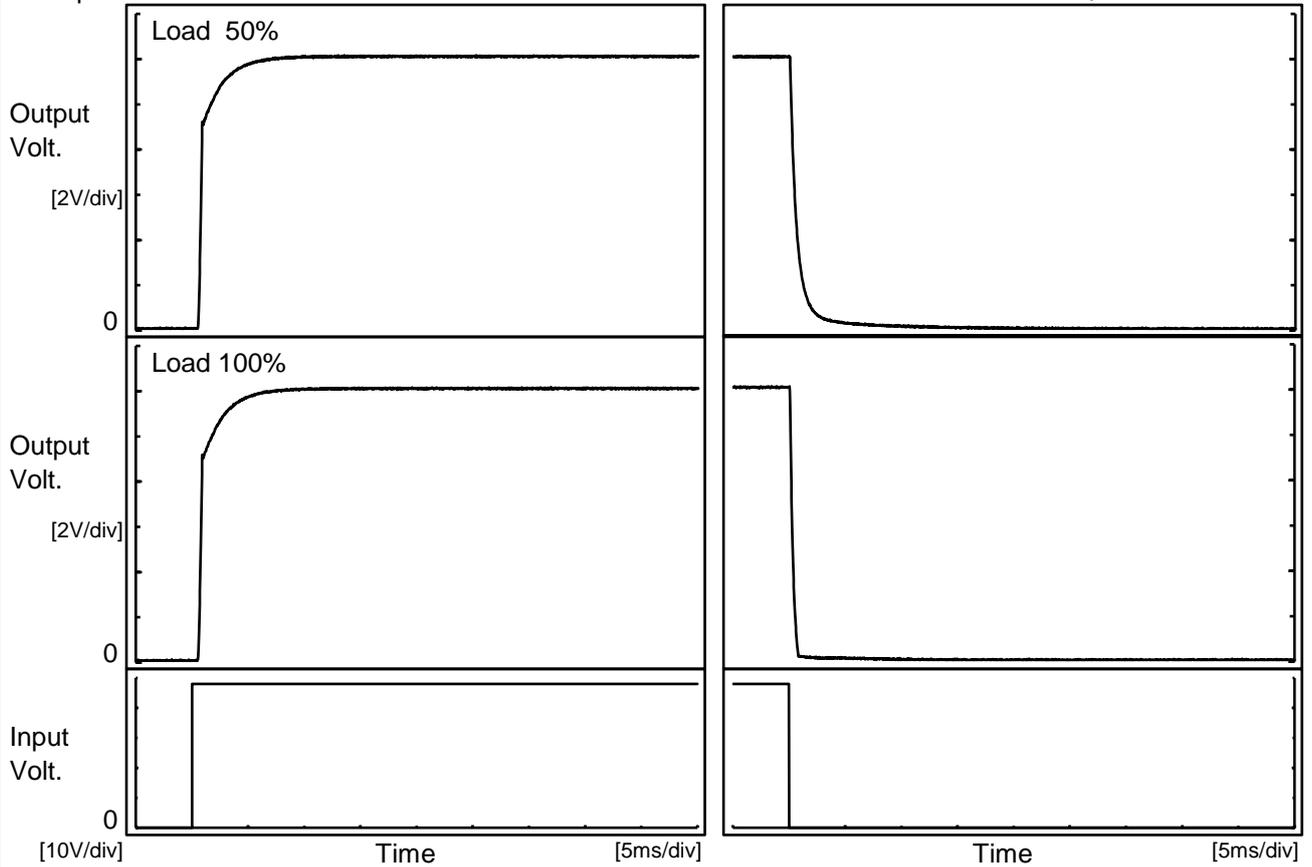
1 ms/div



Model	MHFS64812	Temperature	25°C
Item	Rise and Fall Time	Testing Circuitry	Figure A
Object	+12V0.5A		

1.Graph

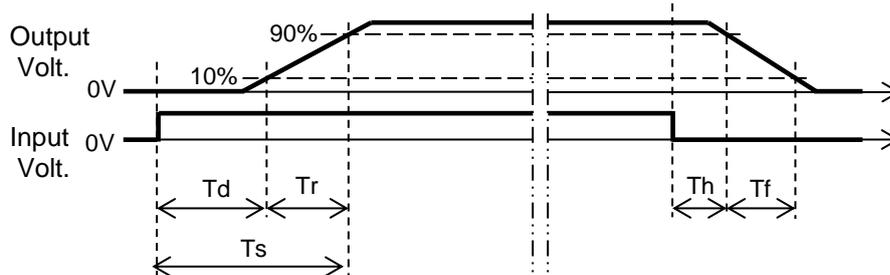
Input Volt. 48 V



2.Values

[ms]

Load \ Time	Td	Tr	Ts	Th	Tf
50 %	0.6	2.0	2.6	0.1	1.5
100 %	0.6	2.1	2.7	0.1	0.5





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Item	Overcurrent Protection	Temperature	25°C																																																																																						
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COSEL		
Model	MHFS64812	
Item	Ambient Temperature Drift	Testing Circuitry Figure A
Object	+12V0.5A	

1.Values Load 100%

Ambient Temperature[°C]	Output Voltage [V]				
	Input Volt. 18V*1	Input Volt. 24V	Input Volt. 36V	Input Volt. 48V	Input Volt. 76V
-40	11.959	11.962	11.964	11.966	11.967
25	12.020	12.020	12.020	12.020	12.019
60	12.024	12.023	12.023	12.022	12.021

*1 Load 80%

Item	Minimum Input Voltage for Regulated Output Voltage	Testing Circuitry Figure A
Object	+12V0.5A	

1.Values

Ambient Temperature[°C]	Input Voltage [V]	
	Load 50%	Load 80%
-40	14.7	14.6
25	14.4	14.5
60	14.0	14.1



<p>Model MHFS64812</p>		<p>Temperature 25°C</p>																																																																														
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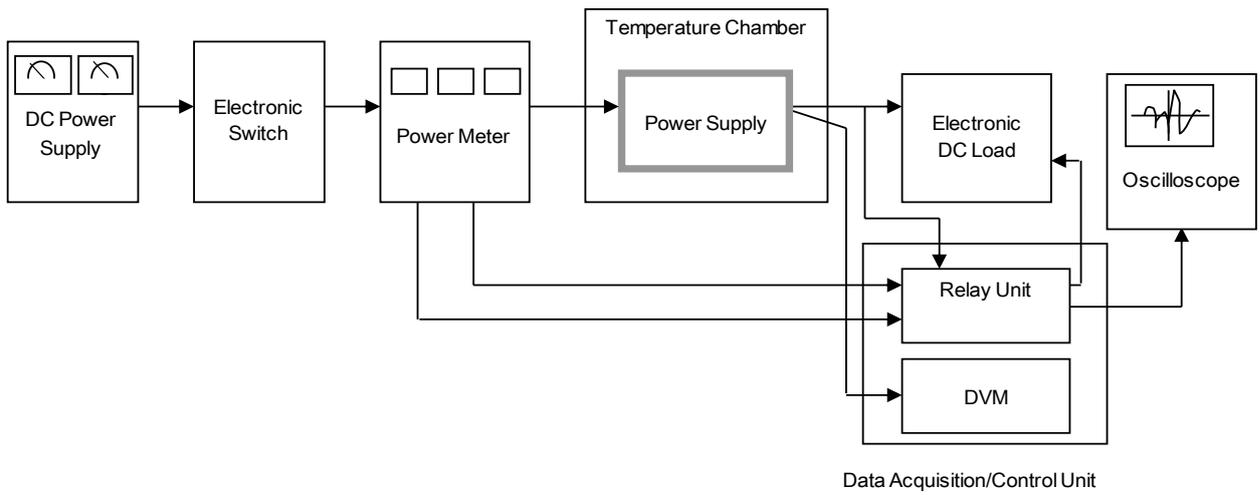


Figure A

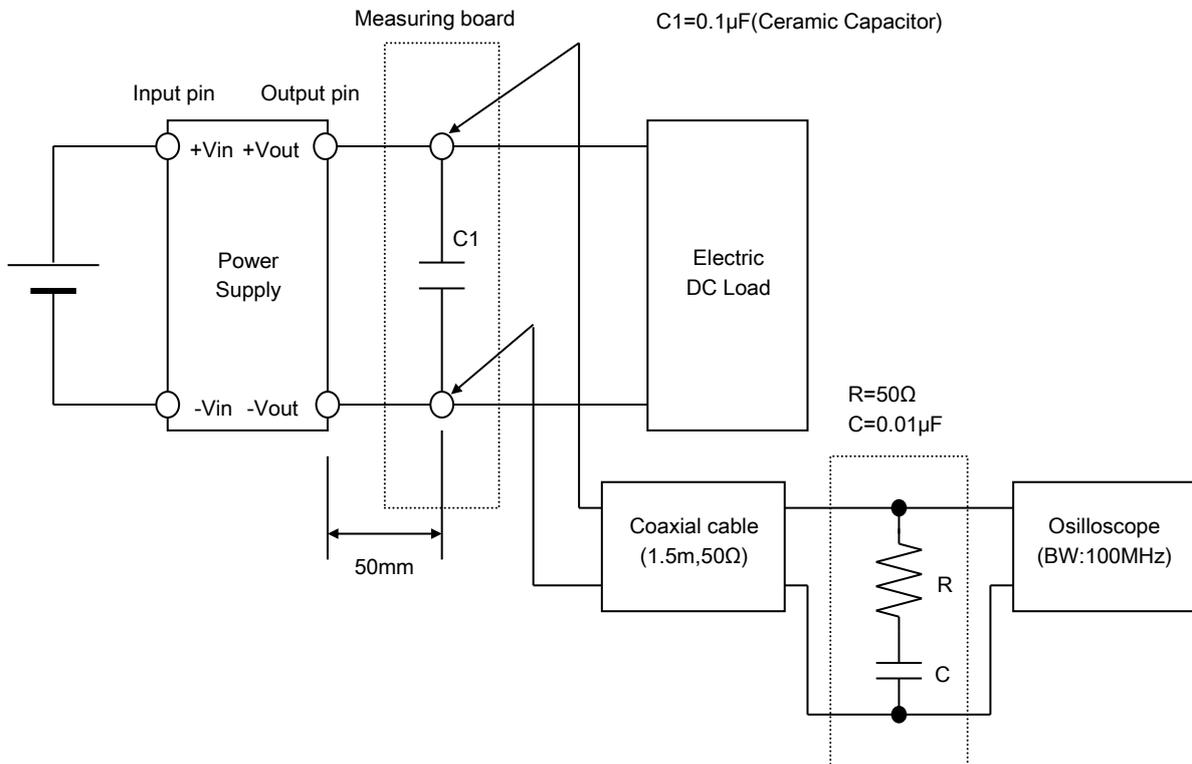


Figure B