



TEST DATA OF LHA150F-48

Regulated DC Power Supply
September 2, 2019

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Tomoyuki Sakuma Design Engineer

COSEL CO.,LTD.



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Model	LHA150F-48																																																					
Item	Input Current (by Load Current)	Temperature 25°C	Testing Circuitry Figure A																																																			
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Note: Slanted line shows the range of the rated load current.

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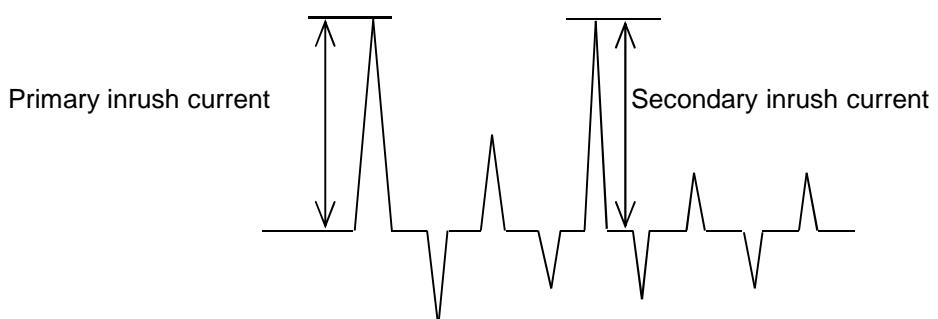
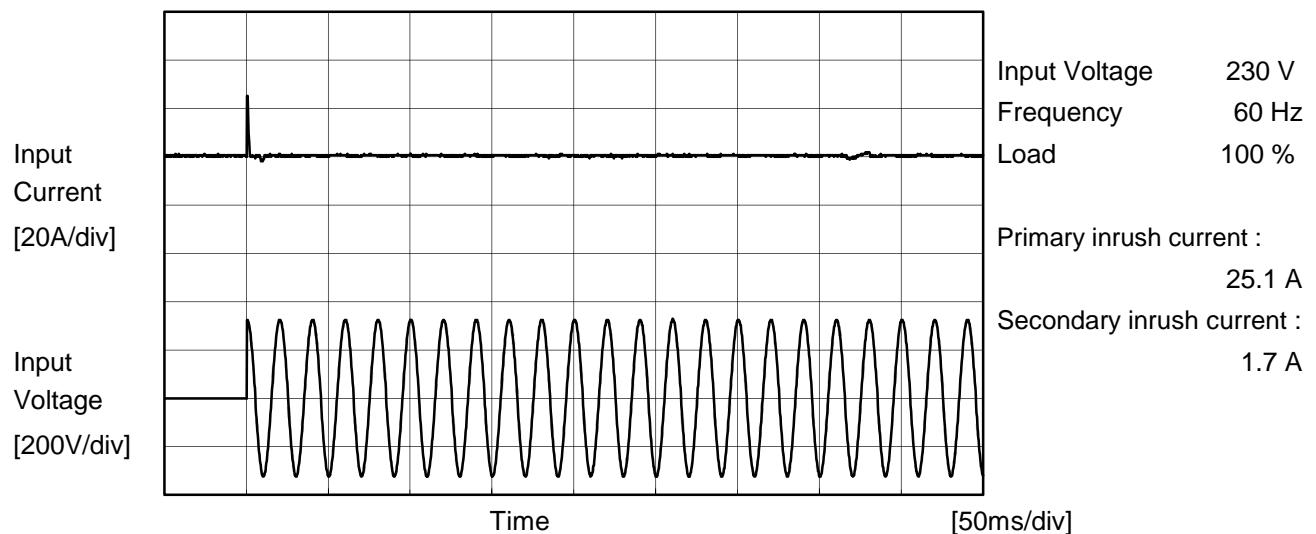
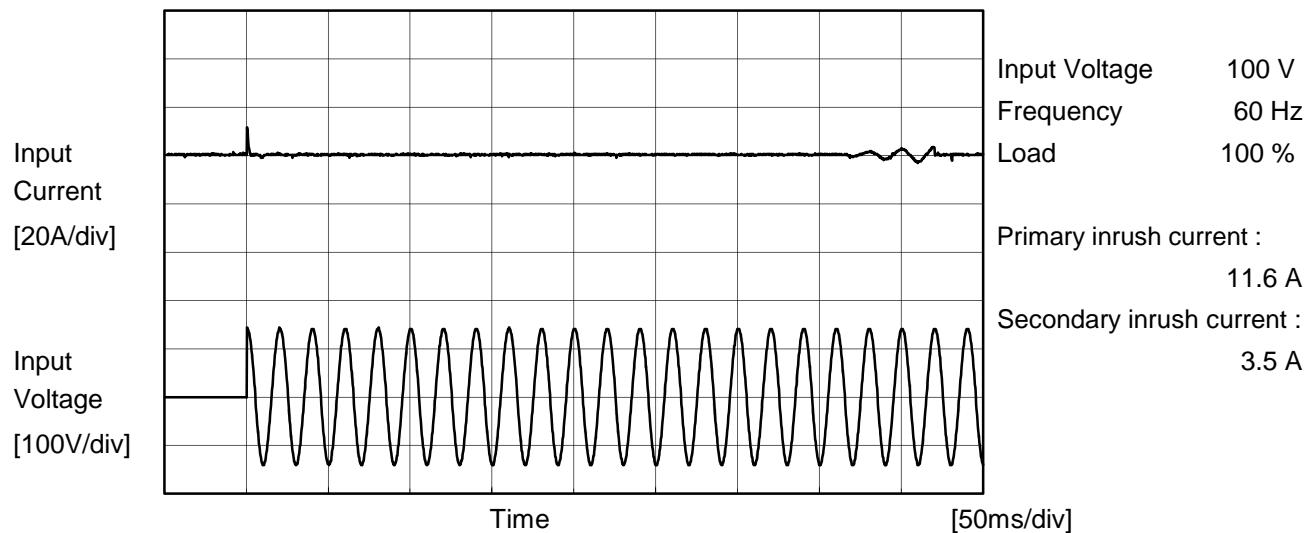
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1.Graph	<p>Graph showing Efficiency [%] vs Load Current [A]. The Y-axis ranges from 44 to 100 in increments of 8. The X-axis ranges from 0.0 to 4.0 in increments of 1.0. Three data series are plotted for Input Voltages: 100V (solid line with open triangles), 200V (dashed line with open squares), and 230V (dash-dot line with open circles). All series show efficiency increasing with load current. A vertical slanted line is drawn at approximately 3.1A, indicating the rated load current range.</p> <table border="1"> <thead> <tr> <th>Load Current [A]</th> <th>Input Volt. 100[V]</th> <th>Input Volt. 200[V]</th> <th>Input Volt. 230[V]</th> </tr> </thead> <tbody> <tr><td>0.00</td><td>-</td><td>-</td><td>-</td></tr> <tr><td>0.60</td><td>84.1</td><td>84.8</td><td>84.5</td></tr> <tr><td>1.20</td><td>88.3</td><td>90.1</td><td>90.2</td></tr> <tr><td>1.80</td><td>89.6</td><td>91.8</td><td>92.0</td></tr> <tr><td>2.40</td><td>90.1</td><td>92.4</td><td>92.7</td></tr> <tr><td>3.00</td><td>90.3</td><td>92.8</td><td>93.1</td></tr> <tr><td>3.20</td><td>90.4</td><td>92.9</td><td>93.2</td></tr> <tr><td>3.52</td><td>90.4</td><td>92.9</td><td>93.2</td></tr> <tr><td>--</td><td>-</td><td>-</td><td>-</td></tr> <tr><td>--</td><td>-</td><td>-</td><td>-</td></tr> <tr><td>--</td><td>-</td><td>-</td><td>-</td></tr> </tbody> </table>			Load Current [A]	Input Volt. 100[V]	Input Volt. 200[V]	Input Volt. 230[V]	0.00	-	-	-	0.60	84.1	84.8	84.5	1.20	88.3	90.1	90.2	1.80	89.6	91.8	92.0	2.40	90.1	92.4	92.7	3.00	90.3	92.8	93.1	3.20	90.4	92.9	93.2	3.52	90.4	92.9	93.2	--	-	-	-	--	-	-	-	--	-	-	-
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Model	LHA150F-48	Temperature	25°C
Item	Inrush Current	Testing Circuitry	Figure A
Object	—		





Model	LHA150F-48	Temperature	25°C
Item	Leakage Current	Testing Circuitry	Figure B
Object	_____		

1. Results

[mA]

Standards	Testing Circuitry	Measuring Method	Input Volt.			Note
			100 [V]	230 [V]	240 [V]	
DEN-AN	Figure B-1	Both phases	0.16	0.39	0.41	Operation
		One of phases	0.27	0.69	0.72	Stand by
IEC62368-1	Figure B-2	Both phases	0.16	0.38	0.39	Operation
		One of phases	0.27	0.67	0.70	Stand by
	Figure B-3	Both phases	0.16	0.38	0.39	Operation
		One of phases	0.27	0.66	0.70	Stand by

The value for "One of phases" is the reference value only.

2. Condition

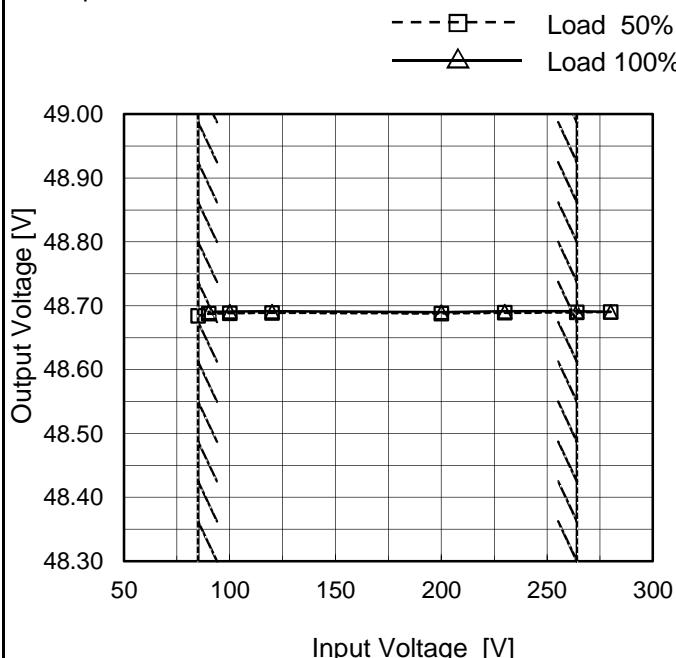
Leakage current value is concluded after measuring both phases of AC input and by choosing the larger one.

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Model	LHA150F-48
Item	Line Regulation
Object	+48V3.2A

 Temperature 25°C
 Testing Circuitry Figure A

1.Graph



2.Values

Input Voltage [V]	Output Voltage [V]	
	Load 50%	Load 100%
85	48.684	-
90	48.688	48.690
100	48.688	48.691
120	48.689	48.691
200	48.688	48.690
230	48.689	48.691
264	48.690	48.691
280	48.690	48.691
--	-	-

Note: Slanted line shows the range of the rated input voltage.

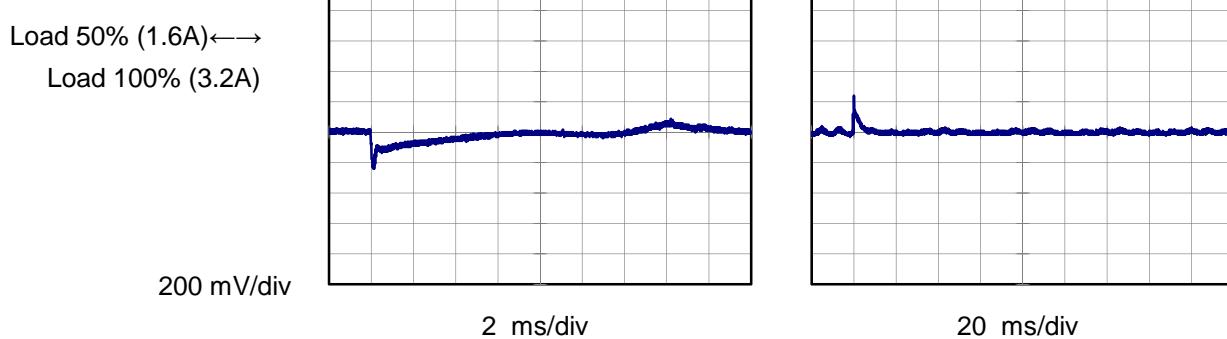
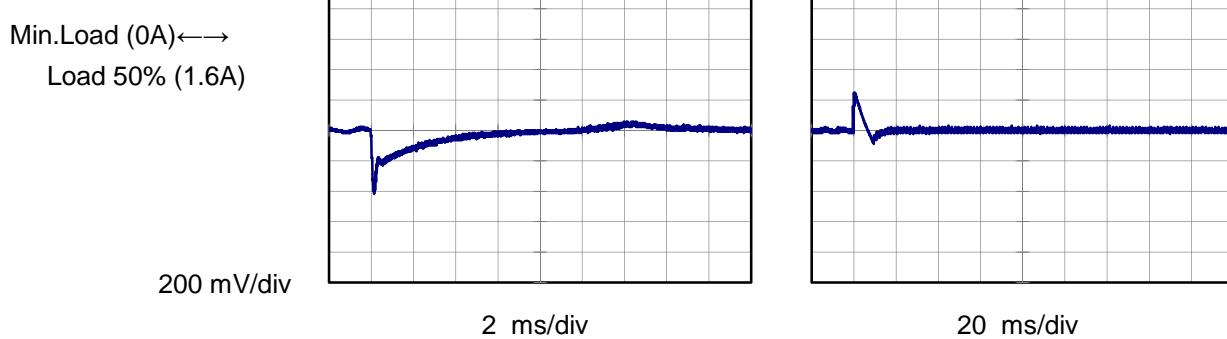
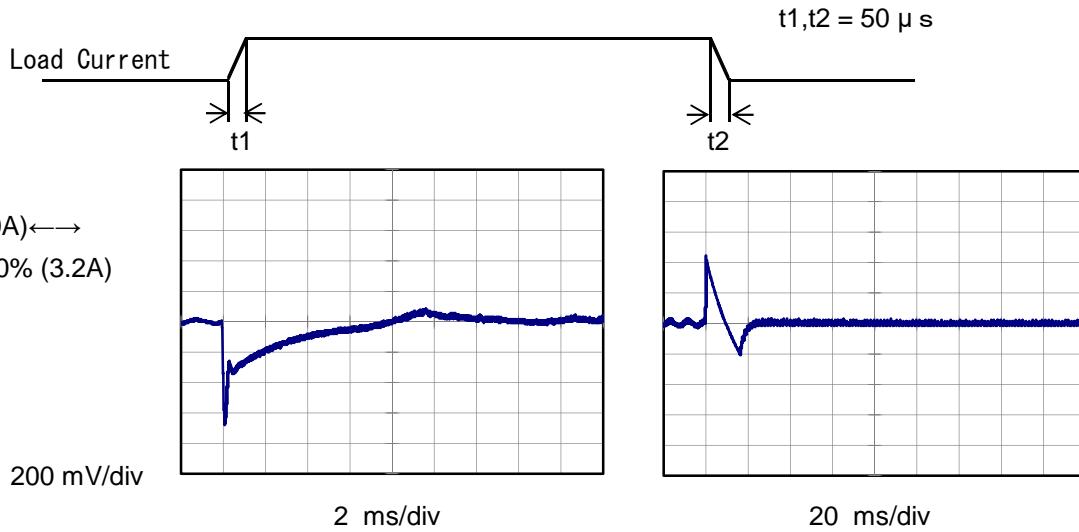
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Model	LHA150F-48	Temperature	25°C
Item	Dynamic Load Response	Testing Circuitry	Figure A
Object	+48V3.2A		

Input Volt. 230 V
 Cycle 1000 ms

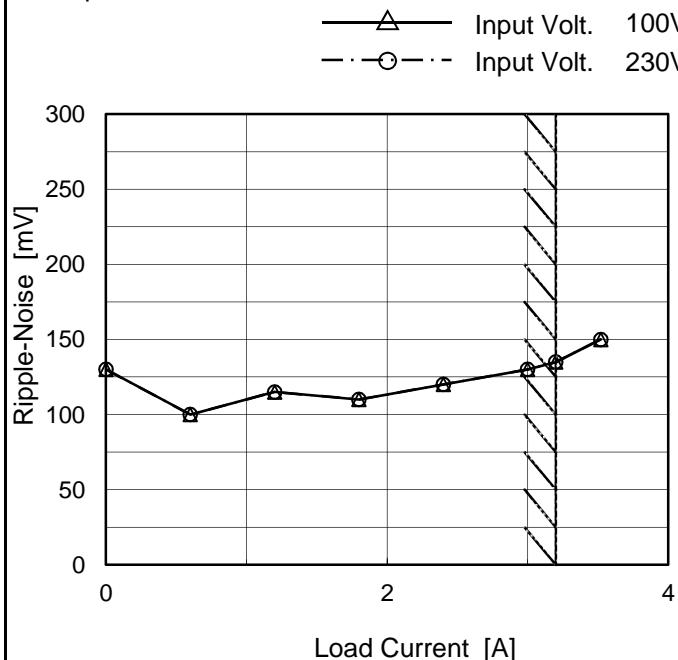


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Model	LHA150F-48
Item	Ripple-Noise (by Load Current)
Object	+48V3.2A

 Temperature 25°C
 Testing Circuitry Figure C

1. Graph



Measured by 20 MHz Oscilloscope.

Ripple-Noise is shown as p-p in the figure below.

Note: Slanted line shows the range of the rated load current.

2. Values

Load Current [A]	Ripple-Noise [mV]	
	Input Volt. 100 [V]	Input Volt. 230 [V]
0.00	130	130
0.60	100	100
1.20	115	115
1.80	110	110
2.40	120	120
3.00	130	130
3.20	135	135
3.52	150	150
--	-	-
--	-	-
--	-	-

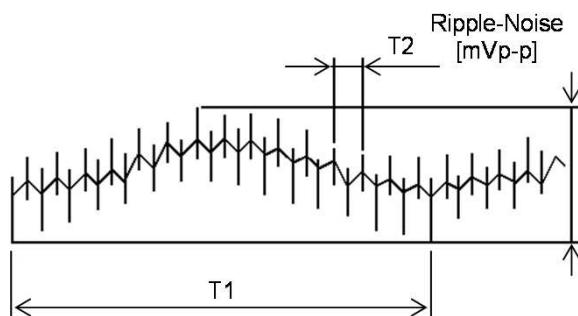
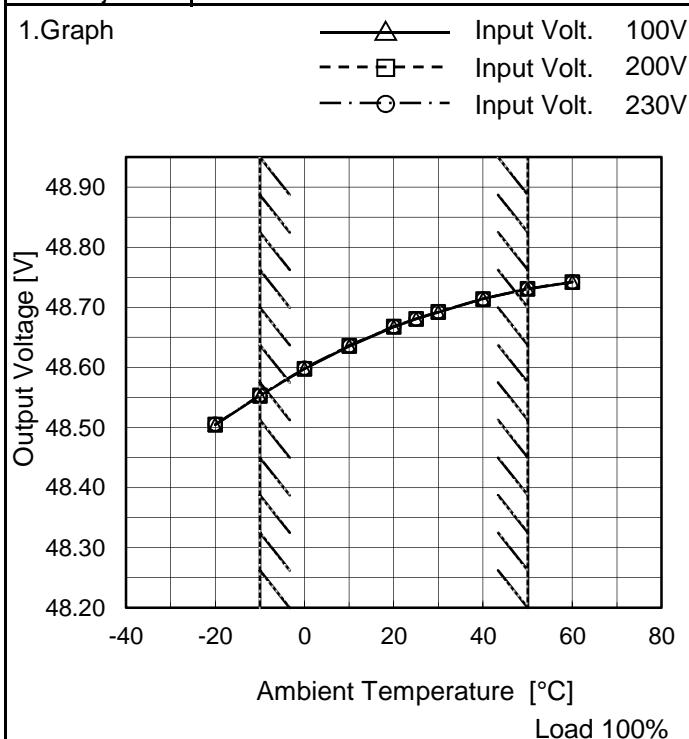
 T1: Due to AC Input Line
 T2: Due to Switching


Fig. Complex Ripple Wave Form

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Model	LHA150F-48
Item	Ambient Temperature Drift
Object	+48V3.2A



Testing Circuitry Figure A

2.Values

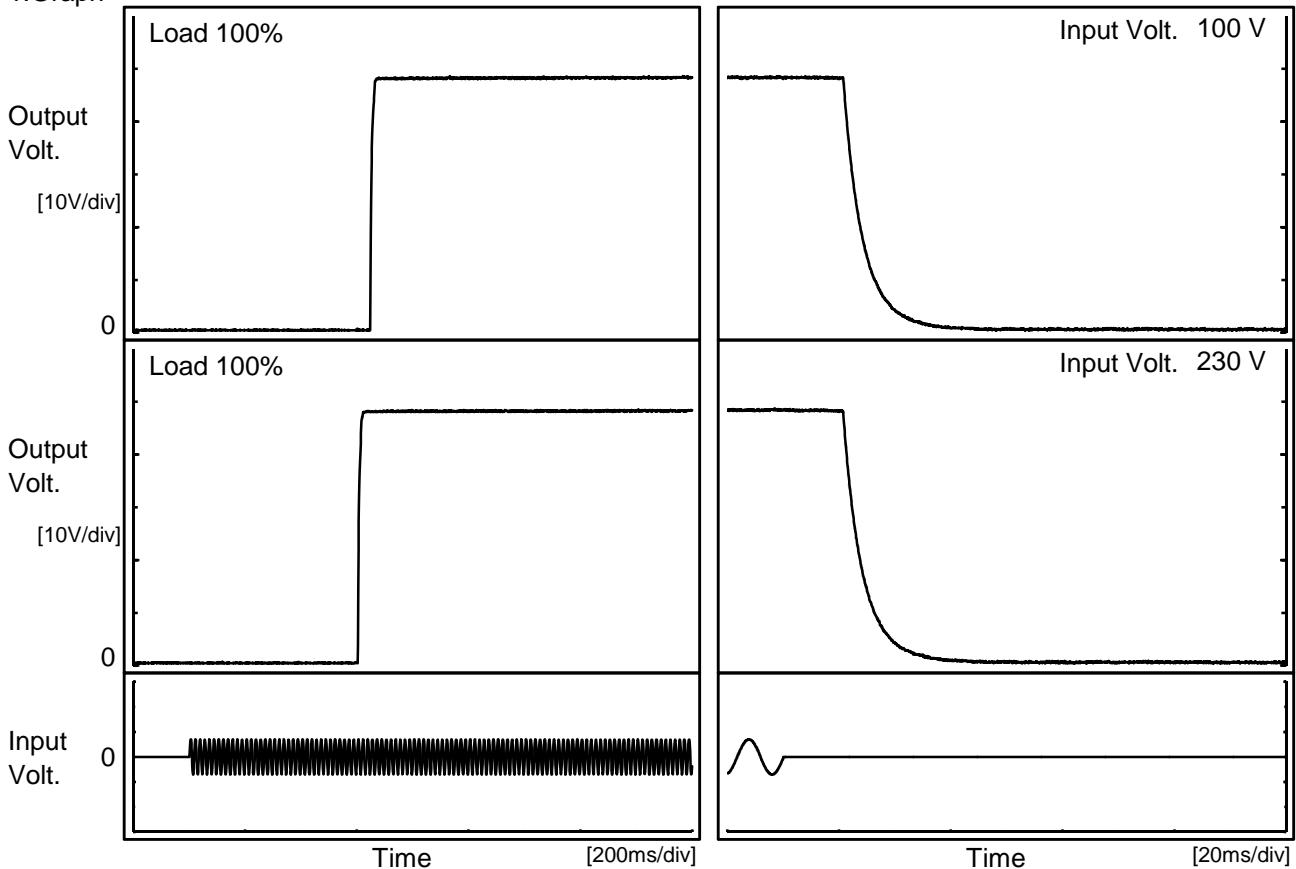
Ambient Temperature [°C]	Output Voltage [V]		
	Input Volt. 100[V]	Input Volt. 200[V]	Input Volt. 230[V]
-20	48.504	48.505	48.505
-10	48.553	48.553	48.554
0	48.598	48.598	48.599
10	48.636	48.636	48.636
20	48.668	48.668	48.668
25	48.681	48.681	48.681
30	48.692	48.693	48.693
40	48.714	48.714	48.714
50	48.731	48.731	48.731
60	48.742	48.742	48.742
--	-	-	-

Note: Slanted line shows the range of the rated ambient temperature.

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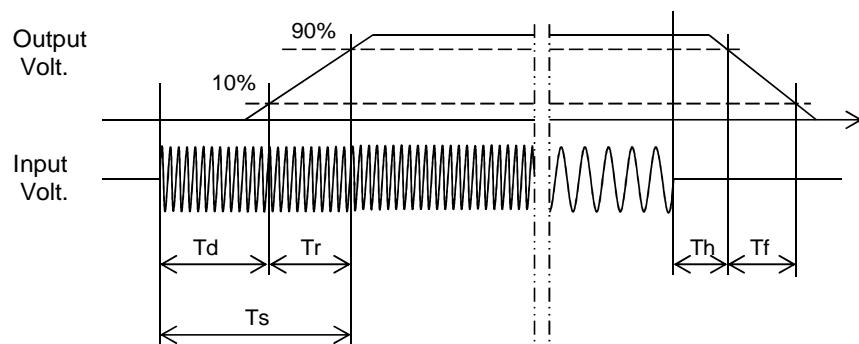
Model	LHA150F-48	Temperature	25°C
Item	Rise and Fall Time	Testing Circuitry	Figure A
Object	+48V3.2A		

1. Graph



2. Values

Input Volt.	Time	Td	Tr	Ts	Th	Tf	[ms]
100 V		649.0	12.0	661.0	22.1	16.2	
230 V		605.0	10.0	615.0	22.0	16.1	



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Item	Hold-Up Time	Temperature 25°C Testing Circuitry Figure A																																
Object	+48V3.2A																																	
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<p>Legend: ---□--- Load 50% —△— Load 100%</p> <p>Y-axis: Hold-Up Time [ms] X-axis: Input Voltage [V]</p>																																		
<p>This duration covers from Shut-off of input voltage to the moment when output voltage descends to the rated range of voltage accuracy.</p> <p>Note: Slanted line shows the range of the rated input voltage.</p>																																		
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<p>—△— Input Volt. 100V - - □ - - Input Volt. 200V - - ○ - - Input Volt. 230V</p>			<table border="1"> <thead> <tr> <th rowspan="2">Load Current [A]</th> <th colspan="3">Time [ms]</th> </tr> <tr> <th>Input Volt. 100[V]</th> <th>Input Volt. 200[V]</th> <th>Input Volt. 230[V]</th> </tr> </thead> <tbody> <tr> <td>0.00</td><td>-</td><td>-</td><td>-</td></tr> <tr> <td>0.60</td><td>90</td><td>105</td><td>105</td></tr> <tr> <td>1.20</td><td>55</td><td>55</td><td>55</td></tr> <tr> <td>1.80</td><td>36</td><td>37</td><td>37</td></tr> <tr> <td>2.40</td><td>27</td><td>28</td><td>28</td></tr> <tr> <td>3.00</td><td>20</td><td>22</td><td>22</td></tr> <tr> <td>3.20</td><td>19</td><td>21</td><td>21</td></tr> <tr> <td>3.52</td><td>17</td><td>17</td><td>17</td></tr> <tr> <td>--</td><td>-</td><td>-</td><td>-</td></tr> <tr> <td>--</td><td>-</td><td>-</td><td>-</td></tr> <tr> <td>--</td><td>-</td><td>-</td><td>-</td></tr> </tbody> </table>	Load Current [A]	Time [ms]			Input Volt. 100[V]	Input Volt. 200[V]	Input Volt. 230[V]	0.00	-	-	-	0.60	90	105	105	1.20	55	55	55	1.80	36	37	37	2.40	27	28	28	3.00	20	22	22	3.20	19	21	21	3.52	17	17	17	--	-	-	-	--	-	-	-	--	-	-	-
Load Current [A]	Time [ms]																																																					
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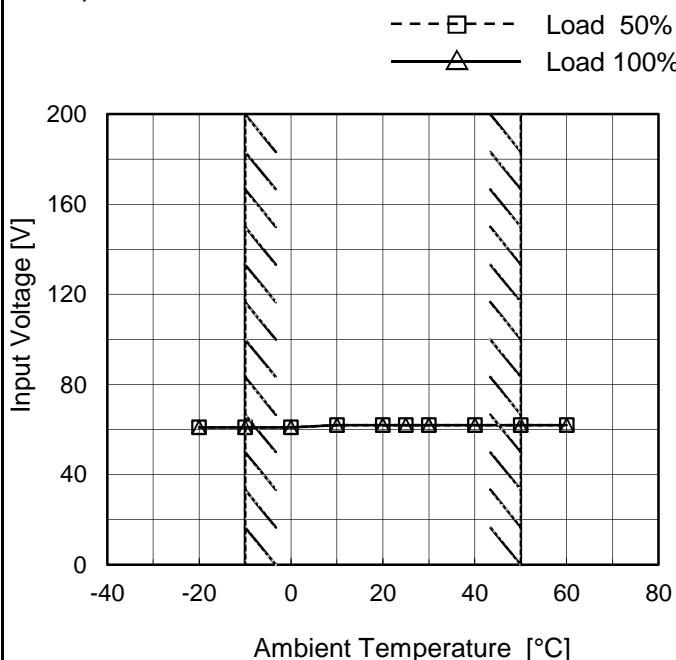
COSEL

Model LHA150F-48

Item Minimum Input Voltage
for Regulated Output Voltage

Object +48V3.2A

1. Graph



Note: Slanted line shows the range of the rated ambient temperature.

Testing Circuitry Figure A

2. Values

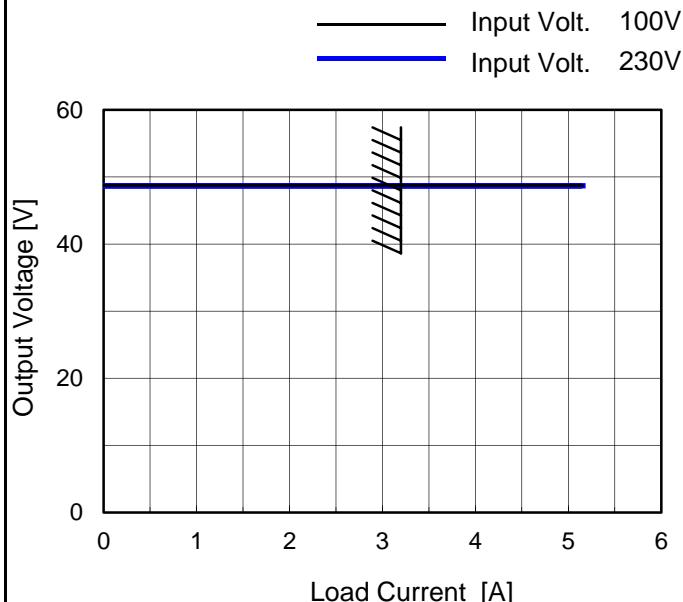
Ambient Temperature [°C]	Input Voltage [V]	
	Load 50%	Load 100%
-20	61	61
-10	61	61
0	61	61
10	62	62
20	62	62
25	62	62
30	62	62
40	62	62
50	62	62
60	62	62
--	-	-

COSEL

Model	LHA150F-48
-------	------------

Item	Overcurrent Protection
Object	+48V3.2A
Temperature	25°C
Testing Circuitry	Figure A

1. Graph



Note: Slanted line shows the range of the rated load current.

Overcurrent protection is Hiccup mode.

2. Values

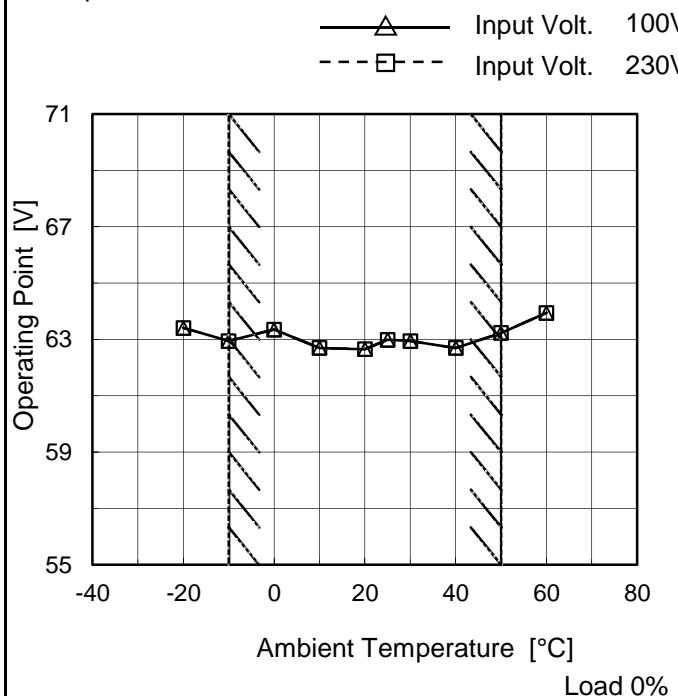
Output Voltage [V]	Load Current [A]	
	Input Volt. 100[V]	Input Volt. 230[V]
48	5.15	5.15
--	-	-
--	-	-
--	-	-
--	-	-
--	-	-
--	-	-
--	-	-
--	-	-
--	-	-
--	-	-
--	-	-
--	-	-
--	-	-
--	-	-
--	-	-

COSEL

Model	LHA150F-48
Item	Overshoot Protection
Object	+48V3.2A

Testing Circuitry Figure A

1. Graph



2. Values

Ambient Temperature [°C]	Operating Point [V]	
	Input Volt. 100[V]	Input Volt. 230[V]
-20	63.41	63.41
-10	62.94	62.94
0	63.35	63.35
10	62.70	62.70
20	62.65	62.65
25	62.99	62.99
30	62.94	62.94
40	62.70	62.70
50	63.23	63.23
60	63.94	63.94
--	-	-

Note: Slanted line shows the range of the rated ambient temperature.

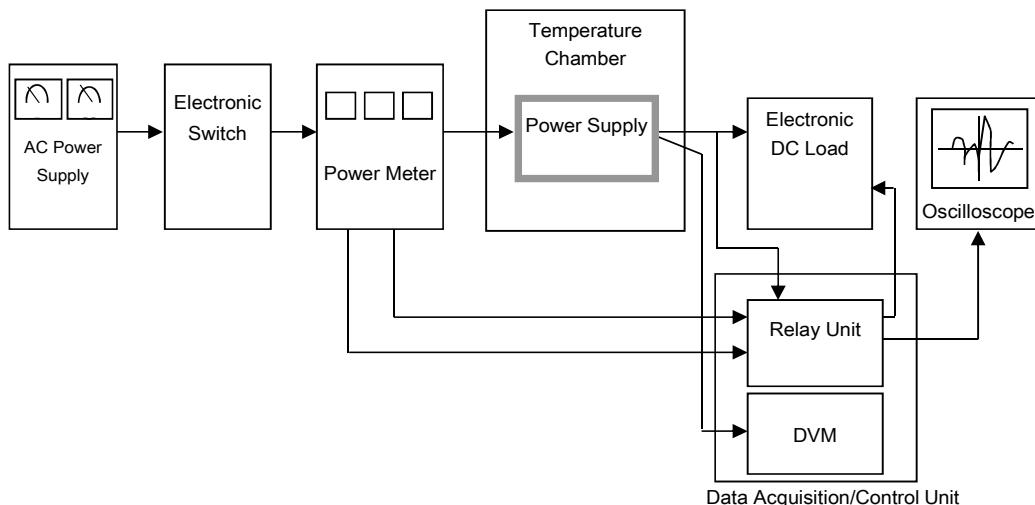


Figure A

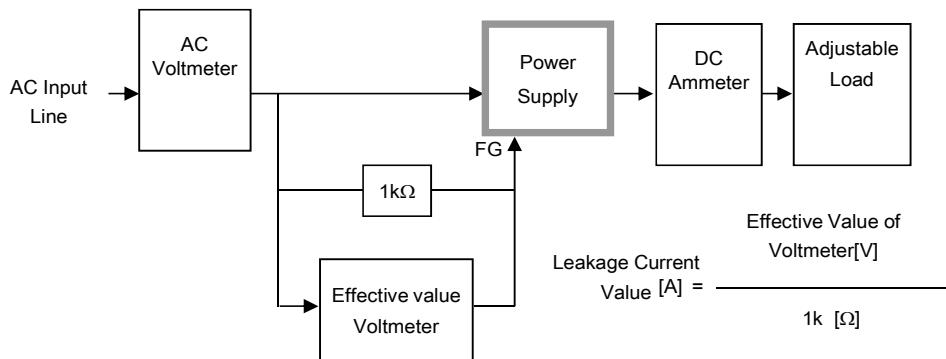


Figure B-1 (DEN-AN)

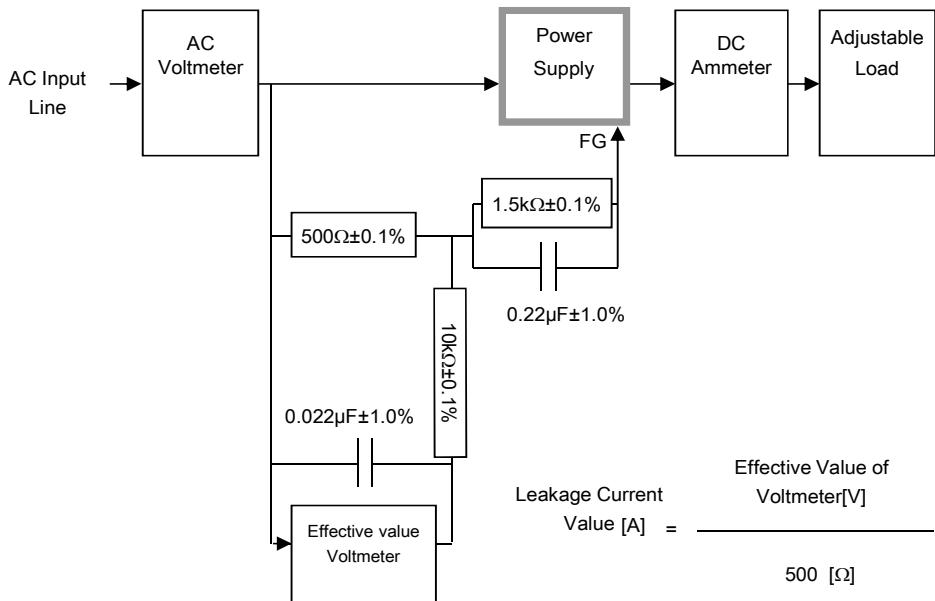


Figure B-2 (IEC62368-1 refer to IEC60990 Fig.4)

COSEL

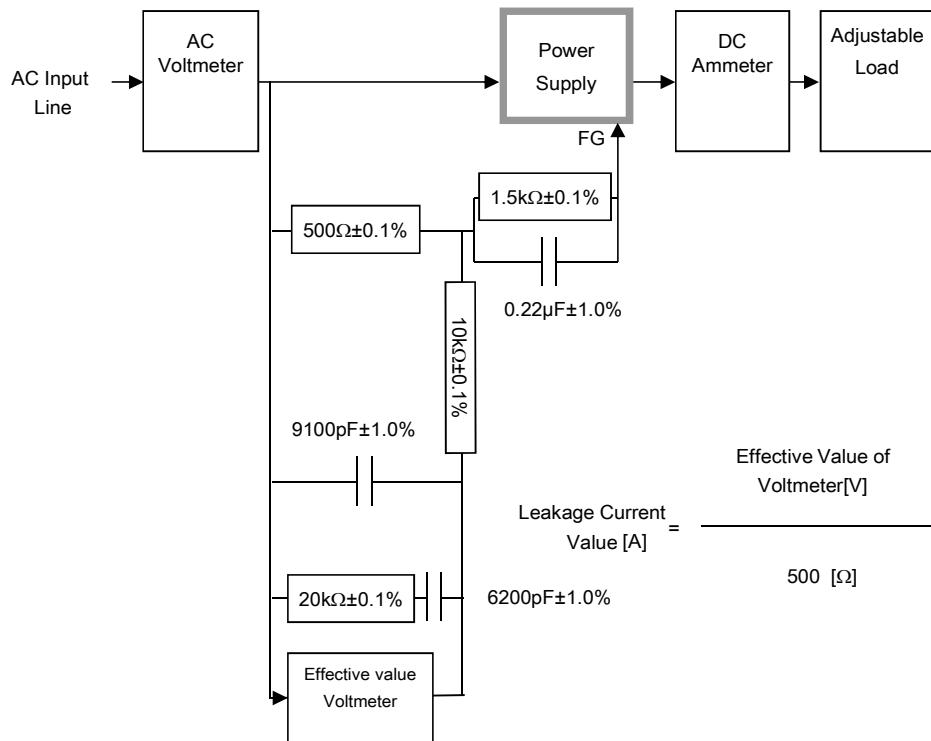
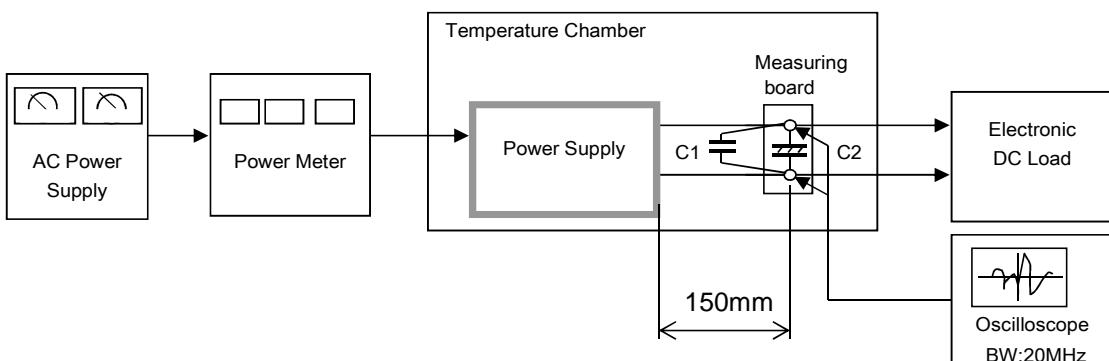


Figure B-3 (IEC62368-1 refer to IEC60990 Fig.5)



$$C1 = 0.1 \mu F$$

(Ceramic capacitor)

$$C2 = 22 \mu F$$

(Electrolytic capacitor)

Figure C