



# TEST DATA OF LGA50A-15

Regulated DC Power Supply  
May 20, 2011

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Kenji Shiho Design Manager

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Yosuke Saitou Design Engineer

**COSEL CO.,LTD.**



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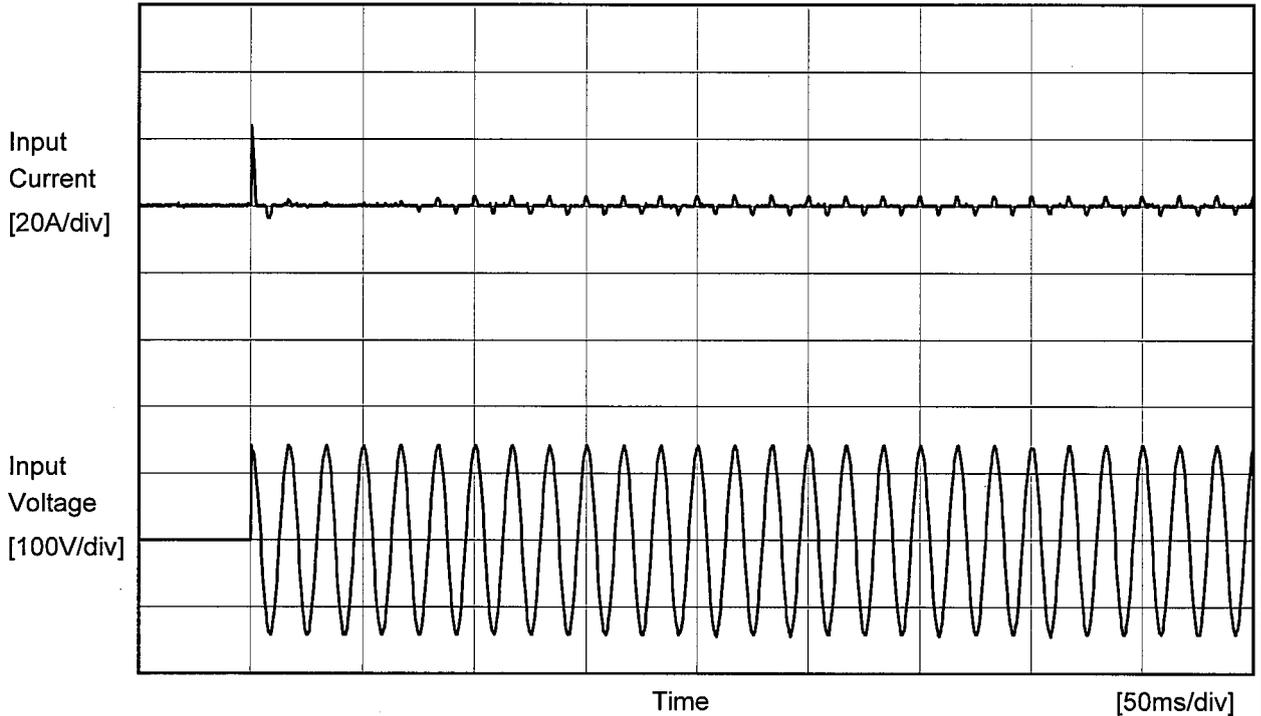
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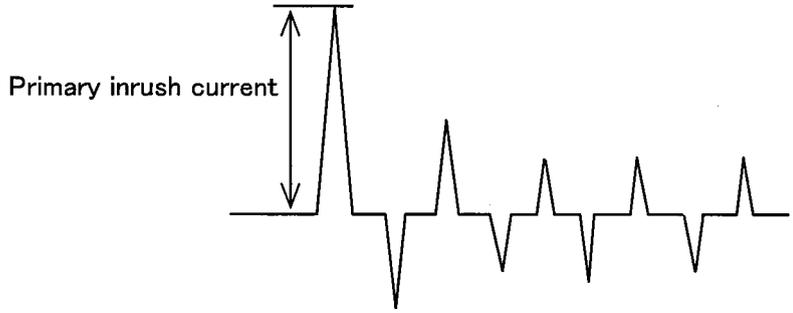
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Model		LGA50A-15	Temperature 25°C Testing Circuitry Figure A
Item		Inrush Current	
Object		_____	



Input Voltage            100 V  
 Frequency                60 Hz  
 Load                      100 %  
  
 Primary inrush current    23.8 A





<b>COSEL</b>		
Model	LGA50A-15	Temperature 25°C Testing Circuitry Figure B
Item	Leakage Current	
Object	_____	

1.Results

Standards	Leakage Current [mA]		
	Input Volt. 100 [V]	Input Volt. 120 [V]	Input Volt. 132 [V]
(A)DEN-AN	0.18	0.20	0.24
(B)IEC60950-1	0.18	0.25	0.27

frequency 60Hz

2.Condition

Leakage current value is concluded after measuring both phases of AC input and by choosing the larger one.



<p>Model LGA50A-15</p> <p>Item Line Regulation</p> <p>Object +15V3.5A</p>		<p>Temperature 25°C</p> <p>Testing Circuitry Figure A</p>																																
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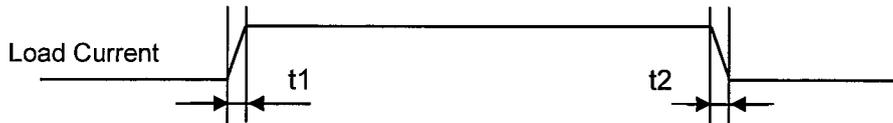
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Item	Load Regulation	Testing Circuitry	Figure A																																																			
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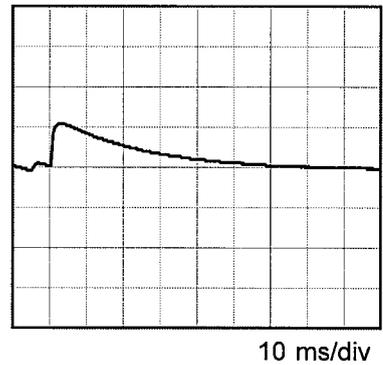
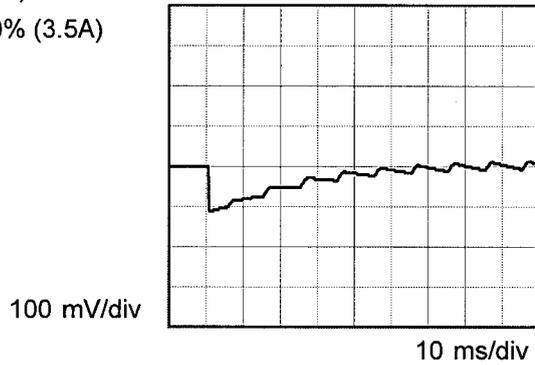
Model		LGA50A-15	
Item		Dynamic Load Response	
Object		+15V3.5A	
		Temperature	25°C
		Testing Circuitry	Figure C

Input Volt. 100 V  
Cycle 1000 ms

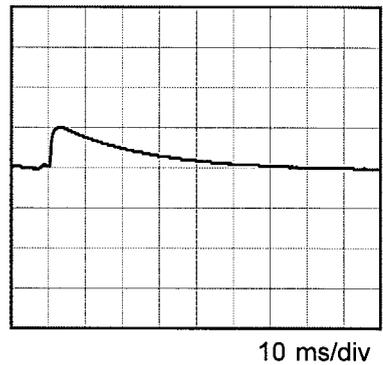
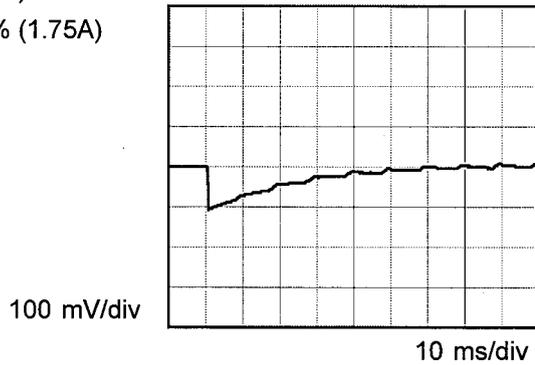
Response.  $t_1=t_2=50 \mu s$ . Typ



Min. Load (0A) ←→  
Load 100% (3.5A)



Min. Load (0A) ←→  
Load 50% (1.75A)





<p>Model LGA50A-15</p> <p>Item Ripple Voltage (by Load Current)</p> <p>Object +15V3.5A</p>		<p>Temperature 25°C</p> <p>Testing Circuitry Figure C</p>																																						
<p>1. Graph</p> <div style="text-align: right;"> <p>—△— Input Volt. 85V</p> <p>-·-○-·- Input Volt. 132V</p> </div> <p>Ripple Voltage [mV]</p> <p>Load Current [A]</p>		<p>2. Values</p> <table border="1"> <thead> <tr> <th rowspan="2">Load Current [A]</th> <th colspan="2">Ripple Voltage [mV]</th> </tr> <tr> <th>Input Volt. 85 [V]</th> <th>Input Volt. 132 [V]</th> </tr> </thead> <tbody> <tr><td>0.00</td><td>20</td><td>20</td></tr> <tr><td>0.60</td><td>30</td><td>30</td></tr> <tr><td>1.20</td><td>40</td><td>40</td></tr> <tr><td>1.80</td><td>50</td><td>45</td></tr> <tr><td>2.40</td><td>50</td><td>45</td></tr> <tr><td>3.00</td><td>50</td><td>50</td></tr> <tr><td>3.50</td><td>50</td><td>50</td></tr> <tr><td>3.85</td><td>50</td><td>50</td></tr> <tr><td>--</td><td>-</td><td>-</td></tr> <tr><td>--</td><td>-</td><td>-</td></tr> <tr><td>--</td><td>-</td><td>-</td></tr> </tbody> </table>	Load Current [A]	Ripple Voltage [mV]		Input Volt. 85 [V]	Input Volt. 132 [V]	0.00	20	20	0.60	30	30	1.20	40	40	1.80	50	45	2.40	50	45	3.00	50	50	3.50	50	50	3.85	50	50	--	-	-	--	-	-	--	-	-
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<div style="text-align: center;"> <p>T1: Due to AC Input Line</p> <p>T2: Due to Switching</p> </div> <p>Fig. Complex Ripple Wave Form</p>																																								



<p>Model LGA50A-15</p> <p>Item Ripple-Noise</p> <p>Object +15V3.5A</p>		<p>Temperature 25°C</p> <p>Testing Circuitry Figure C</p>																																						
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<b>COSEL</b>																											
Model	LGA50A-15																										
Item	Ripple Voltage (by Ambient Temp.)	Testing Circuitry Figure C																									
Object	+15V3.5A																										
<p>1.Graph</p> <p style="text-align: center;">Ripple Voltage [mV]</p> <p style="text-align: center;">Ambient Temperature [°C]</p> <p style="text-align: center;">Input Volt. 100V Input Load. 100%</p> <p>Measured by 20 MHz Oscilloscope. Note: Slanted line shows the range of the rated ambient temperature.</p>		<p>2.Values</p> <table border="1"> <thead> <tr> <th rowspan="2">Ambient Temperature [°C]</th> <th>Ripple Voltage</th> </tr> <tr> <th>[mV]</th> </tr> </thead> <tbody> <tr><td>-30</td><td>160</td></tr> <tr><td>-10</td><td>50</td></tr> <tr><td>0</td><td>50</td></tr> <tr><td>25</td><td>50</td></tr> <tr><td>45</td><td>50</td></tr> <tr><td>50</td><td>50</td></tr> <tr><td>--</td><td>-</td></tr> <tr><td>--</td><td>-</td></tr> <tr><td>--</td><td>-</td></tr> <tr><td>--</td><td>-</td></tr> <tr><td>--</td><td>-</td></tr> </tbody> </table>	Ambient Temperature [°C]	Ripple Voltage	[mV]	-30	160	-10	50	0	50	25	50	45	50	50	50	--	-	--	-	--	-	--	-	--	-
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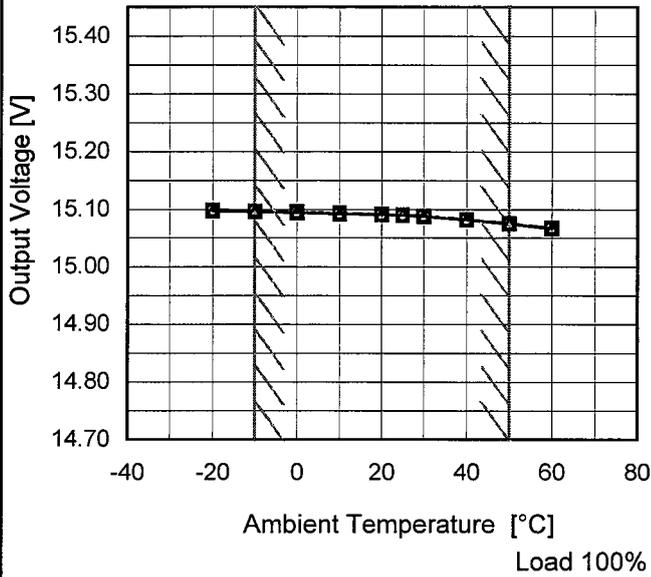
Model	LGA50A-15
Item	Ambient Temperature Drift
Object	+15V3.5A

Testing Circuitry Figure A

1. Graph

—△— Input Volt. 85V  
 - - - □ - - - Input Volt. 100V  
 - - - ○ - - - Input Volt. 132V

2. Values



Ambient Temperature [°C]	Output Voltage [V]		
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
-20	15.097	15.098	15.099
-10	15.096	15.097	15.097
0	15.094	15.095	15.095
10	15.093	15.093	15.094
20	15.091	15.091	15.092
25	15.089	15.090	15.090
30	15.087	15.088	15.088
40	15.082	15.083	15.083
50	15.075	15.075	15.076
60	15.067	15.067	15.068
--	-	-	-

Note: Slanted line shows the range of the rated ambient temperature.



<b>COSEL</b>		
Model	LGA50A-15	
Item	Output Voltage Accuracy	Testing Circuitry Figure A
Object	+15V3.5A	

1. Output Voltage Accuracy

This is defined as the value of the output voltage, regulation load, ambient temperature and input voltage varied at random in the range as specified below.

Temperature : -10 - 50°C

Input Voltage : 85 - 132V

Load Current : 0 - 3.5A

\* Output Voltage Accuracy =  $\pm(\text{Maximum of Output Voltage} - \text{Minimum of Output Voltage}) / 2$

\* Output Voltage Accuracy (Ration) =  $\frac{\text{Output Voltage Accuracy}}{\text{Rated Output Voltage}} \times 100$

2. Values

Item	Temperature [°C]	Input Voltage[V]	Output		Output Voltage Accuracy	
			Current[A]	Voltage[V]	Value [mV]	Ration [%]
Maximum Voltage	-10	100	0	15.099	±12	±0.1
Minimum Voltage	50	85	3.5	15.075		

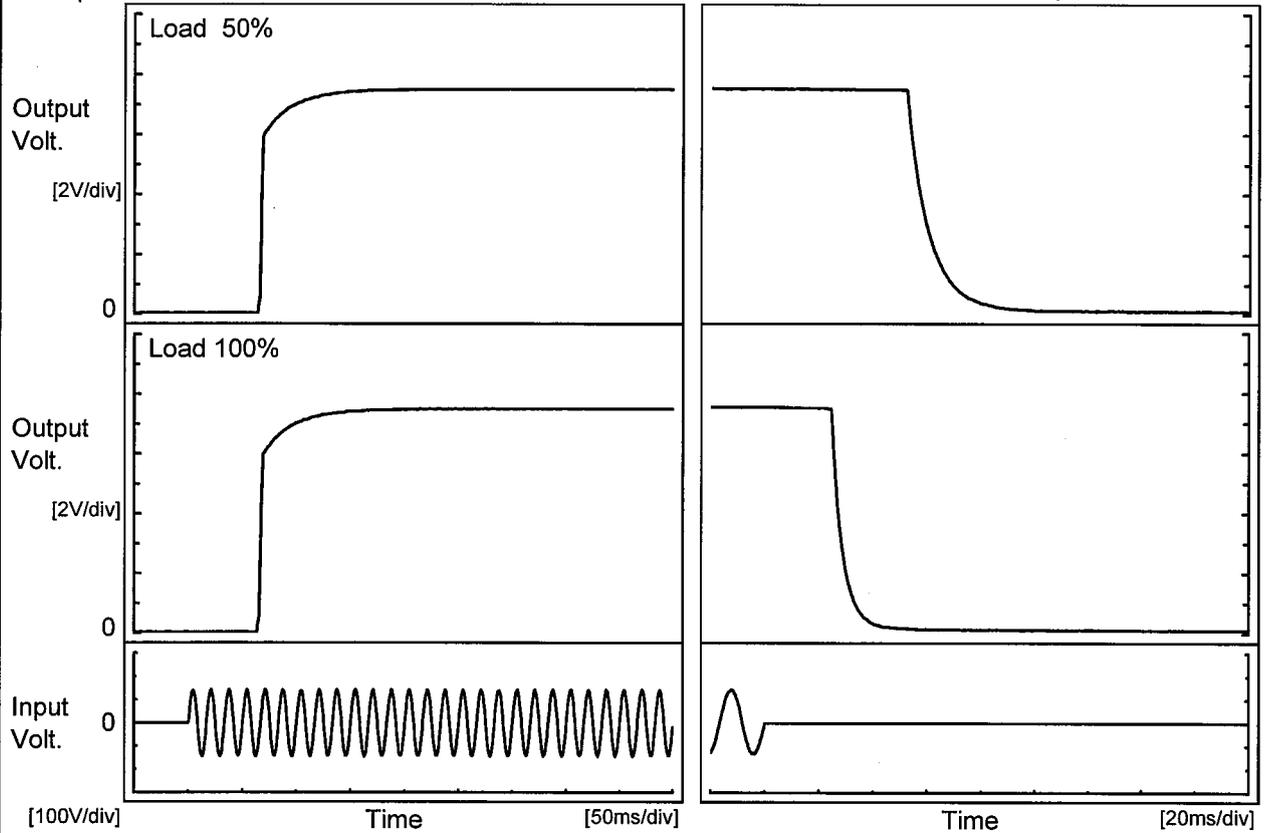


<b>COSEL</b>																									
Model	LGA50A-15	Temperature	25°C																						
Item	Time Lapse Drift	Testing Circuitry	Figure A																						
Object	+15V3.5A																								
<p>1.Graph</p> <p style="text-align: center;">Time [H]</p> <p>Input Volt.    100V Load            100%</p>		<p>2.Values</p> <table border="1"> <thead> <tr> <th>Time since start [H]</th> <th>Output Voltage [V]</th> </tr> </thead> <tbody> <tr><td>0.0</td><td>15.094</td></tr> <tr><td>0.5</td><td>15.085</td></tr> <tr><td>1.0</td><td>15.085</td></tr> <tr><td>2.0</td><td>15.085</td></tr> <tr><td>3.0</td><td>15.085</td></tr> <tr><td>4.0</td><td>15.084</td></tr> <tr><td>5.0</td><td>15.084</td></tr> <tr><td>6.0</td><td>15.084</td></tr> <tr><td>7.0</td><td>15.084</td></tr> <tr><td>8.0</td><td>15.084</td></tr> </tbody> </table>		Time since start [H]	Output Voltage [V]	0.0	15.094	0.5	15.085	1.0	15.085	2.0	15.085	3.0	15.085	4.0	15.084	5.0	15.084	6.0	15.084	7.0	15.084	8.0	15.084
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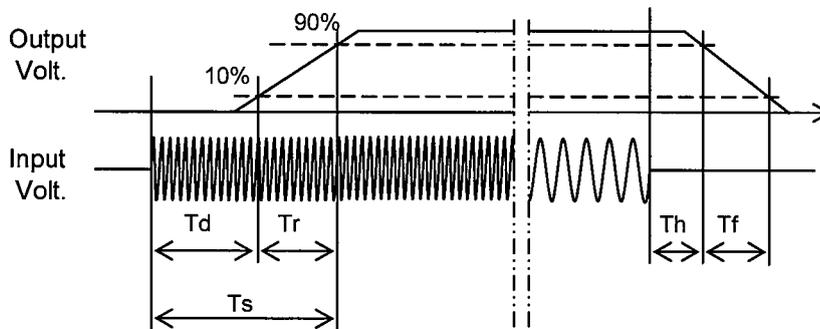
Model	LGA50A-15	Temperature	25°C
Item	Rise and Fall Time	Testing Circuitry	Figure A
Object	+15V3.5A		

1. Graph



2. Values

Load	Time	Td	Tr	Ts	Th	Tf
50 %		66.3	23.5	89.8	53.0	18.7
100 %		65.8	23.8	89.6	25.1	9.5





<p>Model LGA50A-15</p> <p>Item Hold-Up Time</p> <p>Object +15V3.5A</p>		<p>Temperature 25°C</p> <p>Testing Circuitry Figure A</p>																																
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<p>This duration covers from Shut-off of input voltage to the moment when output voltage descends to the rated range of voltage accuracy.                  Note: Slanted line shows the range of the rated input voltage.</p>																																		



<p>Model LGA50A-15</p> <p>Item Instantaneous Interruption Compensation</p> <p>Object +15V3.5A</p>		<p>Temperature 25°C</p> <p>Testing Circuitry Figure A</p>																																																			
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<p>Model LGA50A-15</p> <p>Item Minimum Input Voltage for Regulated Output Voltage</p> <p>Object +15V3.5A</p>		<p>Testing Circuitry Figure A</p>																																						
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<p>Model LGA50A-15</p> <p>Item Overcurrent Protection</p> <p>Object +15V3.5A</p>		<p>Temperature 25°C</p> <p>Testing Circuitry Figure A</p>																																																							
<p>1.Graph</p> <p>                     _____ Input Volt. 85V                      _____ Input Volt. 100V                      _____ Input Volt. 132V                 </p> <p>Output Voltage [V]</p> <p>Load Current [A]</p> <p>Note: Slanted line shows the range of the rated load current.</p>		<p>2.Values</p> <table border="1"> <thead> <tr> <th rowspan="2">Output Voltage [V]</th> <th colspan="3">Load Current [A]</th> </tr> <tr> <th>Input Volt. 85[V]</th> <th>Input Volt. 100[V]</th> <th>Input Volt. 132[V]</th> </tr> </thead> <tbody> <tr><td>15.0</td><td>3.77</td><td>3.52</td><td>3.78</td></tr> <tr><td>14.3</td><td>4.60</td><td>4.56</td><td>4.56</td></tr> <tr><td>13.5</td><td>4.62</td><td>4.59</td><td>4.59</td></tr> <tr><td>12.0</td><td>4.67</td><td>4.65</td><td>4.66</td></tr> <tr><td>10.5</td><td>4.72</td><td>4.72</td><td>4.74</td></tr> <tr><td>9.0</td><td>4.77</td><td>4.78</td><td>4.81</td></tr> <tr><td>7.5</td><td>4.83</td><td>4.82</td><td>4.90</td></tr> <tr><td>6.0</td><td>4.88</td><td>4.89</td><td>4.91</td></tr> <tr><td>4.5</td><td>4.93</td><td>4.94</td><td>4.95</td></tr> <tr><td>3.0</td><td>4.98</td><td>4.98</td><td>4.97</td></tr> <tr><td>1.5</td><td>4.97</td><td>4.96</td><td>4.92</td></tr> <tr><td>0.0</td><td>4.74</td><td>4.70</td><td>4.63</td></tr> </tbody> </table>	Output Voltage [V]	Load Current [A]			Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]	15.0	3.77	3.52	3.78	14.3	4.60	4.56	4.56	13.5	4.62	4.59	4.59	12.0	4.67	4.65	4.66	10.5	4.72	4.72	4.74	9.0	4.77	4.78	4.81	7.5	4.83	4.82	4.90	6.0	4.88	4.89	4.91	4.5	4.93	4.94	4.95	3.0	4.98	4.98	4.97	1.5	4.97	4.96	4.92	0.0	4.74	4.70	4.63
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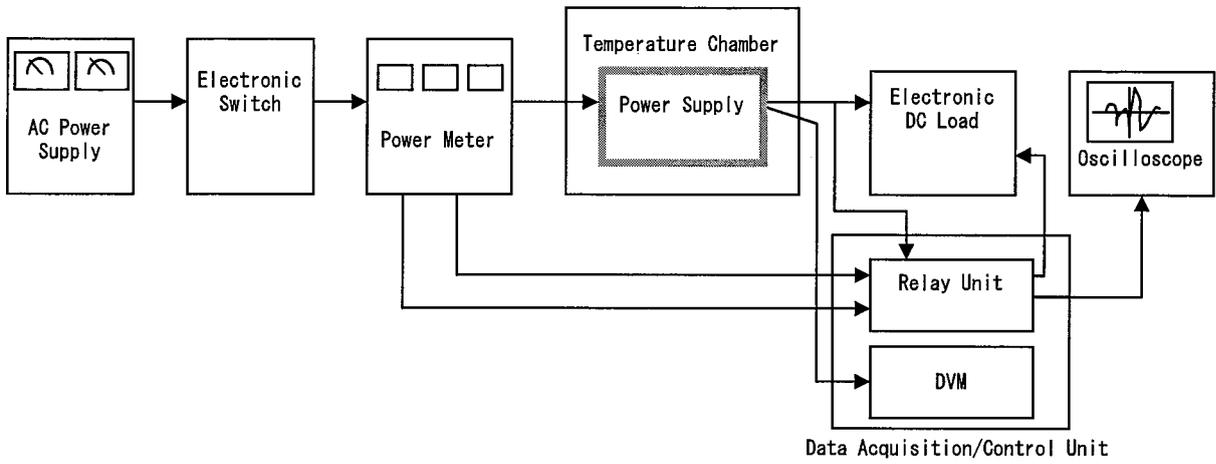


Figure A

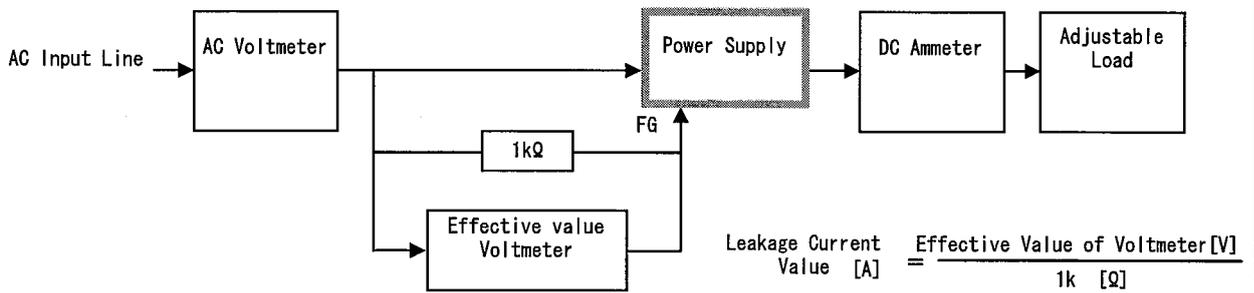


Figure B ( DEN-AN )

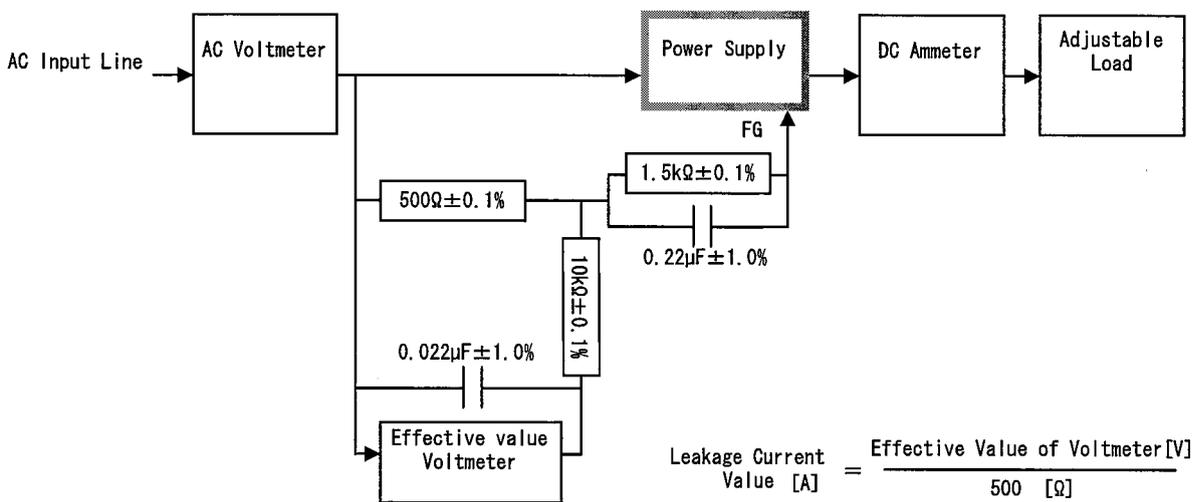


Figure B ( IEC60950-1 )

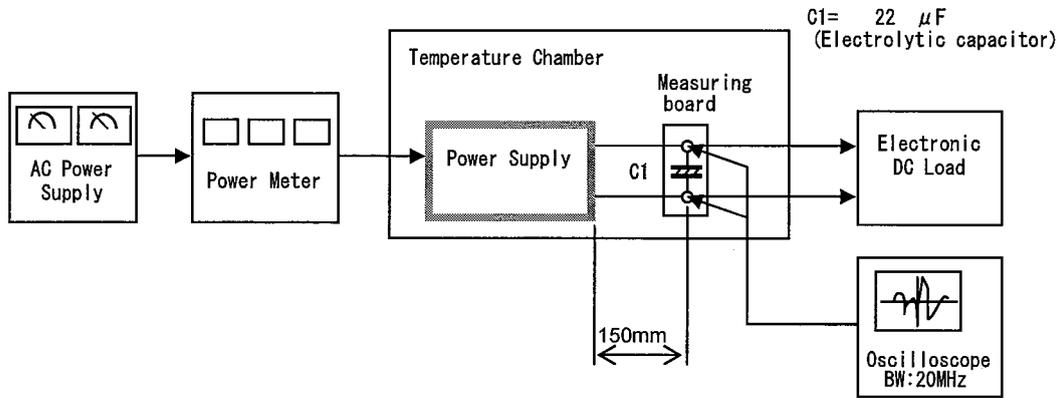


Figure C