

# TEST DATA OF DPG750

(100V INPUT)

AC-DC Front End Module  
March.8. 2010

Approved by : Tatsuya Mano  
Tatsuya Mano Design Manager

Prepared by : Satoshi Uetani  
Satoshi Uetani Design Engineer

**COSEL CO.,LTD.**



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Model	DPG750																																																					
Item	Input Current (by Load Power)																																																					
Object	<hr/>																																																					
1.Graph	—△— Input Volt. 85V - - □ - - Input Volt. 100V - - ○ - - Input Volt. 132V																																																					
	<p>The graph shows three curves representing different input voltages: 85V (solid line with triangles), 100V (dashed line with squares), and 132V (dash-dot line with circles). The curves show that as load power increases, input current also increases. A slanted line is drawn across the graph, representing the rated load current range.</p> <table border="1"> <caption>Data points estimated from the graph</caption> <thead> <tr> <th>Load Power [W]</th> <th>Input Current [A] (85V)</th> <th>Input Current [A] (100V)</th> <th>Input Current [A] (132V)</th> </tr> </thead> <tbody> <tr><td>0</td><td>0.15</td><td>0.18</td><td>0.23</td></tr> <tr><td>50</td><td>0.75</td><td>0.65</td><td>0.53</td></tr> <tr><td>150</td><td>2.00</td><td>1.70</td><td>1.30</td></tr> <tr><td>250</td><td>3.23</td><td>2.77</td><td>2.08</td></tr> <tr><td>300</td><td>3.95</td><td>3.31</td><td>2.48</td></tr> <tr><td>400</td><td>5.10</td><td>4.33</td><td>3.26</td></tr> <tr><td>500</td><td>6.41</td><td>5.40</td><td>4.06</td></tr> <tr><td>550</td><td>7.07</td><td>5.90</td><td>4.45</td></tr> </tbody> </table>			Load Power [W]	Input Current [A] (85V)	Input Current [A] (100V)	Input Current [A] (132V)	0	0.15	0.18	0.23	50	0.75	0.65	0.53	150	2.00	1.70	1.30	250	3.23	2.77	2.08	300	3.95	3.31	2.48	400	5.10	4.33	3.26	500	6.41	5.40	4.06	550	7.07	5.90	4.45															
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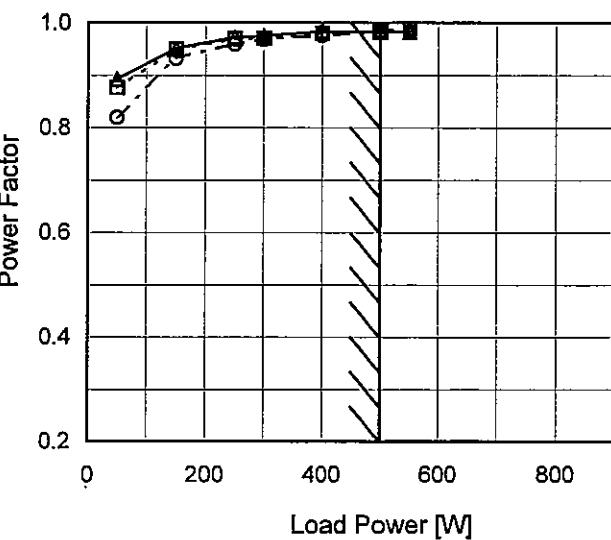
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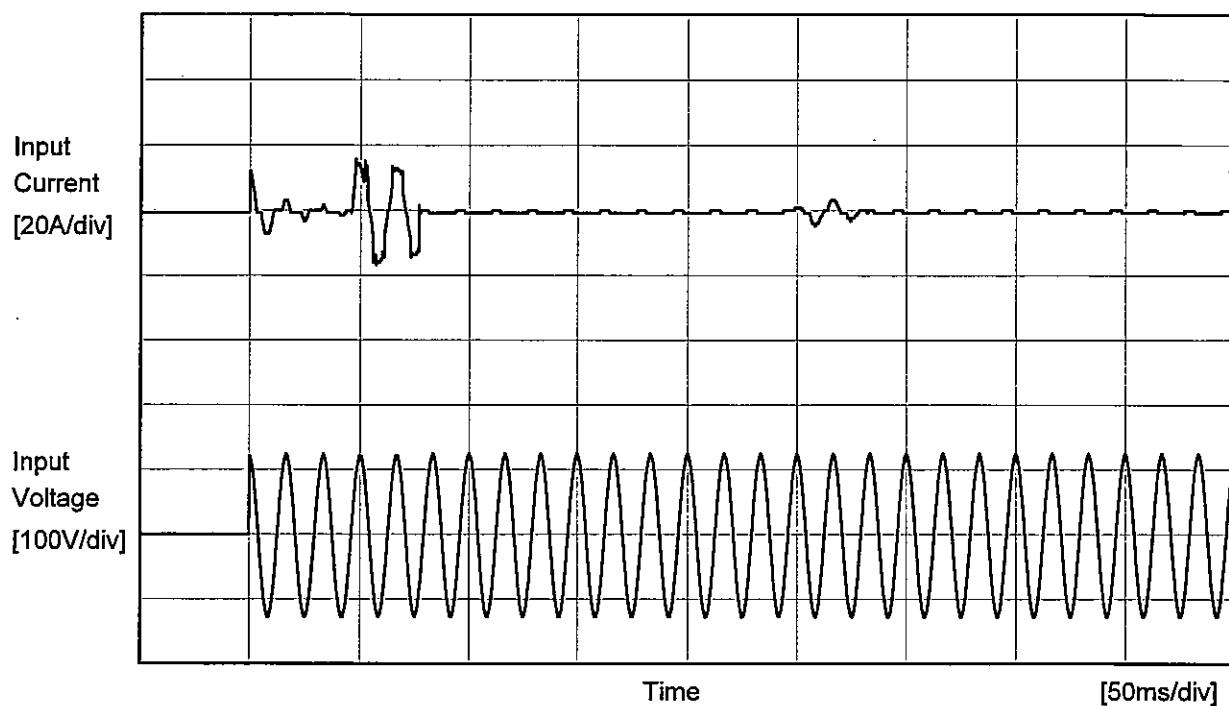
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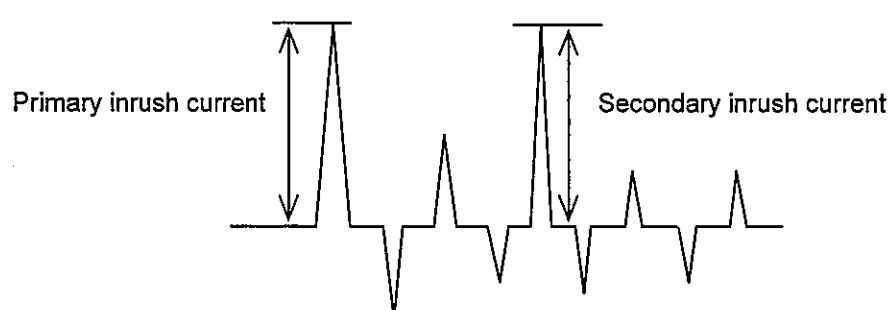
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Model	DPG750	Temperature Testing Circuitry Figure A
Item	Inrush Current	
Object	—	



Input Voltage      100 V  
 Frequency      60 Hz  
 Load      0 %

Primary inrush current      12.0 A  
 Secondary inrush current      16.8 A





Model	DPG750	Temperature Testing Circuitry	25°C Figure B
Item	Leakage Current		
Object	<hr/>		

### 1. Results

Standards	Leakage Current [mA]		
	Input Volt. 85 [V]	Input Volt. 100 [V]	Input Volt. 132 [V]
(A)DEN-AN	0.11	0.15	0.19
(B)IEC60950-1	0.11	0.16	0.19

Standards	Leakage Current [mA]		
	Input Volt. 170 [V]	Input Volt. 230 [V]	Input Volt. 264 [V]
(B)IEC60950-1	-	-	-

### 2. Condition

Leakage current value is concluded after measuring both phases of AC input and by choosing the larger one.

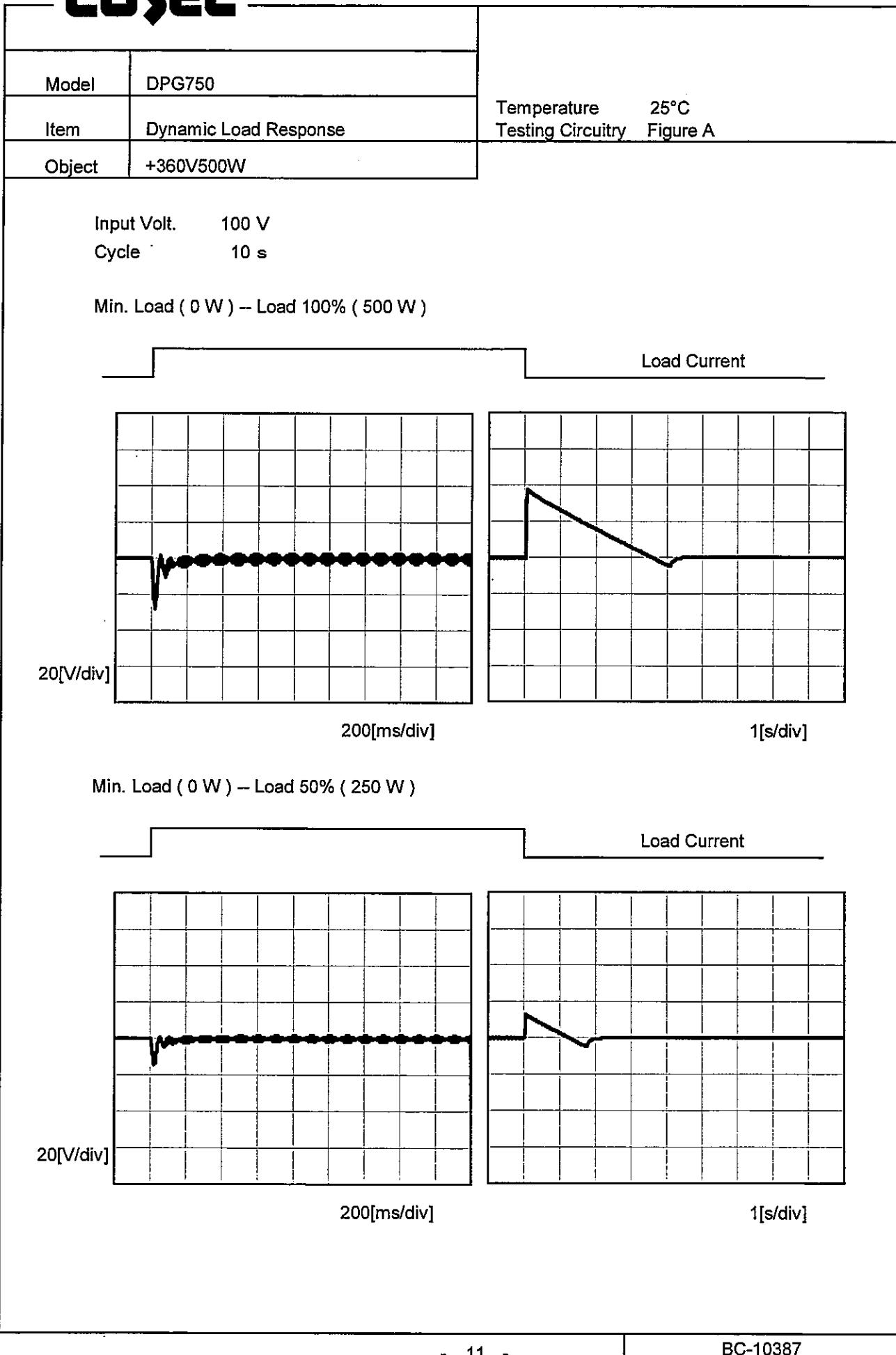
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Item	Line Regulation	Temperature 25°C Testing Circuitry Figure A																																
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**COSEL**

Model	DPG750	Temperature 25°C Testing Circuitry Figure A																																																					
Item	Load Regulation																																																						
Object	+360V 500W																																																						
1.Graph	<p>—▲— Input Volt. 85V      - - □ - - Input Volt. 100V      - - ○ - - Input Volt. 132V</p>	2.Values																																																					
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Note: Slanted line shows the range of the rated load current.

**COSEL**

# COSEL

Model	DPG750																																							
Item	Ripple Voltage (by Load Current)	Temperature 25°C Testing Circuitry Figure A																																						
Object	+360V500W																																							
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<p>Measured by 20 MHz Oscilloscope. Ripple Voltage is shown as p-p in the figure below. Note: Slanted line shows the range of the rated load current.</p> <p>T1: Due to AC Input Line T2: Due to Switching</p> <p>Fig. Complex Ripple Wave Form</p>																																								

**COSEL**

Model	DPG750																																																					
Item	Ambient Temperature Drift																																																					
Object	+360V 500W																																																					
1.Graph	<p>Output Voltage [V]</p> <p>Ambient Temperature [°C]</p> <p>Load 100%</p> <p>Legend:</p> <ul style="list-style-type: none"> <li>Input Volt. 85V</li> <li>Input Volt. 100V</li> <li>Input Volt. 132V</li> </ul>																																																					
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Note: Slanted line shows the range of the rated ambient temperature.																																																						



Model	DPG750	Testing Circuitry Figure A
Item	Output Voltage Accuracy	
Object	+360V 500W	

### 1. Output Voltage Accuracy

This is defined as the value of the output voltage, regulation load, ambient temperature and input voltage varied at random in the range as specified below.

Temperature : -40 - 85°C

Input Voltage : 85 - 132V

Load Power : 0 - 500W

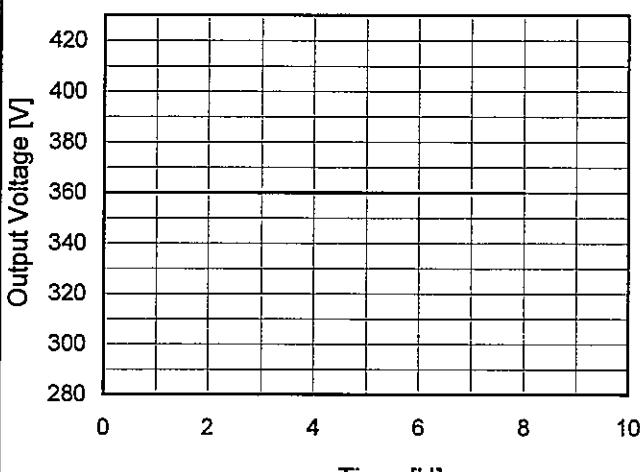
\* Output Voltage Accuracy =  $\pm(\text{Maximum of Output Voltage} - \text{Minimum of Output Voltage}) / 2$

$$\text{* Output Voltage Accuracy (Ration)} = \frac{\text{Output Voltage Accuracy}}{\text{Rated Output Voltage}} \times 100$$

### 2. Values

Item	Temperature [°C]	Input Voltage[V]	Output		Output Voltage Accuracy	
			Power[W]	Voltage[V]	Value [V]	Ration [%]
Maximum Voltage	25	132	0	360.29	$\pm 0.5$	$\pm 0.1$
Minimum Voltage	100	132	500	359.30		

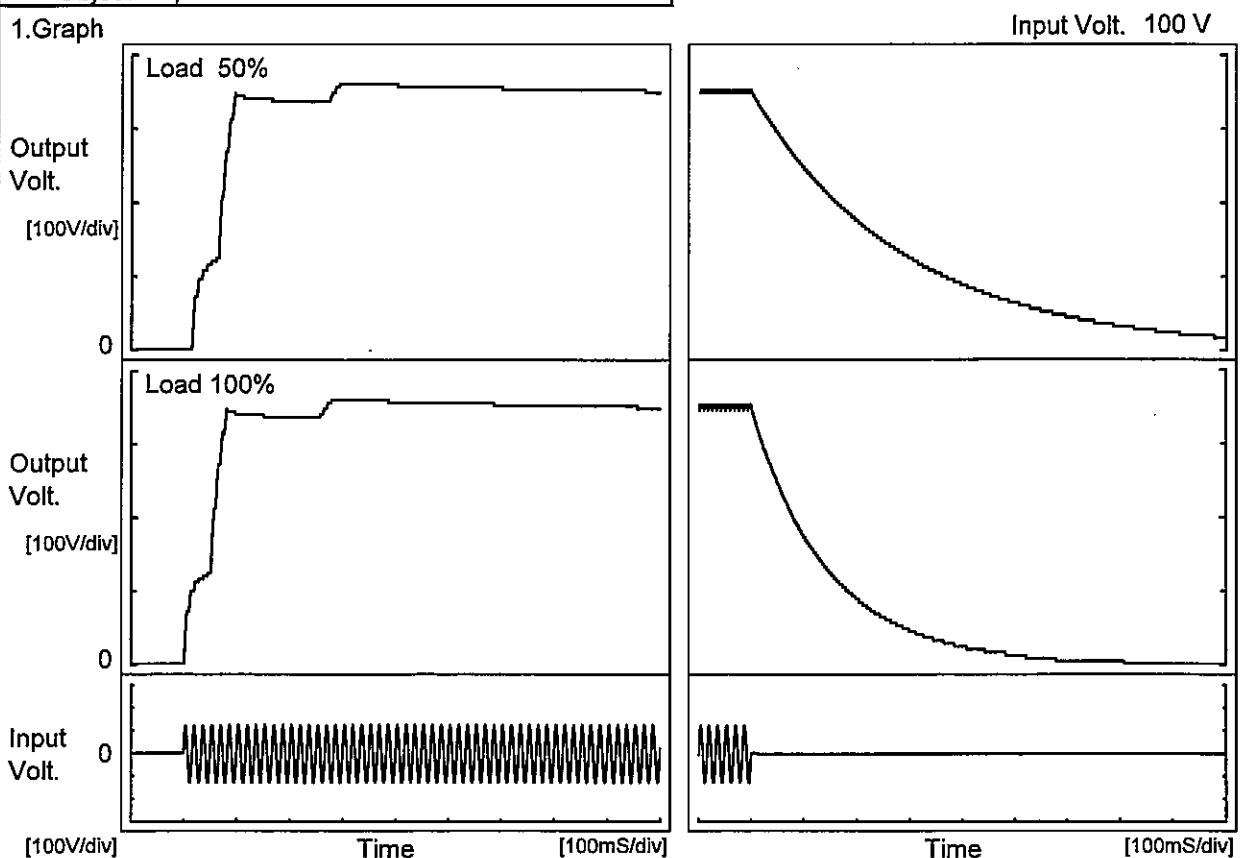
**COSEL**

Model	DPG750	Temperature      25°C Testing Circuitry      Figure A																						
Item	Time Lapse Drift																							
Object	+360V 500W																							
1. Graph		2. Values																						
 <p>Output Voltage [V]</p> <p>Time [H]</p> <p>Input Volt. 100V Load 100%</p>		<table border="1"> <thead> <tr> <th>Time since start [H]</th> <th>Output Voltage [V]</th> </tr> </thead> <tbody> <tr><td>0.0</td><td>359.96</td></tr> <tr><td>0.5</td><td>360.01</td></tr> <tr><td>1.0</td><td>360.01</td></tr> <tr><td>2.0</td><td>360.01</td></tr> <tr><td>3.0</td><td>360.01</td></tr> <tr><td>4.0</td><td>360.01</td></tr> <tr><td>5.0</td><td>360.01</td></tr> <tr><td>6.0</td><td>360.01</td></tr> <tr><td>7.0</td><td>360.01</td></tr> <tr><td>8.0</td><td>360.01</td></tr> </tbody> </table>	Time since start [H]	Output Voltage [V]	0.0	359.96	0.5	360.01	1.0	360.01	2.0	360.01	3.0	360.01	4.0	360.01	5.0	360.01	6.0	360.01	7.0	360.01	8.0	360.01
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8.0	360.01																							

**COSEL**

Model	DPG750	Temperature Testing Circuitry Figure A	25°C
Item	Rise and Fall Time		
Object	+360V 500W		

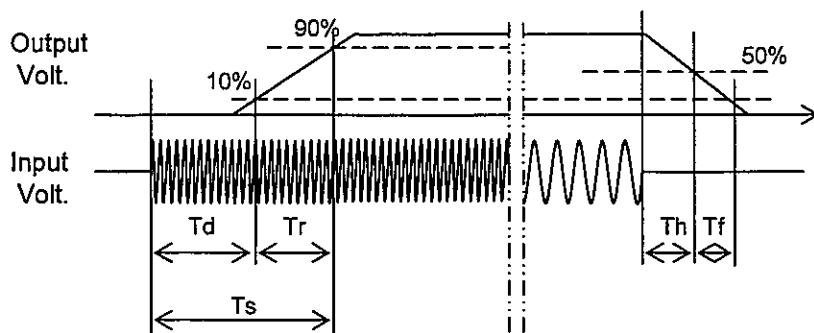
## 1. Graph



## 2. Values

[mS]

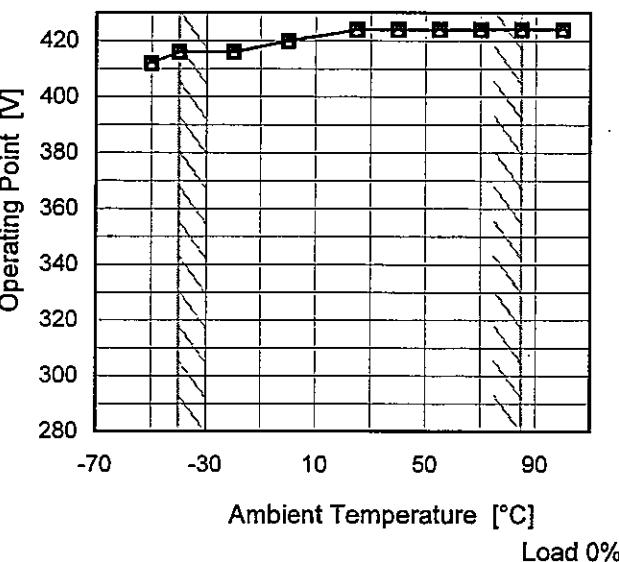
Load	Time	Td	Tr	Ts	Th	Tf
50 %		3.0	75.0	78.0	194.0	492.0
100 %		3.0	74.0	77.0	97.0	247.0



**COSEL**

Model	DPG750																																							
Item	Minimum Input Voltage for Regulated Output Voltage																																							
Object	+360V 500W																																							
1. Graph																																								
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**COSEL**

Model	DPG750																																																					
Item	Overvoltage Protection																																																					
Object	+360V500W																																																					
1.Graph	—△— Input Volt. 85V - - □ - - Input Volt. 100V - - ○ - - Input Volt. 132V																																																					
																																																						
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COSEL

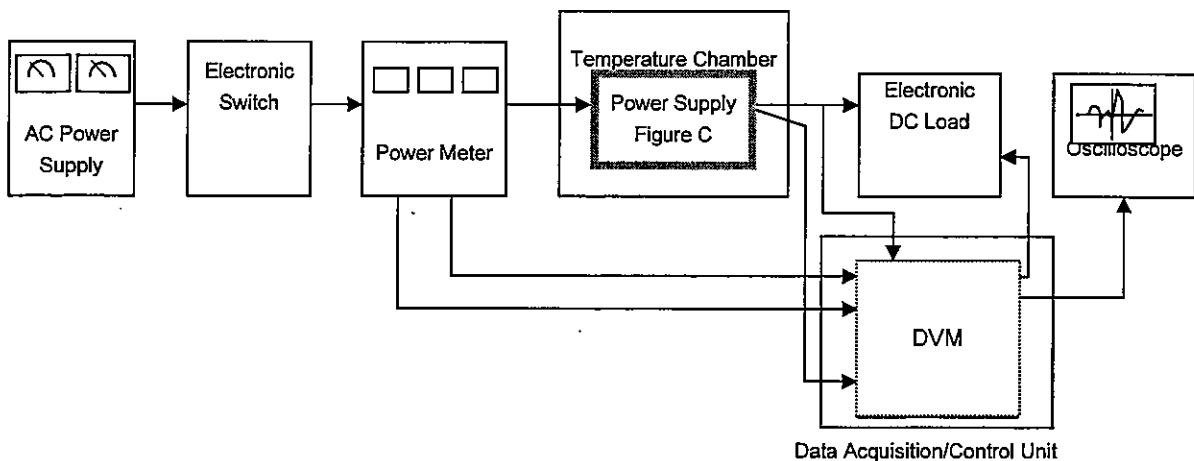


Figure A

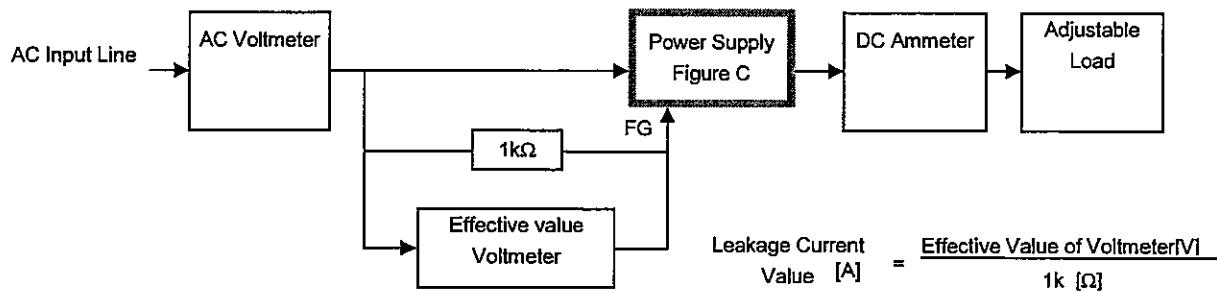


Figure B ( DEN-AN )

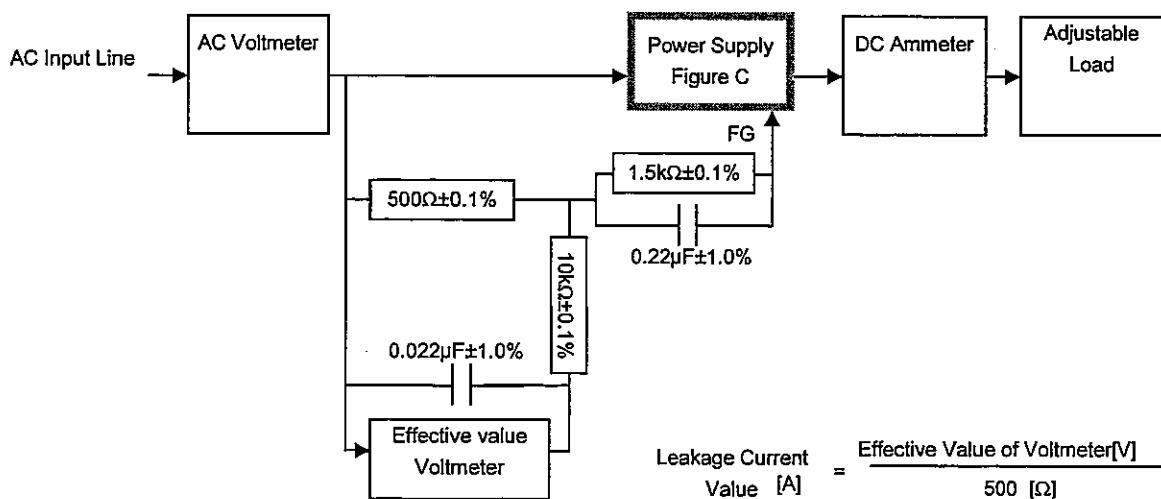


Figure B ( IEC60950-1 )

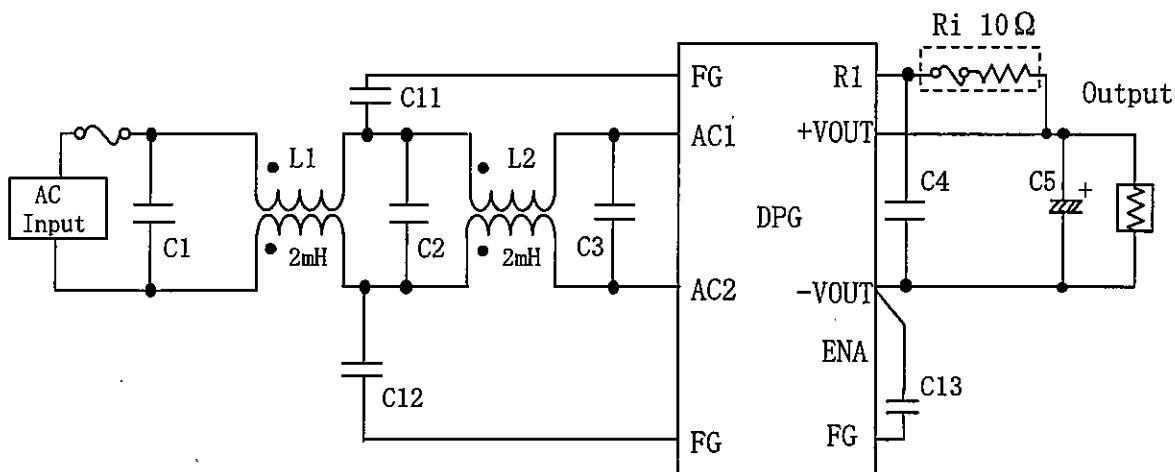
**COSEL**

Figure C

- C1, C2, C4 : 0.68μF 250V Film Capacitor ×2  
C3 : 1.0μF 250V Film Capacitor ×2  
C5 : 560μF 450V Electrolytic Capacitor  
C11, C12, C13 : 2200pF Ceramic Capacitor  
L1, L2 : SC-15-200(NEC TOKIN)