



## CBS20048 EMI/EMS Test result

May 13, 2003  
Design engineering dep.

Approved : *J. Yasuda*

Prepared : *T. Oiwake*

No.	Test item	Conditions	Conditions of Acceptability	Result
1	Line conduction	(1) Rated input(DC48V) (2) Rated load (3) Ambient temp. $25 \pm 10^{\circ}\text{C}$ (4) Testing circuitry Fig.1 and Fig.3	(1)Meets the undermentioned standard. FCC Part15 classB , VCCI classB CISPR22 classB , EN55022-B	OK
2	Radiated emission	(1) Rated input(DC48V) (2) Rated load (3) Ambient temp. $25 \pm 10^{\circ}\text{C}$ (4) Testing circuitry Fig.1 and Fig.3	(1)Meets the undermentioned standard. FCC Part15 classB , VCCI classB CISPR22 classB , EN55022-B	OK
3	Static electricity immunity test (EN61000-4-2)	(1) Rated input(DC48V) (2) Rated load (3) Ambient temp. $25 \pm 10^{\circ}\text{C}$ (4) Contact discharge voltage 8[kV] (EN61000-4-2 Level 4) (5) Testing circuitry Fig.5	(1)No protection circuit failure. (2)No output voltage drop with control circuit failure. (3)No any other function failure	OK
4	Radiated, radio-frequency, electromagnetic field immunity test (EN61000-4-3)	(1) Rated input(DC48V) (2) Rated load (3) Ambient temp. $25 \pm 10^{\circ}\text{C}$ (4)Testing field strength 10[V/m] (EN61000-4-3 Level 3) (5) Testing circuitry Fig.3	(1)No protection circuit failure. (2)No output voltage drop with control circuit failure. (3)No any other function failure	OK
5	Electrical fast transient/ burst immunity test (EN61000-4-4)	(1) Rated input(DC48V) (2) Rated load (3) Ambient temp. $25 \pm 10^{\circ}\text{C}$ (4) Test peak voltage 4[kV] (IEC61000-4-4 Level 4) (5) Testing circuitry Fig.5	(1)No protection circuit failure. (2)No output voltage drop with control circuit failure. (3)No any other function failure	OK
6	Surge immunity test (EN61000-4-5)	(1) Rated input(DC48V) (2) Rated load (3) Ambient temp. $25 \pm 10^{\circ}\text{C}$ (4) Test voltage Line to line 2[kV] (Level 3) Line to earth 4[kV] (Level 4) (5) Testing circuitry Fig.2 and Fig.4	(1)The power supply is not stop (2)Circuit does not malfunction. (3)No abnormality of the insulation destruction etc. (4)Parts are no damaged.	OK
7	Immunity to conducted disturbances, induced by radio-frequency fields (EN61000-4-6)	(1) Rated input (DC48V) (2) Rated load (3) Ambient temp. $25 \pm 10^{\circ}\text{C}$ (4) Voltage level (e.m.f.) 10[V] (EN61000-4-6 Level 3) (5) Testing circuitry Fig.3	(1)No protection circuit failure. (2)No output voltage drop with control circuit failure. (3)No any other function failure	OK

## EMI/EMS testing circuitry

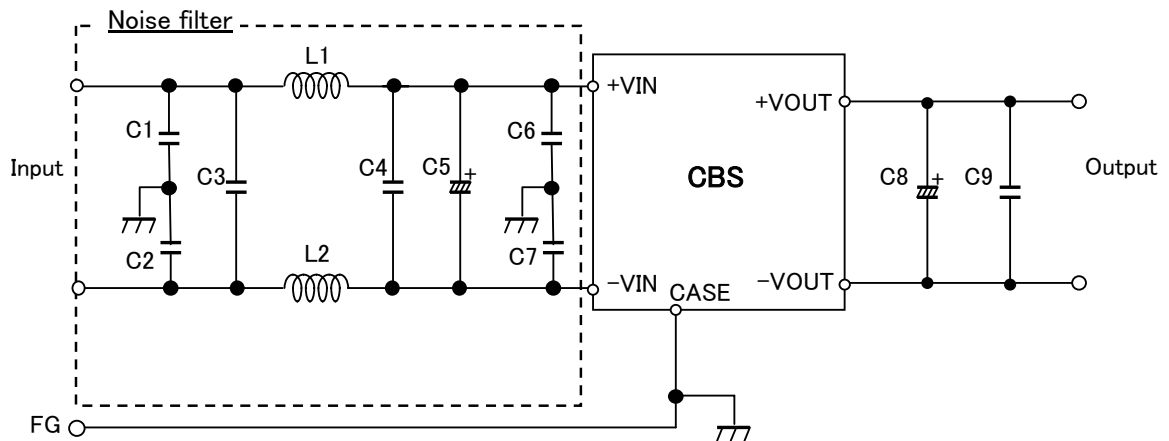


Fig.1 testing circuitry (No.1 and No.2)

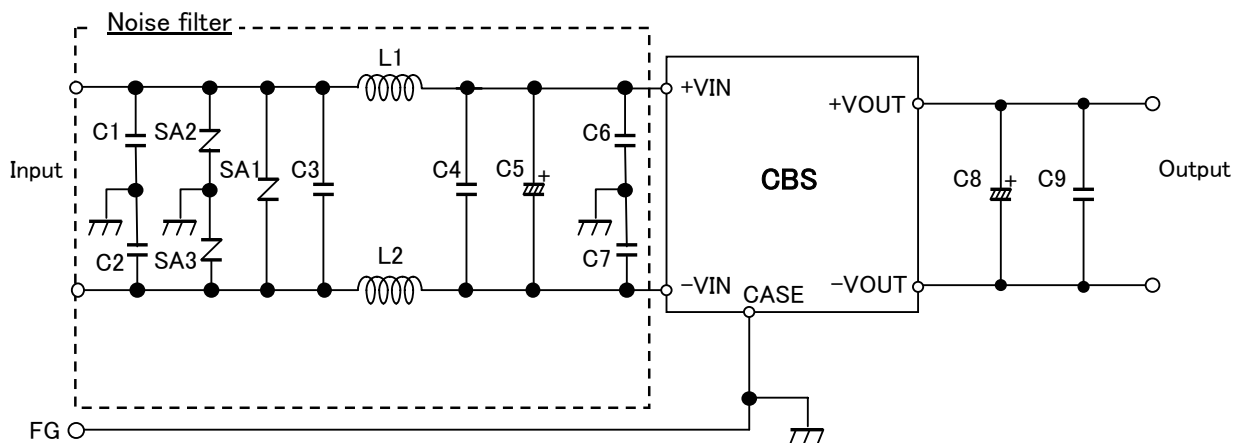


Fig.2 testing circuitry (No.6)

- |                        |  |
|------------------------|--|
| C1, C2, C3, C4, C6, C7 | : 3 $\mu$ F 100V Ceramic capacitor                         |
| C5                     | : 220 $\mu$ F 80V Electrolytic capacitor                   |
| C8                     | : 2200 $\mu$ F 10V Electrolytic capacitor (CBS2004803, 05) |
|                        | : 1000 $\mu$ F 25V Electrolytic capacitor (CBS2004812, 15) |
|                        | : 470 $\mu$ F 35V Electrolytic capacitor (CBS2004824, 28)  |
|                        | : 330 $\mu$ F 100V Electrolytic capacitor (CBS2004848)     |
| C9                     | : 0.1 $\mu$ F Film capacitor                               |
| L1, L2                 | : 1.3 $\mu$ H Choke Coil                                   |
| SA1                    | : ERZV10D101 (MATSUSHITA ELECTNIC CO., LTD.)               |
| SA2, SA3               | : ERZV07D820 (MATSUSHITA ELECTNIC CO., LTD.)               |
|                        | or equivalent  |

○EMI/EMS testing circuitry

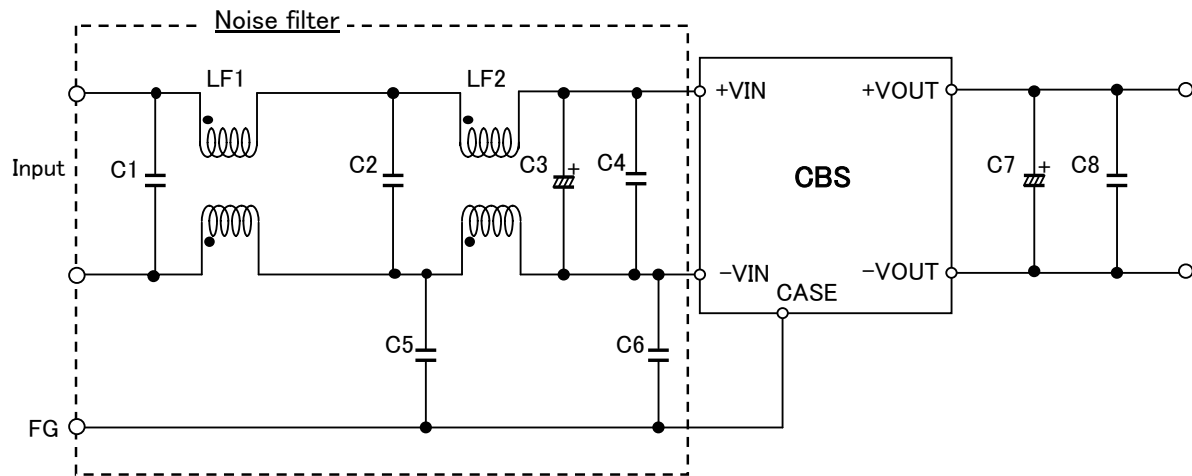


Fig.3 testing circuitry (No.1, No.2, No.4 and No.7)

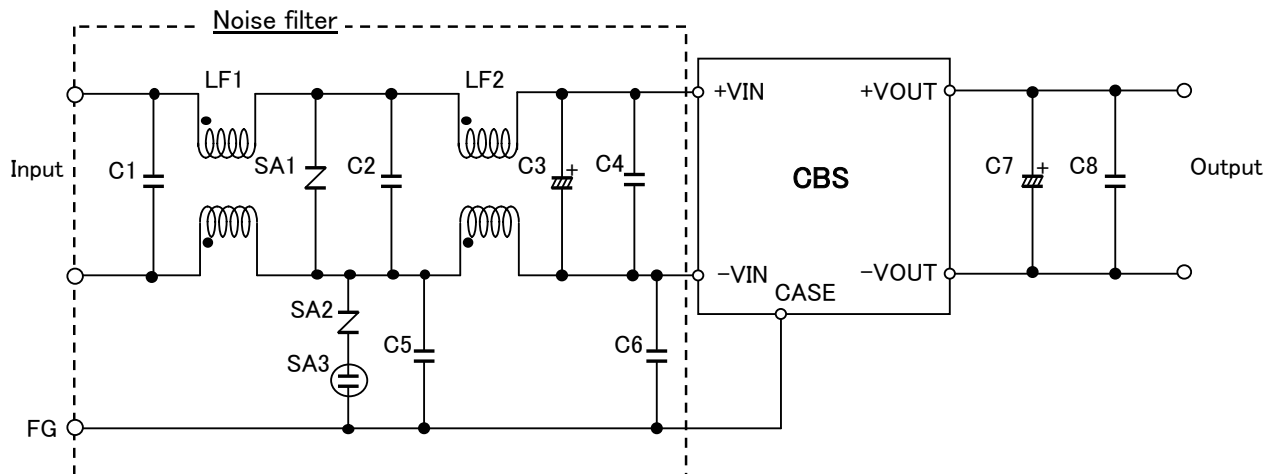


Fig.4 testing circuitry (No.6)

- C1、C2、C4 : 0.33  $\mu$  F 250V Film capacitor
- C3 : 33  $\mu$  F 100V Electrolytic capacitor
- C5、C6 : 4700pF 250V Ceramic capacitor
- C7 : 2200  $\mu$  F 10V Electrolytic capacitor (CBS2004803、05)
- : 1000  $\mu$  F 25V Electrolytic capacitor (CBS2004812、15)
- : 470  $\mu$  F 35V Electrolytic capacitor (CBS2004824、28)
- : 330  $\mu$  F 100V Electrolytic capacitor (CBS2004848)
- C8 : 0.1  $\mu$  F Film capacitor
- LF1、LF2 : 3.0mH 5A Common mode Choke Coil
- SA1、SA2 : ERZV10D101 (MATSUSHITA ELECTNIC CO., LTD.)
- SA3 : DSA-302MA (MITSUBISHI MATERIALS CORP ADVANCED PRODUCTS)
- or equivalent

○EMI/EMS testing circuitry

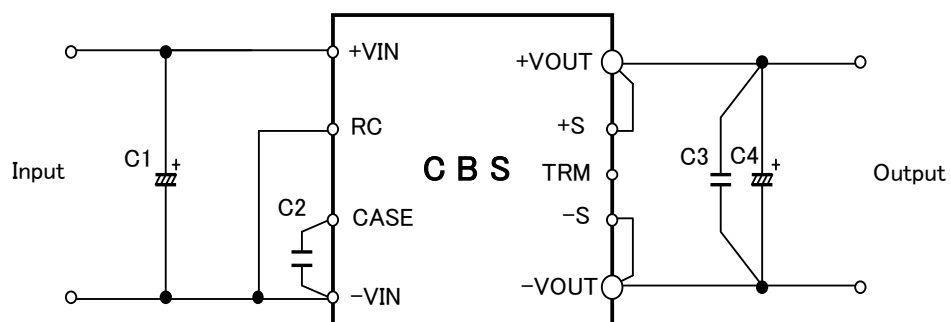


Fig.5 testing circuitry (No.3 and No.5)

- C1 : 33  $\mu$  F 100V Electrolytic capacitor
  - C2 : 4700pF 250V Seramic capacitor
  - C3 : 0.1  $\mu$  F Film capacitor
  - C4 : 2200  $\mu$  F 10V Electrolytic capacitor (CBS2004803、05)
  - : 1000  $\mu$  F 25V Electrolytic capacitor (CBS2004812、15)
  - : 470  $\mu$  F 35V Electrolytic capacitor (CBS2004824、28)
  - : 330  $\mu$  F 100V Electrolytic capacitor (CBS2004848)
- or equivalent

DATA SHEET		Date	2001/3/7
Model	CBS2004828	Temp.	25 °C
Test	Surge immunity test サージ・immunity試験	Humid.	40 %Rh
		Tested by	H.Senda

## 1. Method — according to EN61000-4-5 —

### (1) Points to be applied voltage

電圧印加箇所

— Line to line (ライン - ライン間 : ノーマル) —

1) Between input pin (+V) and input pin (-V)

入力ピン(+V) - 入力ピン(-V)

— Line to case pin (ライン - ケースピン間 : コモン) —

2) Between input pin (+V) and case pin

入力ピン(+V) - ケースピン

3) Between input pin (-V) and case pin

入力ピン(-V) - ケースピン

### (2) Test at the selected levels shown below

印加電圧(レベル)は、下表に従う

### (3) Change the polarity (+/-) of applied voltage

印加極性 +/- の条件でそれぞれ実施

### (4) Number of tests : Six positive and six negative at selected points.

試験の回数 : それぞれの印加箇所、正負各6回試験する

### (5) Repetition rate : maximum 1/min.

繰り返し速度 : 最大1回/分 (1分以上の間隔をおく)

Test levels of EN61000-4-5

Level	1	2	3	4
Test voltage [kV]	0.5	1	2	4

## 2. Conditions

(1) Input : DC48V

(2) Output : Rated output

(3) Ambient temp. : 25 ± 10°C

(4) Testing circuitry : Refer to item 5

## 3. Conditions of Acceptability

Line to line : According to EN50082-2 (EN61000-4-5 Level 3)

ライン - ライン間 (ノーマル) : EN50082-2 (EN61000-4-5 レベル3) を満足すること

Line to earth : According to EN50082-2 (EN61000-4-5 Level 4)

ライン - ケースピン間 (コモン) : EN50082-2 (EN61000-4-5 レベル4) を満足すること

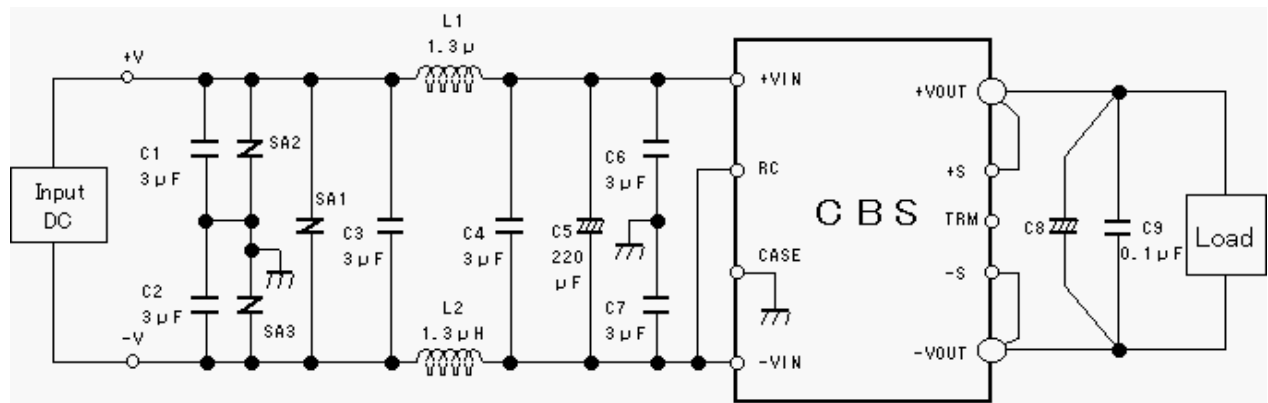
## 4. Result

No.	Voltage [kV]	Polarity	Line (+V) - Line (-V)
1	0.5	+	OK
2		-	OK
3	1	+	OK
4		-	OK
5	2	+	OK
6		-	OK

No.	Voltage [kV]	Polarity	Line (+V) - Case pin	Line (-V) - Case pin
1	0.5	+	OK	OK
2		-	OK	OK
3	1	+	OK	OK
4		-	OK	OK
5	2	+	OK	OK
6		-	OK	OK
7	4	+	OK	OK
8		-	OK	OK

All are satisfactory to item 3: OK

5. Testing circuitry



L1, L2 : ETQP6F1R3LFA (MATSUSHITA)  
 C1, C2, C3, C5, C6, C7 : CY55Y5P2A305M(TOKIN)  
 C4 : 80V 220 $\mu$ F PFseries (nichicon)  
 C8 : CBS2004803,05 10V 2200 $\mu$ F LXZseries (NIPPON CHEMI-CON)  
       CBS2004812,15 25V 1000 $\mu$ F LXZseries (NIPPON CHEMI-CON)  
       CBS2004824,28 35V 470 $\mu$ F LXZseries (NIPPON CHEMI-CON)  
 SA1 : ERZV10D101 (MATSUSHITA)  
 SA2, SA3 : ERZV07D820 (MATSUSHITA)  
 C9 : MDD21H104M (Nitsuko)

Fig. Testing circuitry

DATA SHEET		Date	2001/3/7
Model	CBS2004828	Temp.	25 °C
Test	Surge immunity test サージ・immunity試験	Humid.	40 %Rh
		Tested by	H.Senda

## 1. Method — according to EN61000-4-5 —

### (1) Points to be applied voltage

電圧印加箇所

— Line to line (ライン - ライン間 : ノーマル) —

1) Between input pin (+V) and input pin (-V)

入力ピン(+V) - 入力ピン(-V)

— Line to case pin (ライン - ケースピン間 : コモン) —

2) Between input pin (+V) and case pin

入力ピン(+V) - ケースピン

3) Between input pin (-V) and case pin

入力ピン(-V) - ケースピン

### (2) Test at the selected levels shown below

印加電圧(レベル)は、下表に従う

### (3) Change the polarity (+/-) of applied voltage

印加極性 +/- の条件でそれぞれ実施

### (4) Number of tests : Six positive and six negative at selected points.

試験の回数 : それぞれの印加箇所、正負各6回試験する

### (5) Repetition rate : maximum 1/min.

繰り返し速度 : 最大1回/分 (1分以上の間隔をおく)

Test levels of EN61000-4-5

Level	1	2	3	4
Test voltage [kV]	0.5	1	2	4

## 2. Conditions

(1) Input : DC48V

(2) Output : Rated output

(3) Ambient temp. : 25 ± 10°C

(4) Testing circuitry : Refer to item 5

## 3. Conditions of Acceptability

Line to line : According to EN50082-2 (EN61000-4-5 Level 3)

ライン - ライン間 (ノーマル) : EN50082-2 (EN61000-4-5 レベル3) を満足すること

Line to earth : According to EN50082-2 (EN61000-4-5 Level 4)

ライン - ケースピン間 (コモン) : EN50082-2 (EN61000-4-5 レベル4) を満足すること

## 4. Result

No.	Voltage [kV]	Polarity	Line (+V) - Line (-V)
1	0.5	+	OK
2		-	OK
3	1	+	OK
4		-	OK
5	2	+	OK
6		-	OK

No.	Voltage [kV]	Polarity	Line (+V) - Case pin	Line (-V) - Case pin
1	0.5	+	OK	OK
2		-	OK	OK
3	1	+	OK	OK
4		-	OK	OK
5	2	+	OK	OK
6		-	OK	OK
7	4	+	OK	OK
8		-	OK	OK

All are satisfactory to item 3: OK

Fig. Testing circuitry