



TEST DATA OF AEA800F-24

Regulated DC Power Supply
August 9, 2022

Approved by : _____ Jun Uchida

Design Manager

Prepared by : _____ Koro Yo

Design Engineer

COSEL CO.,LTD.



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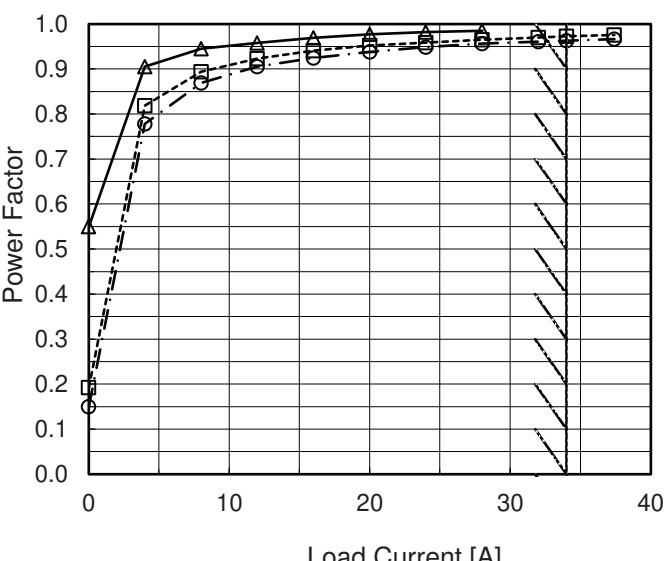
COSEL

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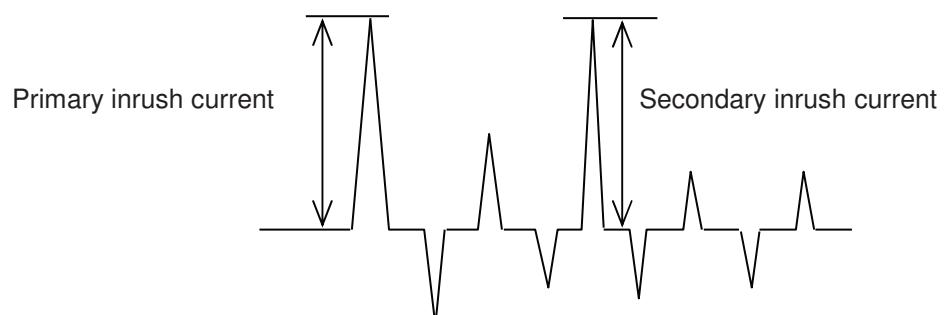
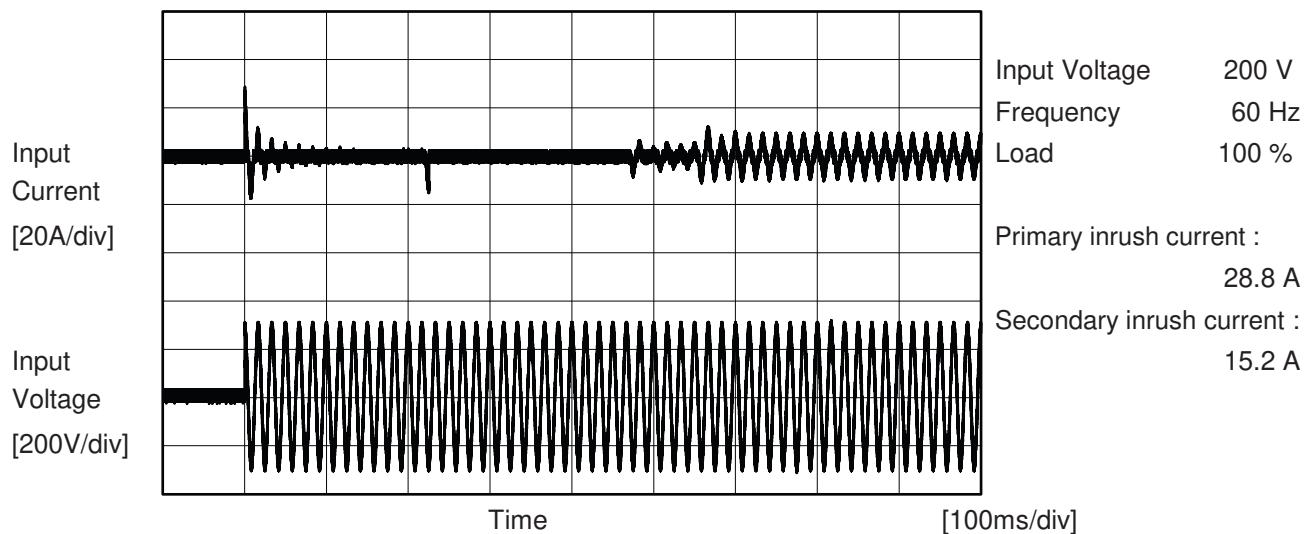
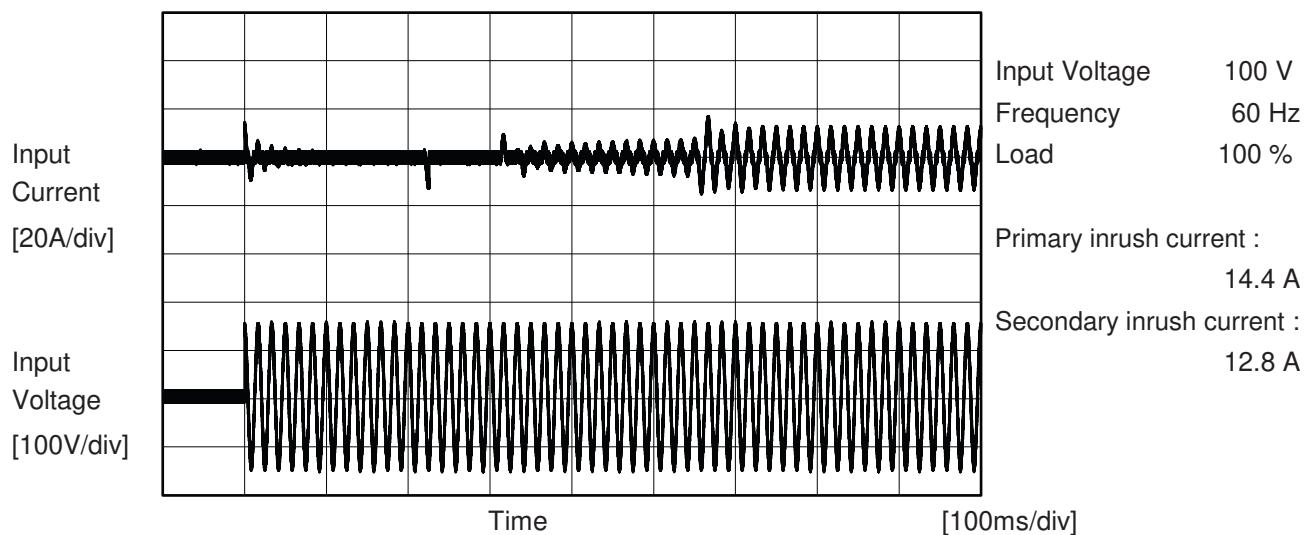
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Note: Slanted line shows the range of the rated load current.

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Model	AEA800F-24	Temperature Testing Circuitry Figure A	25°C
Item	Inrush Current		
Object	_____		





Model	AEA800F-24	Temperature Testing Circuitry	25°C Figure B
Item	Leakage Current		
Object	_____		

1. Results

[mA]

Standards	Testing Circuitry	Measuring Method	Input Volt.			Note
			100 [V]	240 [V]	264 [V]	
DEN-AN	Figure B-1	Both phases	0.08	0.21	0.23	Operation
		One of phases	0.15	0.39	0.44	Stand by
IEC62368-1	Figure B-2	Both phases	0.08	0.20	0.23	Operation
		One of phases	0.15	0.39	0.43	Stand by
	Figure B-3	Both phases	0.08	0.20	0.23	Operation
		One of phases	0.15	0.38	0.43	Stand by
IEC60601-1	Figure B-4	Both phases	0.08	0.20	0.23	Operation
		One of phases	0.15	0.38	0.43	Stand by

The value for "One of phases" is the reference value only.

2. Condition

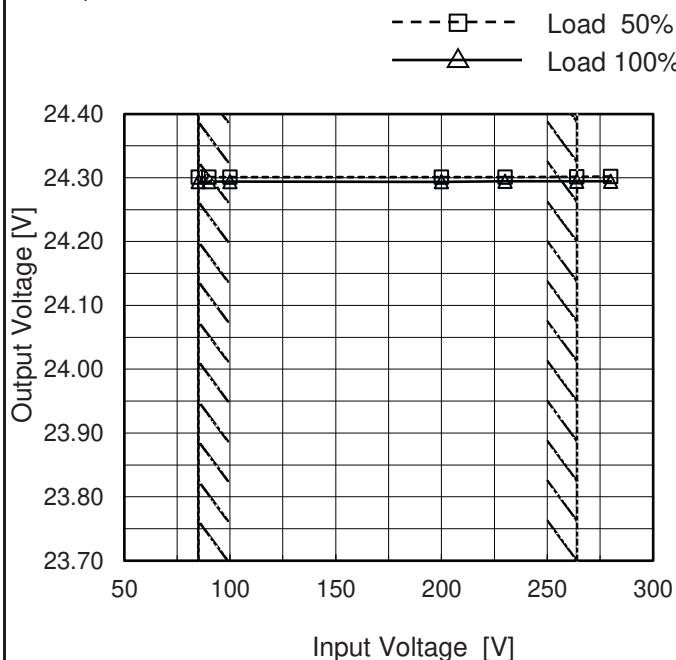
Leakage current value is concluded after measuring both phases of AC input and by choosing the larger one.

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Model	AEA800F-24
Item	Line Regulation
Object	+24V34A

 Temperature 25°C
 Testing Circuitry Figure A

1.Graph



2.Values

Input Voltage [V]	Output Voltage [V]	
	Load 50%	Load 100%
85	24.301	24.294 ※1
90	24.301	24.294 ※2
100	24.301	24.294 ※2
200	24.301	24.294
230	24.301	24.295
264	24.302	24.294
280	24.302	24.294
--	-	-
--	-	-

※1 : Load 60%

※2 : Load 75%

Note: Slanted line shows the range of the rated input voltage.

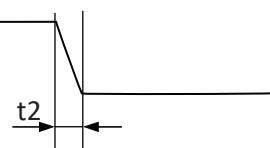
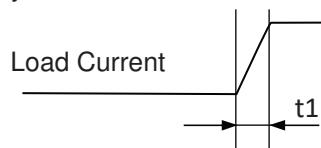
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Item	Ripple-Noise	Temperature	25°C																																																			
Object	+24V34A	Testing Circuitry	Figure C																																																			
1. Graph	<p>Input Voltage 200V</p> <p>Load 100%</p> <p>50[mV/div]</p> <p>20[μs/div]</p>																																																					

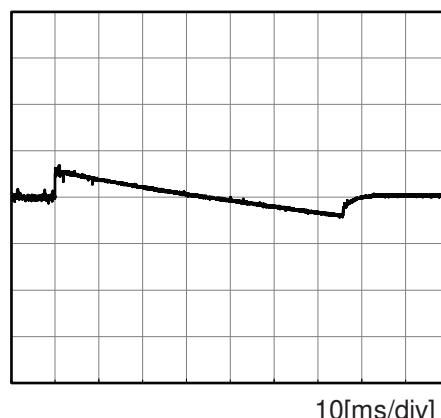
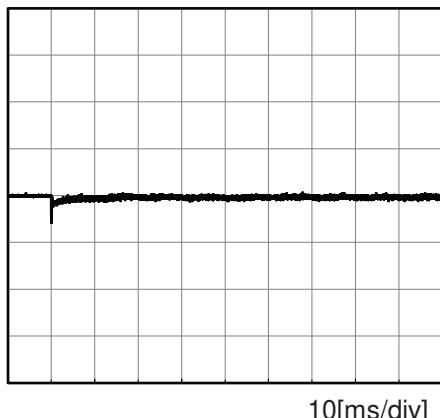
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Model	AEA800F-24	Temperature	25°C
Item	Dynamic Load Response	Testing Circuitry	Figure A
Object	+24V34A		

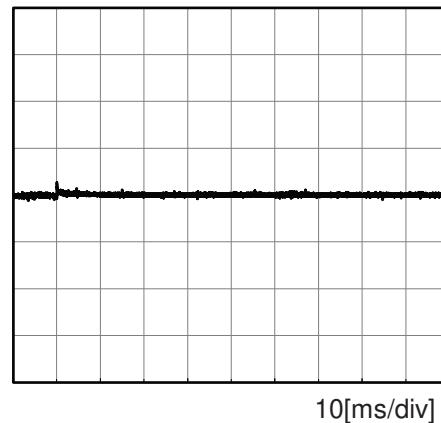
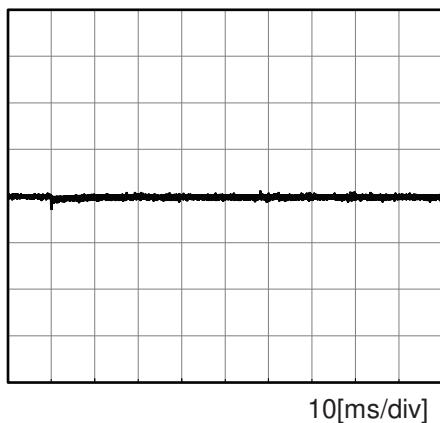
Input Volt. 200 V
Cycle 1000 ms

Response. $t_1=t_2=50\mu\text{s}$. Typ

Load 0%(0A) \longleftrightarrow
Load 100%(34A)



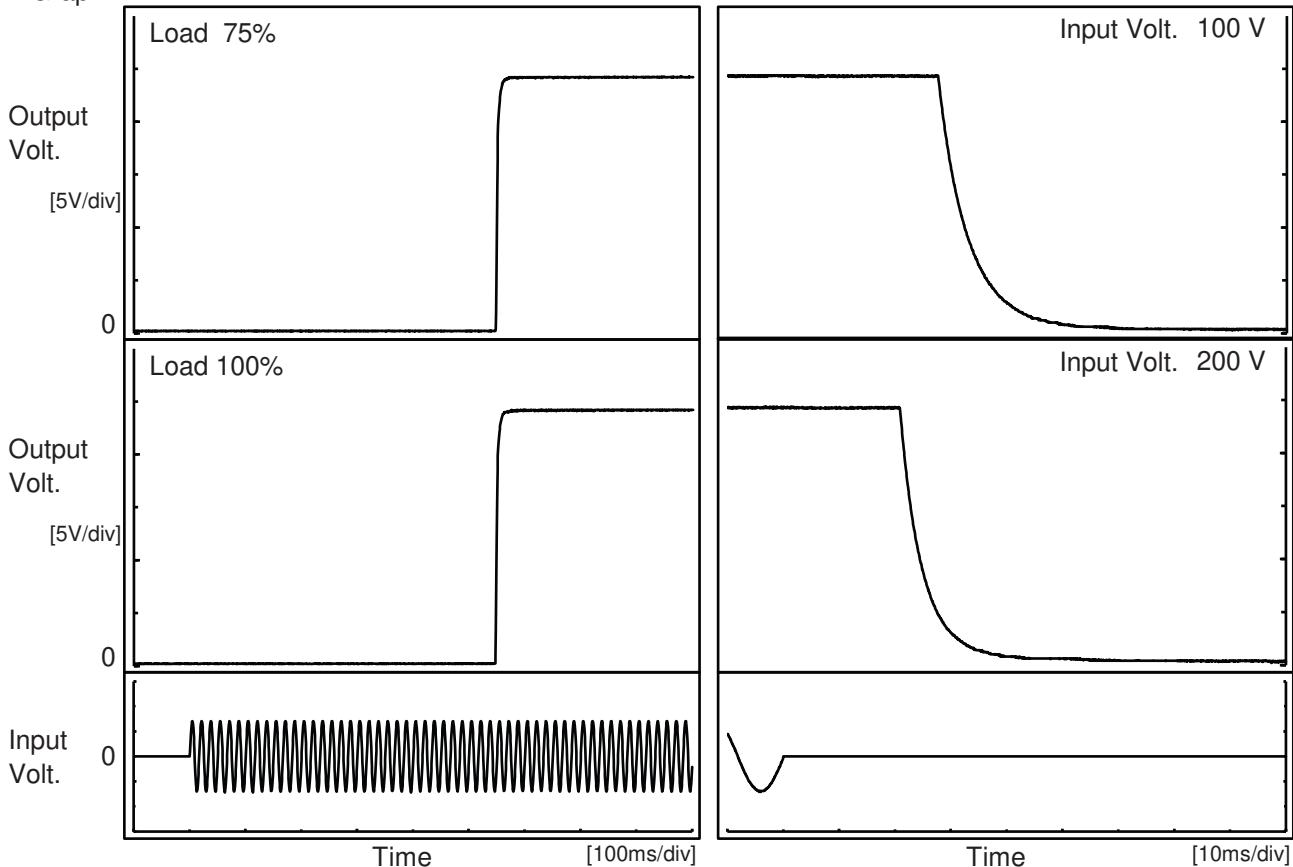
Load 50%(17A) \longleftrightarrow
Load 100%(34A)



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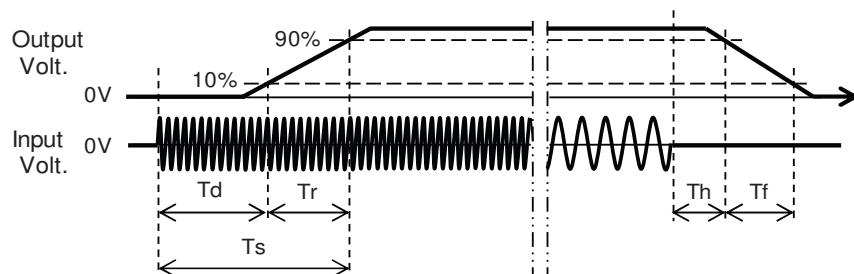
Model	AEA800F-24	Temperature	25°C
Item	Rise and Fall Time	Testing Circuitry	Figure A
Object	+24V34A		

1. Graph



2. Values

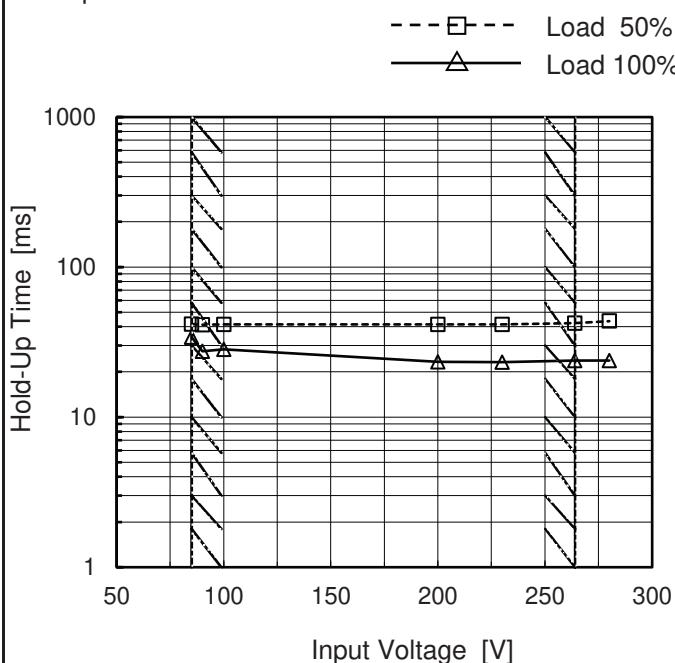
Load	Time	Td	Tr	Ts	Th	Tf	[ms]
100 %		548.0	6.0	554.4	28.0	12.5	
100 %		547.5	6.0	553.5	21.3	9.7	



COSEL

Model	AEA800F-24	Temperature	25°C
Item	Hold-Up Time	Testing Circuitry	Figure A
Object	+24V34A		

1.Graph



2.Values

Input Voltage [V]	Hold-Up Time [ms]	
	Load 50%	Load 100%
85	42	34 ※1
90	41	28 ※2
100	41	28 ※2
200	41	23
230	41	23
264	42	24
280	44	24
--	-	-
--	-	-

※1 : Load 60%

※2 : Load 75%

This duration covers from Shut-off of input voltage to the moment when output voltage descends to the rated range of voltage accuracy.
 Note: Slanted line shows the range of the rated input voltage.

COSEL

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Item	Instantaneous Interruption Compensation	Temperature Testing Circuitry	25°C Figure A																																																			
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<p>Note: Slanted line shows the range of the rated load current.</p>																																																						



Model	AEA800F-24	Temperature Testing Circuitry 25°C Figure A																																																																											
Item	Overcurrent Protection																																																																												
Object	+24V34A																																																																												
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Model	AEA800F-24	Testing Circuitry Figure A
Item	Ambient Temperature Drift	
Object	+24V34A	

1.Values

Load 100%

Ambient Temperature[°C]	Output Voltage [V]		
	Input Volt. 100V	Input Volt. 200V	Input Volt. 230V
-20	24.142	24.142	24.142
25	24.291	24.291	24.291
50	24.333	24.333	24.334

Item	Minimum Input Voltage for Regulated Output Voltage	Testing Circuitry Figure A	
Object	+24V34A		

1.Values

Ambient Temperature[°C]	Input Voltage [V]	
	Load 50%	Load 100%
-20	72	72
25	73	73
50	73	73

Item	Overvoltage Protection	Testing Circuitry Figure A	
Object	+24V34A		

1.Values

Load 0%

Ambient Temperature[°C]	Operating Point [V]	
	Input Volt. 100V	Input Volt. 200V
-20	30.75	30.70
25	31.74	31.74
50	32.27	32.27

COSEL

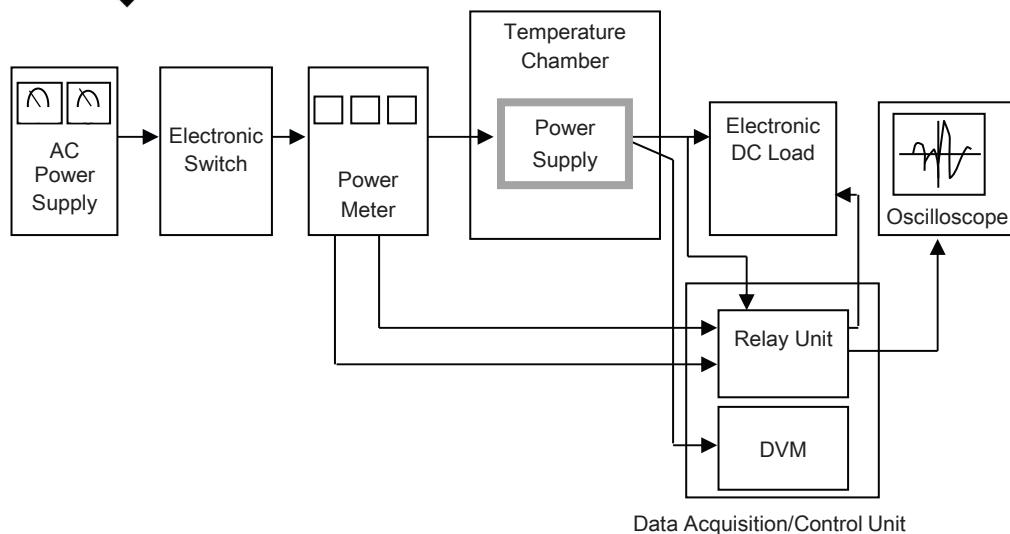


Figure A

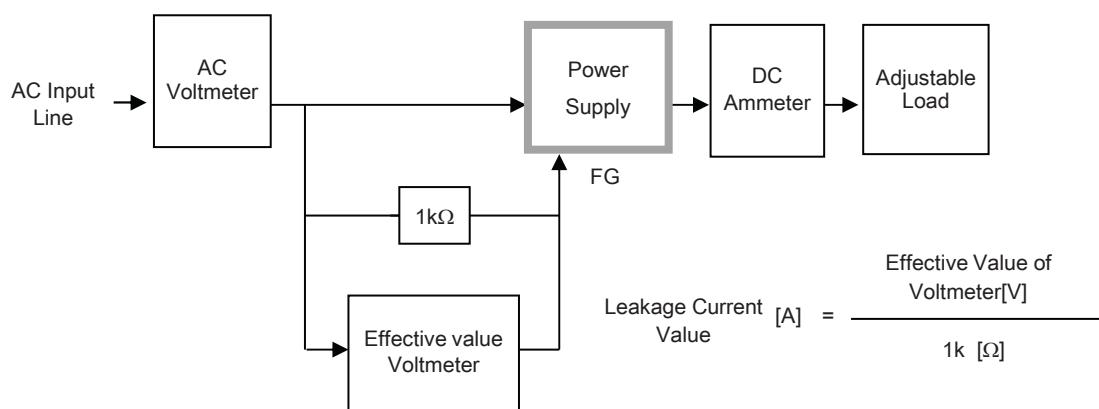


Figure B-1 (DEN-AN)

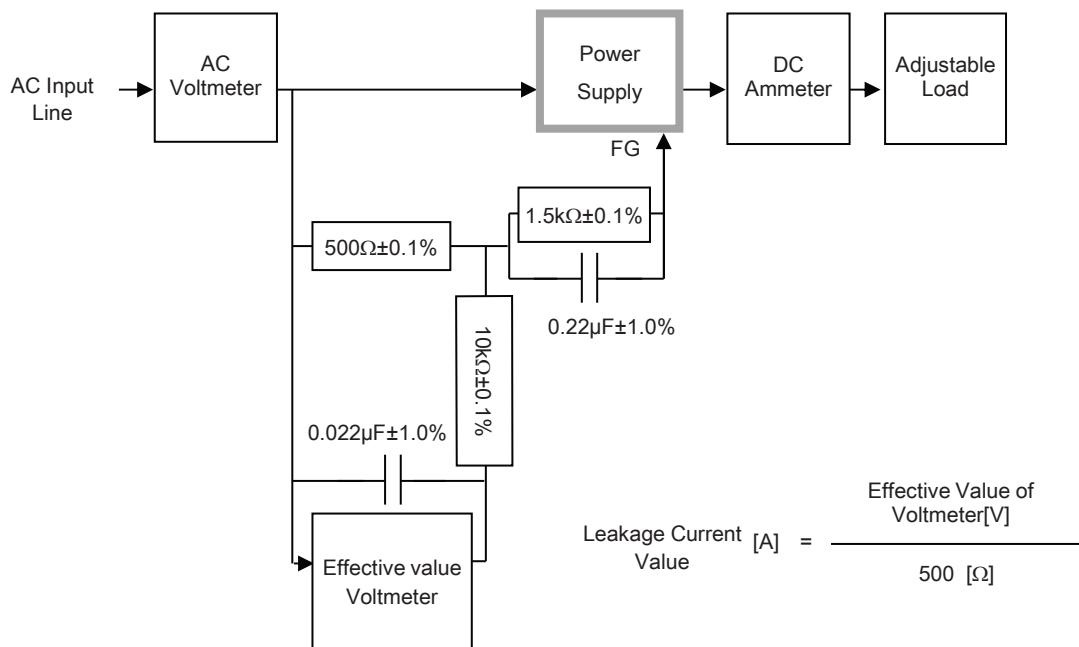


Figure B-2 (IEC62368-1 refer to IEC60990 Fig.4)

COSEL

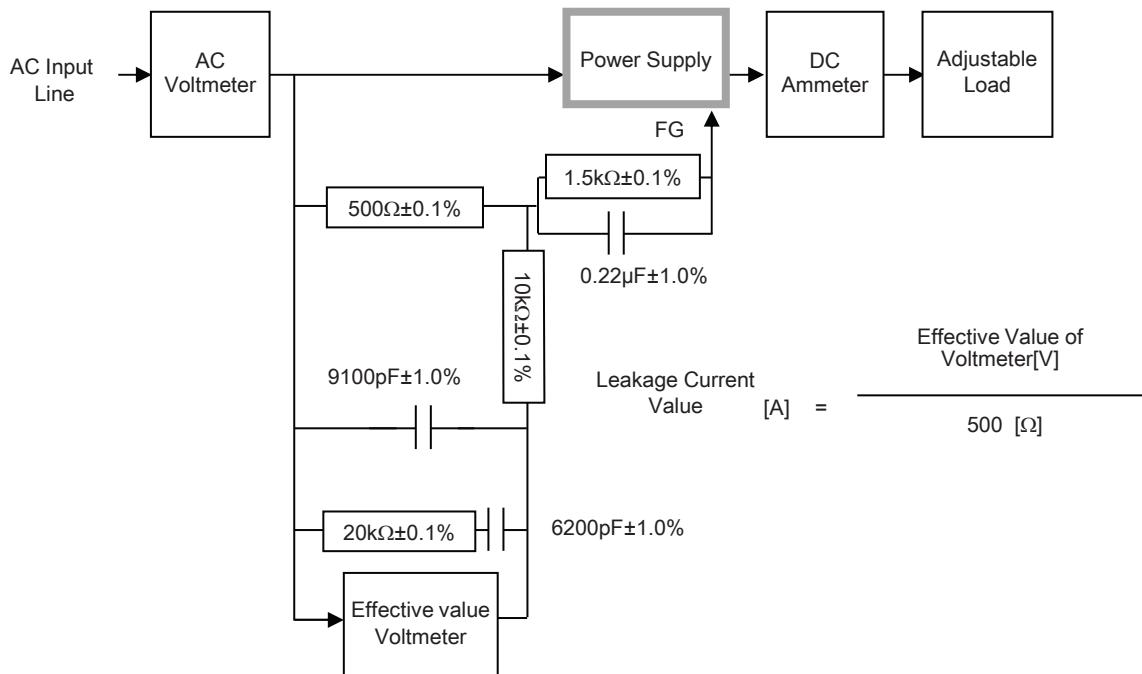


Figure B-3 (IEC62368-1 refer to IEC60990 Fig.5)

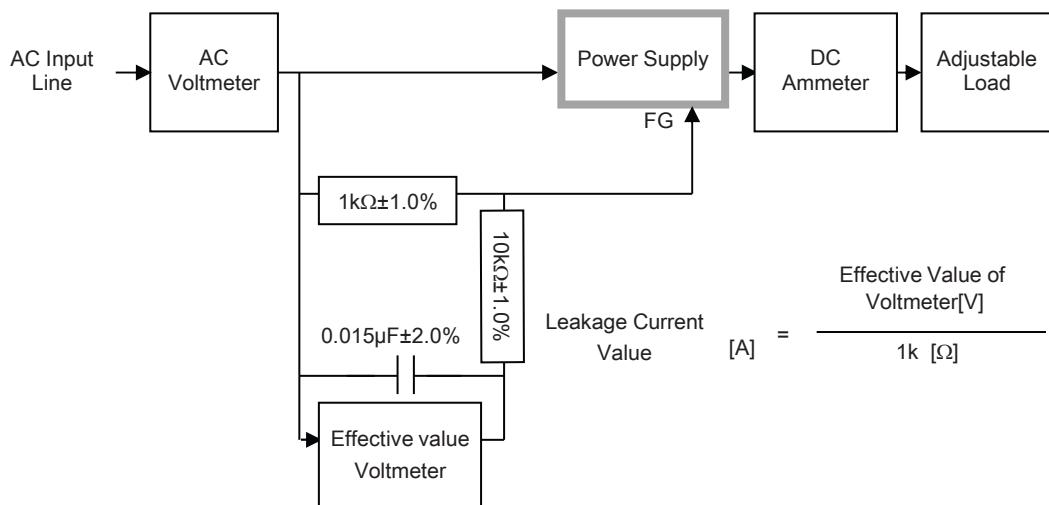


Figure B-4 (IEC60601-1)

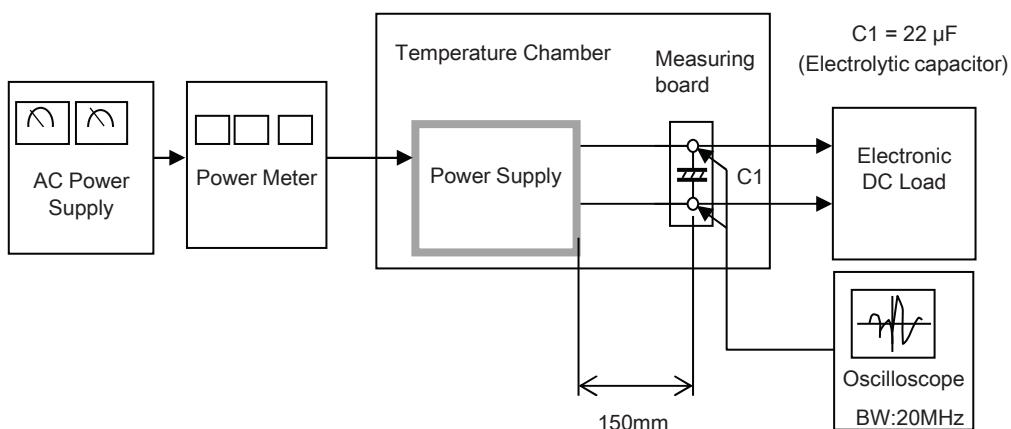


Figure C